RL78/G10

Timer Array Unit (Interval Timer) CC-RL

R01AN3839EJ0100
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Introduction

This application note describes the interval timer function of the timer array unit (TAU). This unit inverts the LED indication each time a timer interrupt occurs. Also, it changes the timer interrupt cycle time based on the number of times the switch is pressed.

Target Device

RL78/G10

When applying the sample program covered in this application note to another microcomputer, modify the program according to the specifications for the target microcomputer and conduct an extensive evaluation of the modified program.

Contents

1. Specifications

This application note shows example settings for using timer interrupts (INTTM00) from the interval timer and interrupts (INTP0) generated on pin input edge detection. The TAU inverts the LED indication each time a timer interrupt (INTTM00) occurs. Also, this unit changes the timer interrupt (INTTM00) cycle time based on the number of times the switch (SW) is pressed.

Table 1.1 lists the peripheral functions to be used and their uses. Figure 1.1 shows the timer and its interrupt operation.

Table 1.1 Peripheral Functions to be Used and Their Uses

Peripheral Function	Use
Timer array unit (channel 0)	Time interval control for inversion of the P10 pin output (LED indication)
P00	Output port for LED indications
P137/INTP0	Switch input for changing the timer interrupt (INTTM00) cycle time

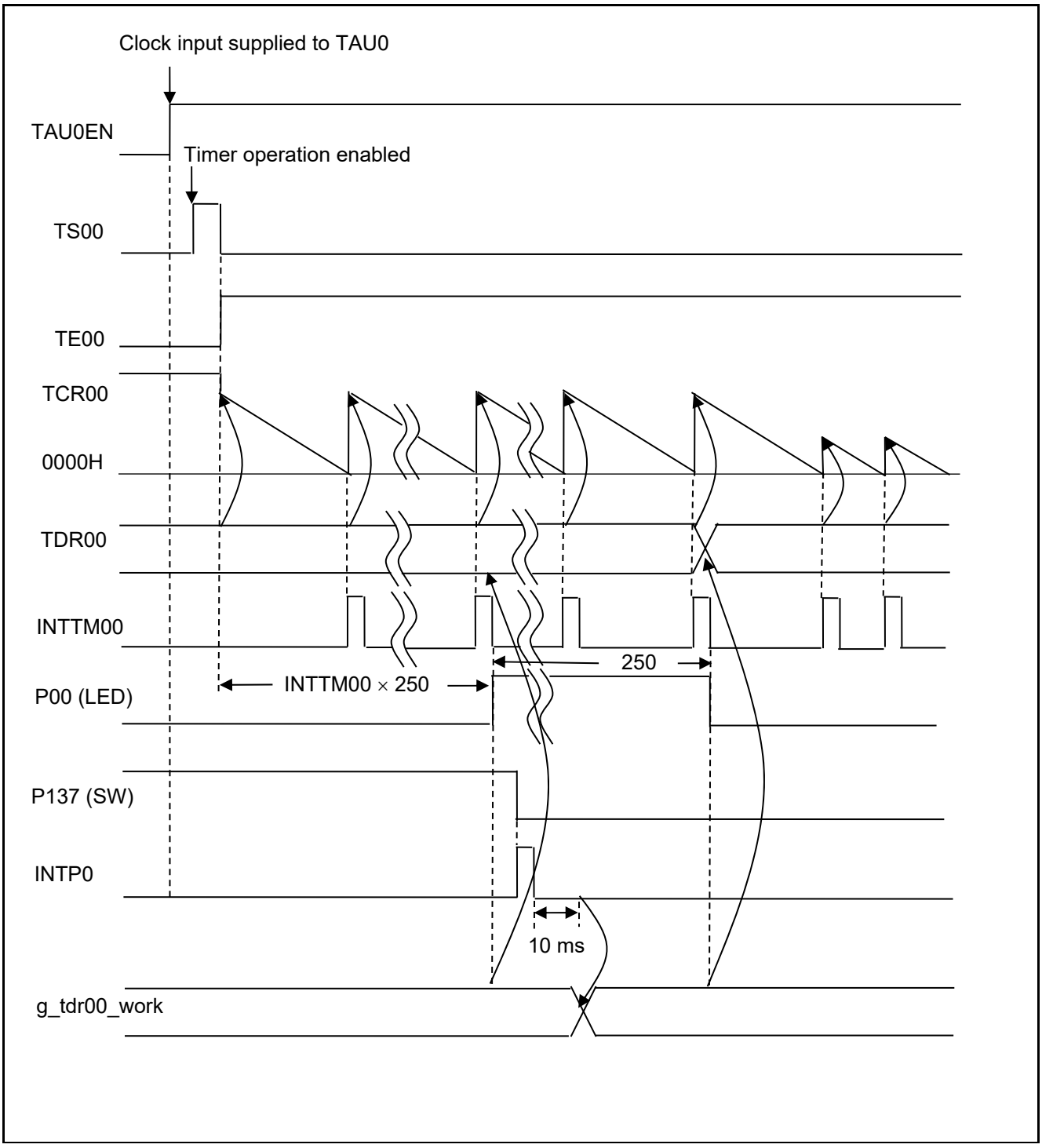


Figure 1.1 Overview of Timer Operation and Interrupts

2. Operation Check Conditions

The sample code contained in this application note has been checked under the conditions listed in the table below.

Table 2.1 Operation Check Conditions

Item	Description
Microcontroller used	RL78/G10 (R5F10Y16ASP)
Operating frequency	• High-speed on-chip oscillator (HOCO) clock: 20 MHz • CPU/peripheral hardware clock: 20 MHz
Operating voltage	5.0V (can run on a voltage range of 2.7 V to 5.5 V.) SPOR detection voltage Falling edge 2.84 V Rising edge 2.90 V
Integrated development environment (CS+)	CS+ for CC V4.01.00 from Renesas Electronics Corp.
C compiler (CS+)	CC-RL V1.03.00 from Renesas Electronics Corp.
Integrated development environment (e^2 studio)	e^2 studio V5.2.0.020 from Renesas Electronics Corp.
C compiler (e^2 studio)	CC-RL V1.03.00 from Renesas Electronics Corp.

3. Related Application Note

The application note that is related to this application note is listed below for reference.

- RL78/G10 Initialization CC-RL (R01AN2668E) Application Note
- RL78/G13 Timer Array Unit (Interval Timer) CC-RL (R01AN2576E) Application Note

4. Description of the Hardware

4.1 Hardware Configuration Example

Figure 4.1 shows an example of hardware configuration that is used for this application note.

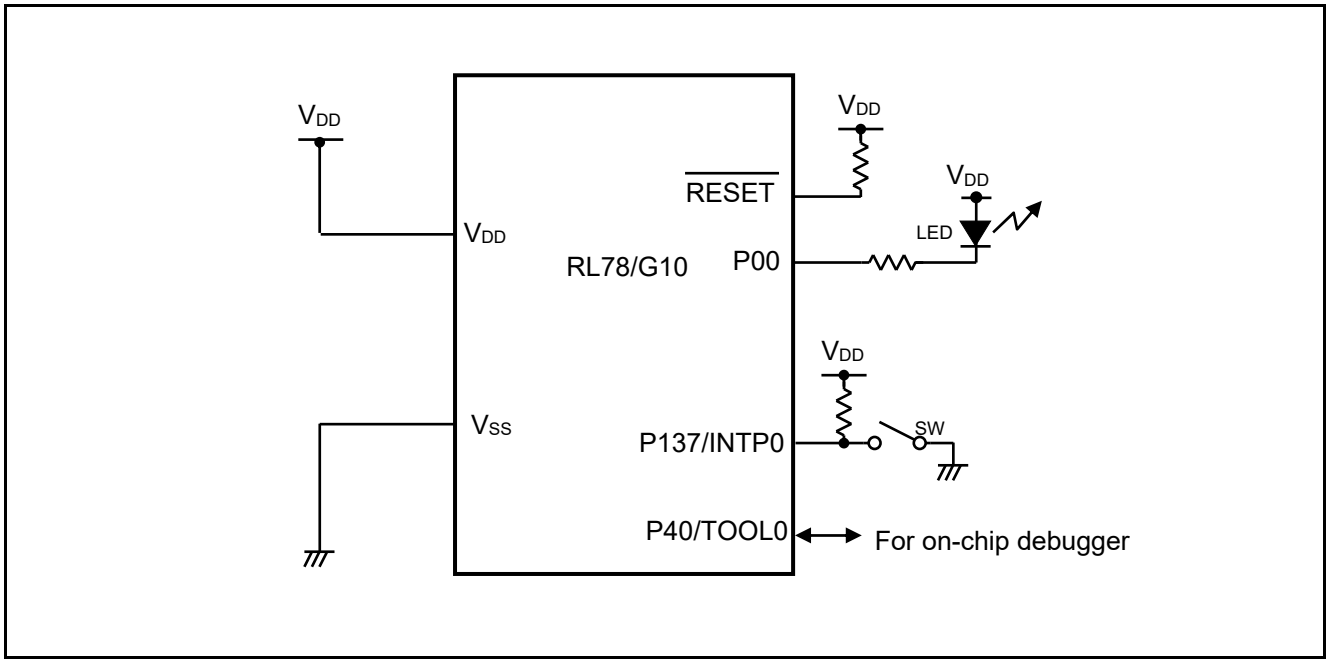


Figure 4.1 Hardware Configuration

Cautions: 1. The purpose of this circuit is only to provide the connection outline and the circuit is simplified accordingly. When designing and implementing an actual circuit, provide proper pin treatment and make sure that the hardware's electrical specifications are met (connect the input-only ports separately to V_{DD} or V_{SS} via a resistor).

2. V_{DD} must be held at not lower than the reset release voltage (V_{SPOR}) that is specified as SPOR.

4.2 List of Pins to be Used

Table 4.1 lists the pins to be used and their functions.

Table 4.1 Pins to be Used and Their Functions

Pin Name	I/O	Description
P00	Output	Output port for LED indications
P137/INTP0	Input	Switch (SW) input pin (external interrupt request input pin)

5. Description of the Software

5.1 Operation Outline

This application note describes how to set up the interval timer function of TAU0.

This setup is followed by operation for counting the number of timer interrupts (INTTM00) generated by the interval timer. Each time the count reaches 250, the LED indication is inverted. The timer interrupt (INTTM00) cycle time is changed according to the number of times the switch is pressed. The LED on/off cycle time is changed as follows.

500 ms → 250 ms → 125 ms → 62.5 ms → 500 ms → …

(1) Initialize the TAU.
- Use the interval timer mode as the timer operation mode.
- Initialize timer data register 00 (TDR00) to 2 ms.
- Set the timer output enable register to disable operation.
- Use timer interrupts (INTTM00) from timer channel 0.

(2) Initialize the external edge detection interrupt.
- Select a falling edge as the valid edge for INTP0.
- Use INTP0 interrupts.

(3) Execute a HALT instruction to wait for timer interrupts (INTTM00).

(4) After the HALT mode is cancelled by a timer interrupt (INTTM00), the number of INTTM00 interrupts generated is counted.

(5) When the timer interrupt count reaches 250, the LED indication is inverted. The value (g_tdr00_work) in RAM for the timer data register is set in the timer data register (TDR00).

(6) INTP0 interrupt processing changes the switch input count (INTP0 interrupt count) and g_tdr00_work value.

5.2 List of Option Byte Settings

Table 5.1 summarizes the settings of the option bytes.

Table 5.1 Option Byte Settings

Address	Value	Description
000C0H	11101110B	Stops the watchdog timer operation. (Stops counting after the release of the reset state.)
000C1H	11110111B	SPOR detection voltage Falling edge 2.84 V Rising edge 2.90 V
000C2H	11111001B	HOCO: 20 MHz
000C3H	10000101B	Enables the on-chip debugging function.

5.3 List of Constants

Table 5.2 lists the constants that are used in this sample program.

Table 5.2 Constants for the Sample Program

Constant	Setting	Description
_01_INTP0_EDGE_FALLING_SEL	01h	Selects a falling edge as the valid edge of INTP0.
g_tdr00_data[]	(64000-1) (32000-1) (16000-1) (8000-1)}	TDR00 settings by number of times the switch is pressed
g_10ms_count[]	(5+1) (10+1) (20+1) (40+1)	10 ms timer count values by number of times the switch is pressed

5.4 List of Variables

Table 5.3 lists the global variable that is used by this sample program.

Table 5.3 Global Variables for the Sample Program

Type	Variable Name	Contents	Function Used
__saddr uint8_t	g_sw_counter	Switch press count	r_intc0_interrupt() main() r_invert_led()
__saddr uint16_t	g_tdr00_work	Value which is set in TDR00 each time the timer interrupt count reaches 250.	r_intc0_interrupt() main() r_invert_led()
__saddr uint8_t	g_inttm00counter	The number of timer interrupt generation	main() r_invert_led()

5.5 List of Functions

Table 5.4 lists the functions that are used by this sample program.

Table 5.4 Functions

Function Name	Outline
R_TAU0_Channel0_Start	Starts operation of TAU0 channel 0.
R_TAU0_Channel0_Interrupt()	Processes timer interrupts on TAU0 channel 0.
r_invert_led()	Counts the number of INTTM00 interrupts generated. Inverts the LED indication each time the interrupt count reaches 250.
R_INTC0_Start	Enables INTP0 interrupts.
r_intc0_interrupt	Processes INTP0 interrupts.

5.6 Function Specifications

This section describes the specifications for the functions that are used in the sample code.

[Function Name] R_TAU0_Channel0_Start	
Synopsis	TAU0 channel 0 operation start
Header	r_cg_macrodriver.h r_cg_tau.h r_cg_userdefine.h
Declaration	void R_TAU0_Channel0_Start(void)
Explanation	This function unmasks TAU0 channel 0 interrupts and starts count operation.
Arguments	None
Return value	None
Remarks	None

[Function Name] r_tau0_channel0_interrupt()	
Synopsis	TAU0 channel 0 timer interrupt processing
Header	r_cg_macrodriver.h r_cg_tau.h r_cg_userdefine.h
Declaration	static void __near r_tau0_channel0_interrupt(void)
Explanation	This function calls the function which will invert the LED indication.
Arguments	None
Return value	None
Remarks	None

[Function Name] r_invert_led()	
Synopsis	LED indication inversion processing
Header	r_cg_macrodriver.h r_cg_cgc.h r_cg_port.h r_cg_intp.h r_cg_tau.h r_cg_userdefine.h
Declaration	void r_invert_led(void)
Explanation	This function counts 250 timer interrupts (INTTM00) and then inverts the LED indication (for port latch inversion). It also changes the TDR00 setting to the value specified with g_tdr00_work.
Arguments	None
Return value	None
Remarks	None

[Function Name] R_INTC0_Start	
Synopsis	INTP0 interrupt enable
Header	r_cg_intp.h
Declaration	void R_INTC0_Start(void)
Explanation	This function clears the interrupt request flag. It enables INTP0 interrupts and starts taking in the switch input.
Arguments	None
Return value	None
Remarks	None

[Function Name] r_intc0_interrupt()	
Synopsis	INTP0 interrupt processing
Header	r_cg_macrodriver.h r_cg_intp.h r_cg_userdefine.h
Declaration	static void __near r_intc0_interrupt(void)
Explanation	This function processes INTP0 interrupts as they occur. It waits 10 ms and then scans P13.7 (SW input pin). When the switch is pressed, this function changes the g_tdr00_work value.
Arguments	None
Return value	None
Remarks	None

5.7 Flowcharts

Figure 5.1 shows the overall flow of the sample program described in this application note.

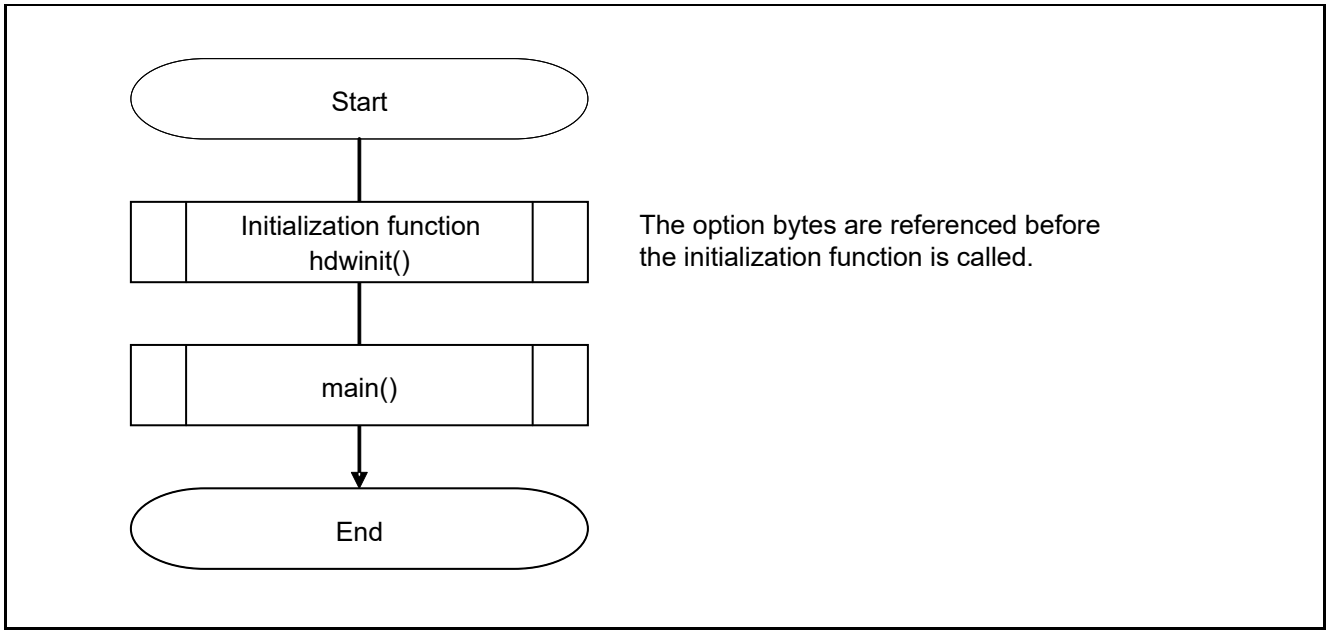


Figure 5.1 Overall Flow

Note: Startup routine is executed before and after the initialization function.

5.7.1 Initialization Function

Figure 5.2 shows the flowchart for the initialization function.

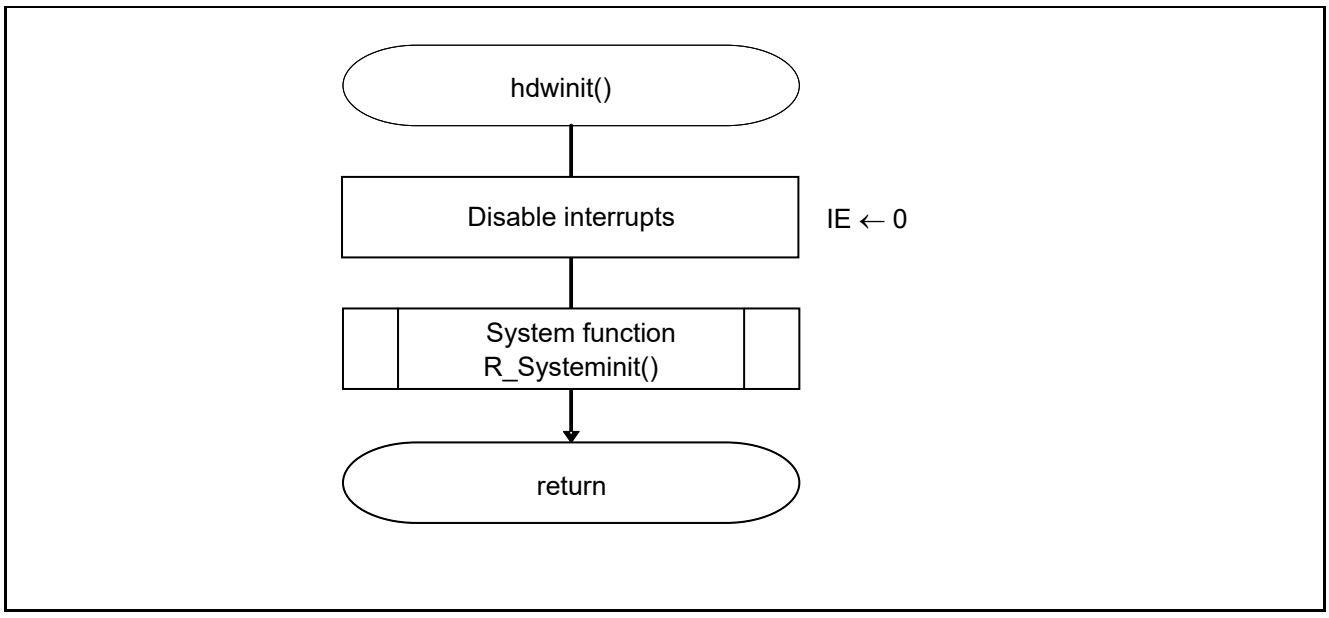


Figure 5.2 Initialization Function

5.7.2 System Function

Table 5.3 shows the flowchart for the system function.

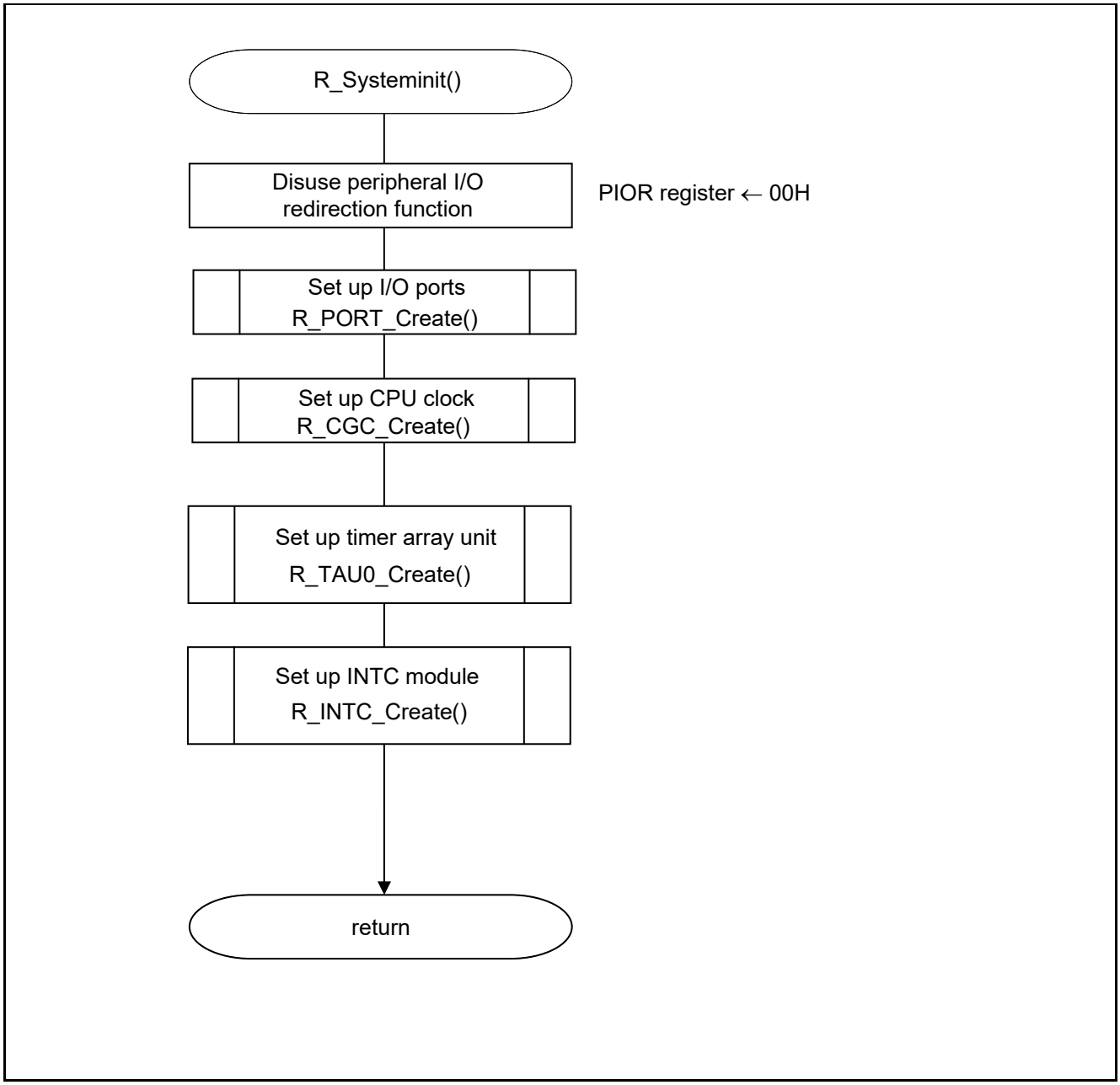


Figure 5.3 System Function

5.7.3 I/O Port Setup

Table 5.4 shows the flowchart for I/O port setup.

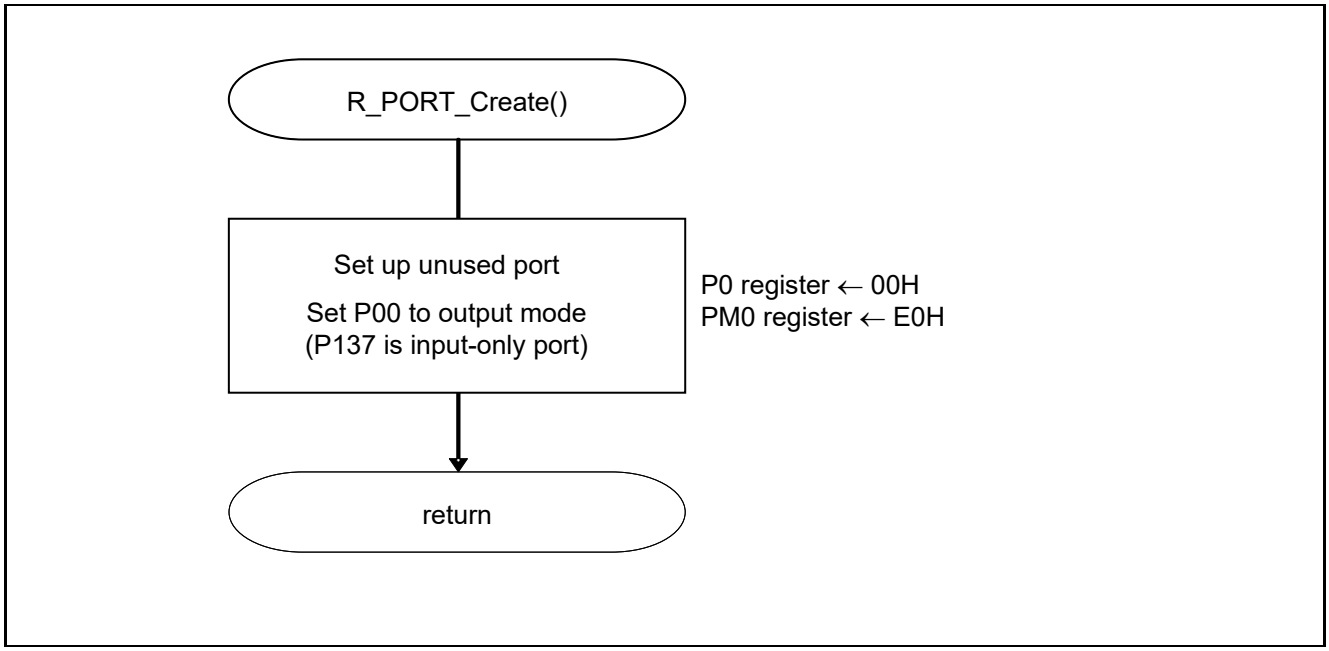


Figure 5.4 I/O Port Setup

Note: Refer to RL78/G10 User's Manual: Hardware for the configuration of the unused ports.

Caution: Provide proper treatment for unused pins so that their electrical specifications are observed. Connect each of any unused input-only ports to V_{DD} or V_{SS} via a separate resistor.

Setting up the LED port

- Port mode register0 (PM0)
  Select I/O mode for the port.

Symbol: PM0

7	6	5	4	3	2	1	0
1	1	1	PM04	PM03	PM02	PM01	PM00
1	1	1	x	x	x	x	**0**

Bit 0

PM00	P00 pin I/O mode selection
0	**Output mode (output buffer on)**
1	Input mode (output buffer off)

Caution: For details on the register setup procedures, refer to RL78/G10 User's Manual: Hardware.

5.7.4 CPU Clock Setup

Figure 5.5 shows the flowchart for setting up the CPU clock.

This setup is only for 16-pin products because 10-pin products do not have the resonator connection pins for the main system clock (X1 and X2) and the external clock input pin (EXCLK). Select only the high-speed on-chip oscillator frequency in 10-pin products.

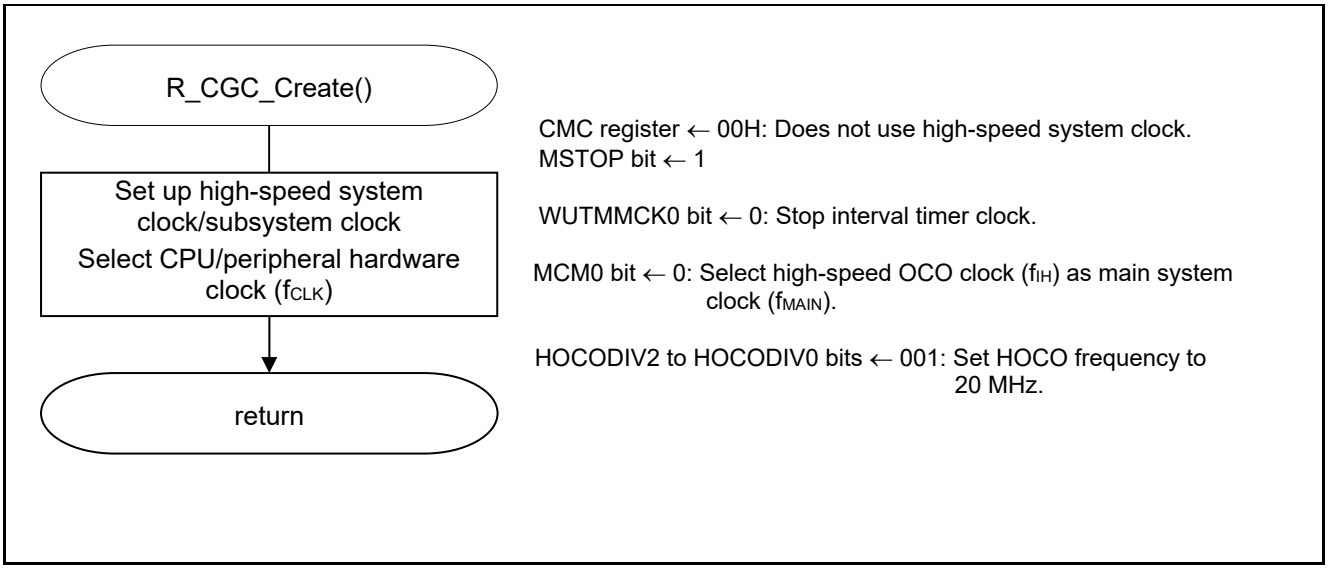


Figure 5.5 CPU Clock Setup

Caution: For details on the procedure for setting up the CPU clock (R_CGC_Create ()), refer to RL78/G10 User's Manual: Hardware.

5.7.5 Timer Array Unit Setup

Figure 5.6 shows the flowchart for setting up the timer array unit.

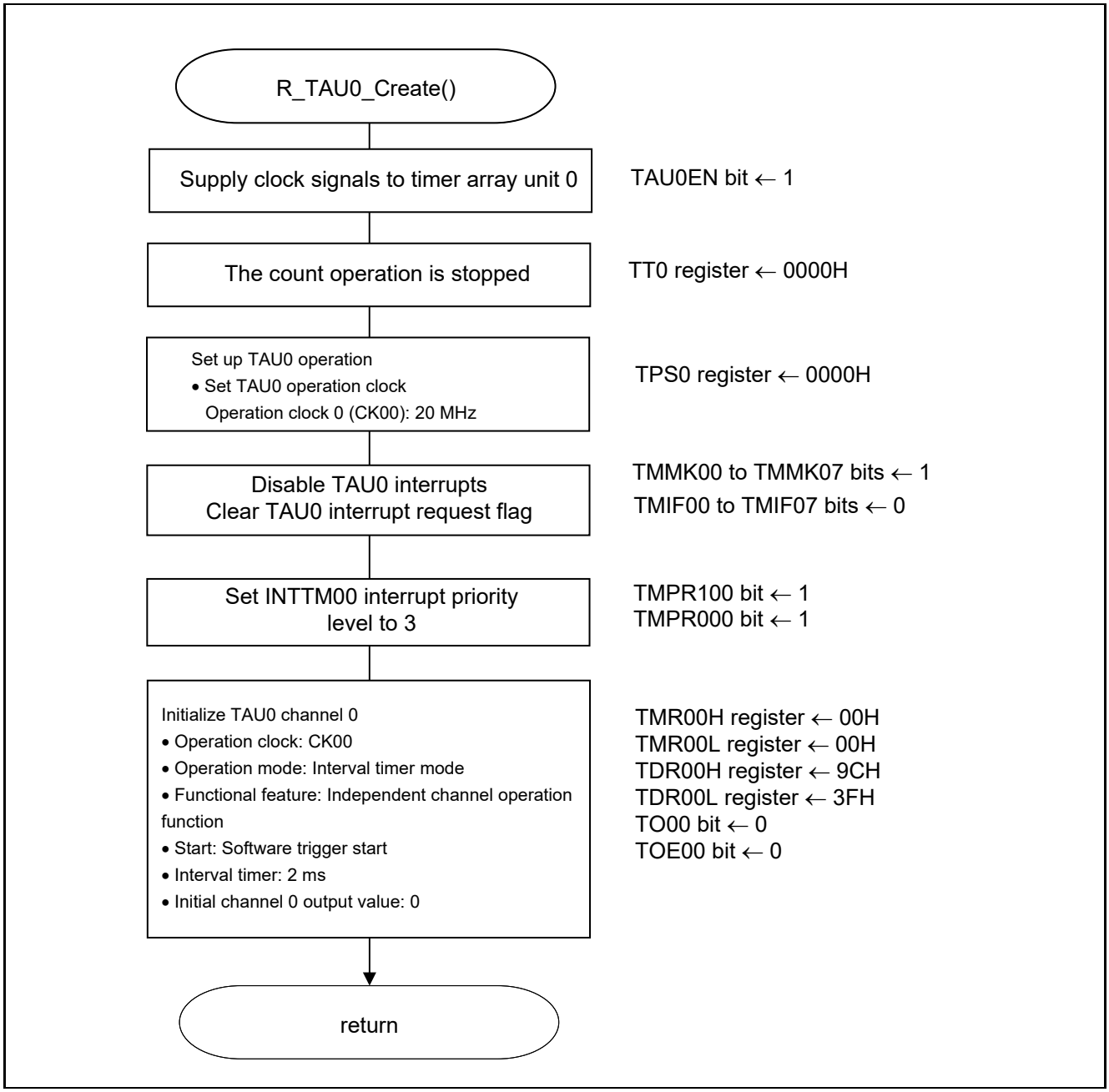


Figure 5.6 Timer Array Unit Setup

Starting clock signal supply to the timer array unit 0

- Peripheral enable register 0 (PER0)
  Start supplying clock signals to the timer array unit 0.

Symbol: PER0

7	6	5	4	3	2	1	0
TMKAEN	CMPEN	ADCEN	IICA0EN	0	SAU0EN	0	TAU0EN
x	x	x	x	0	x	0	**1**

Bit 0

TAU0EN	Control of timer array unit 0 input clock supply
0	Stops input clock supply.
1	**Enables input clock supply.**

Caution: For details on the register setup procedures, refer to RL78/G10 User's Manual: Hardware.

Configuring the timer clock frequency

- Timer clock select register 0 (TPS0)
  Select an operation clock for timer array unit 0.

Symbol: TPS0

7	6	5	4	3	2	1	0
PRS013	PRS012	PRS011	PRS010	PRS003	PRS002	PRS001	PRS000
x	x	x	x	**0**	**0**	**0**	**0**

Bits 3 to 0

PRS 003	PRS 002	PRS 001	PRS 000	Operation clock (CK00) selection					
					f_{CLK}= 1.25 MHz	f_{CLK}= 2.5 MHz	f_{CLK}= 5 MHz	f_{CLK}= 10 MHz	**f_{CLK}= 20 MHz**
0	**0**	**0**	**0**	**f_{CLK}**	1.25 MHz	2.5 MHz	5 MHz	10 MHz	**20 MHz**
0	0	0	1	$f_{CLK}/2$	650 kHz	1.25 MHz	2.5 MHz	5 MHz	10 MHz
0	0	1	0	$f_{CLK}/2^2$	313 kHz	650 kHz	1.25 MHz	2.5 MHz	5 MHz
0	0	1	1	$f_{CLK}/2^3$	156 kHz	313 kHz	650 kHz	1.25 MHz	2.5 MHz
0	1	0	0	$f_{CLK}/2^4$	78.1 kHz	156 kHz	313 kHz	650 kHz	1.25 MHz
0	1	0	1	$f_{CLK}/2^5$	39.1 kHz	78.1 kHz	156 kHz	313 kHz	650 kHz
0	1	1	0	$f_{CLK}/2^6$	19.5 kHz	39.1 kHz	78.1 kHz	156 kHz	313 kHz
0	1	1	1	$f_{CLK}/2^7$	9.77 kHz	19.5 kHz	39.1 kHz	78.1 kHz	156 kHz
1	0	0	0	$f_{CLK}/2^8$	4.88 kHz	9.77 kHz	19.5 kHz	39.1 kHz	78.1 kHz
1	0	0	1	$f_{CLK}/2^9$	2.44 kHz	4.88 kHz	9.77 kHz	19.5 kHz	39.1 kHz
1	0	1	0	$f_{CLK}/2^{10}$	1.22 kHz	2.44 kHz	4.88 kHz	9.77 kHz	19.5 kHz
1	0	1	1	$f_{CLK}/2^{11}$	610 Hz	1.22 kHz	2.44 kHz	4.88 kHz	9.77 kHz
1	1	0	0	$f_{CLK}/2^{12}$	305 Hz	610 Hz	1.22 kHz	2.44 kHz	4.88 kHz
1	1	0	1	$f_{CLK}/2^{13}$	153 Hz	305 Hz	610 Hz	1.22 kHz	2.44 kHz
1	1	1	0	$f_{CLK}/2^{14}$	76.3 Hz	153 Hz	305 Hz	610 Hz	1.22 kHz
1	1	1	1	$f_{CLK}/2^{15}$	38.1 Hz	76.3 Hz	153 Hz	305 Hz	610 Hz

Caution: For details on the register setup procedures, refer to RL78/G10 User's Manual: Hardware.

Setting up channel 0 operation mode

- Timer mode register 00 (TMR00L, TMR00H)
  Select an operation clock (f_{MCK}).
  Select a count clock.
  Select the software trigger start.
  Set up the operation mode.

Symbol: TMR00H

7	6	5	4	3	2	1	0
CKS001	0	0	CCS00	0	STS002	STS001	STS000
0	0	0	**0**	0	**0**	**0**	**0**

Bits 7

CKS001	Channel 0 operation clock (f_{MCK}) selection
0	**Operation clock CK00 set by timer clock select register 0 (TPS0)**
1	**Operation clock CK01 set by timer clock select register 0 (TPS0)**

Bit 4

CCS00	Channel 0 count clock (f_{TCLK}) selection
0	**Operation clock (f_{MCK}) specified by the CKS000 and CKS001 bits**
1	Valid edge of input signal input from the TI00 pin

Caution: For details on the register setup procedures, refer to RL78/G10 User's Manual: Hardware.

Symbol: TMR00L

7	6	5	4	3	2	1	0
CKS001	CIS000	0	0	MD003	MD002	MD001	MD000
0	**0**	0	0	**0**	**0**	**0**	**0**

Bits 7 to 6

CIS001	CIS000	Selection of TI00 pin input valid edge
0	**0**	**Falling edge**
0	1	Rising edge
1	0	Both edges (when low-level width is measured) Start trigger: Falling edge, Capture trigger: Rising edge
1	1	Both edges (when high-level width is measured) Start trigger: Rising edge, Capture trigger: Falling edge

Caution: For details on the register setup procedures, refer to RL78/G10 User's Manual: Hardware.

Symbol: TMR00L

7	6	5	4	3	2	1	0
CKS001	CIS000	0	0	MD003	MD002	MD001	MD000
0	**0**	0	0	**0**	**0**	**0**	**0**

Bits 3 to 0

MD003	MD002	MD001	Operation mode of channel 0	Related function	TCR counting operation
0	**0**	**0**	**Interval timer mode**	**Interval timer / Square wave output / Divider function / PWM output (master)**	**Counting down**
0	1	0	Capture mode	Input pulse interval measurement	Counting up
0	1	1	Event counter mode	External event counter	Counting down
1	0	0	One-count mode	Delay counter / One-shot pulse output / PWM output (slave)	Counting down
1	1	0	Capture & one-count mode	Measurement of high-/low-level width of input signal	Counting up
Other than above			Setting prohibited		

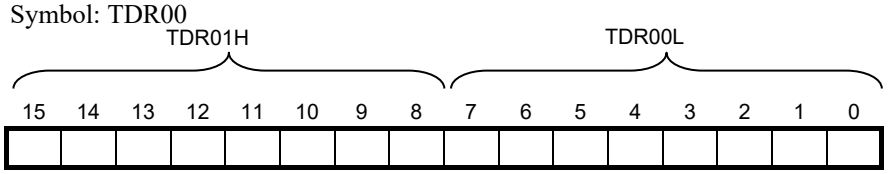
Operation mode (selected with MD003 to MD001) (See the table above)	MD000	TCR counting operation
• Interval timer mode (0, 0, 0) **• Capture mode (0, 1, 0)**	**0**	**Timer interrupt is not generated when counting is started (timer output does not change, either).**
	1	Timer interrupt is generated when counting is started (timer output also changes).
• Event counter mode (0, 1, 1)	0	Timer interrupt is not generated when counting is started (timer output does not change, either).
• One-count mode (1, 0, 0)	0	Start trigger is invalid during counting operation. At that time, interrupt is not generated, either.
	1	Start trigger is valid during counting operation. At that time, interrupt is also generated.
• Capture/one-count mode (1, 1, 0)	0	Timer interrupt is not generated when counting is started (timer output does not change, either). Start trigger is invalid during counting operation. At that time, interrupt is not generated, either.
Other than above		Setting prohibited

Caution: For details on the register setup procedures, refer to RL78/G10 User's Manual: Hardware.

Configuring the interval timer cycle time

- Timer data register 00 (TDR00L, TDR00H)
  Configure the interval timer compare value.

Symbol: TDR00



Timer interrupt (INTTM00) occurrence = (TDR00 setting + 1) × Count clock cycle time

Enabling the timer output

- Timer output enable register 0 (TOE0)
  Enable/disable the timer output for each channel.

Symbol: TOE0

7	6	5	4	3	2	1	0
0	0	0	0	TOE03	TOE02	TOE01	TOE00
0	0	0	0	x	x	x	**0**

Bit 0

TOE00	Timer output enable/disable of channel 0
0	**Timer output is disabled.** **Timer operation is not applied to the TO00 bit and the output is fixed.** **Writing to the TO00 bit is enabled and the level set in the TO00 bit is output from the TO00 pin.**
1	Timer output is enabled. Timer operation is applied to the TO00 bit and an output waveform is generated. Writing to the TO00 bit is ignored.

Caution: For details on the register setup procedures, refer to RL78/G10 User's Manual: Hardware.

5.7.6 INTP0 Initialization

Figure 5.7 shows the flowchart for INTP0 initialization.

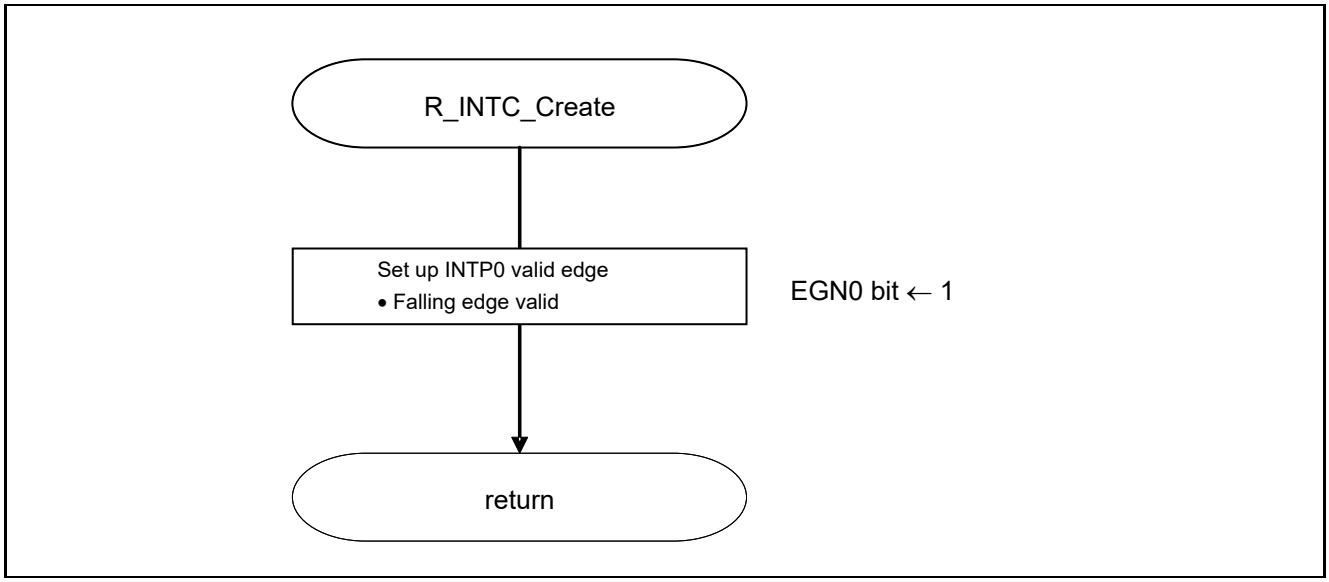


Figure 5.7 INTC Module Setup

Setup for INTP0 pin edge detection

- External interrupt rising edge enable register (EGP0)
- External interrupt falling edge enable register (EGN0)
  Select a valid edge for INTP0

Symbol: EGP0

7	6	5	4	3	2	1	0
0	0	0	0	EGP3	EGP2	EGP1	EGP0
0	0	0	0	x	x	x	**0**

Symbol: EGN0

7	6	5	4	3	2	1	0
0	0	0	0	EGN3	EGN2	EGN1	EGN0
0	0	0	0	x	x	x	**1**

EGP0	EGN0	INTP0 pin valid edge selection
0	0	Edge detection disabled.
0	**1**	**Falling edge**
1	0	Rising edge
1	1	Both rising and falling edges

Caution: For details on the register setup procedures, refer to RL78/G10 User's Manual: Hardware.

5.7.7 Main Processing

Figure 5.8 shows the flowchart for main processing.

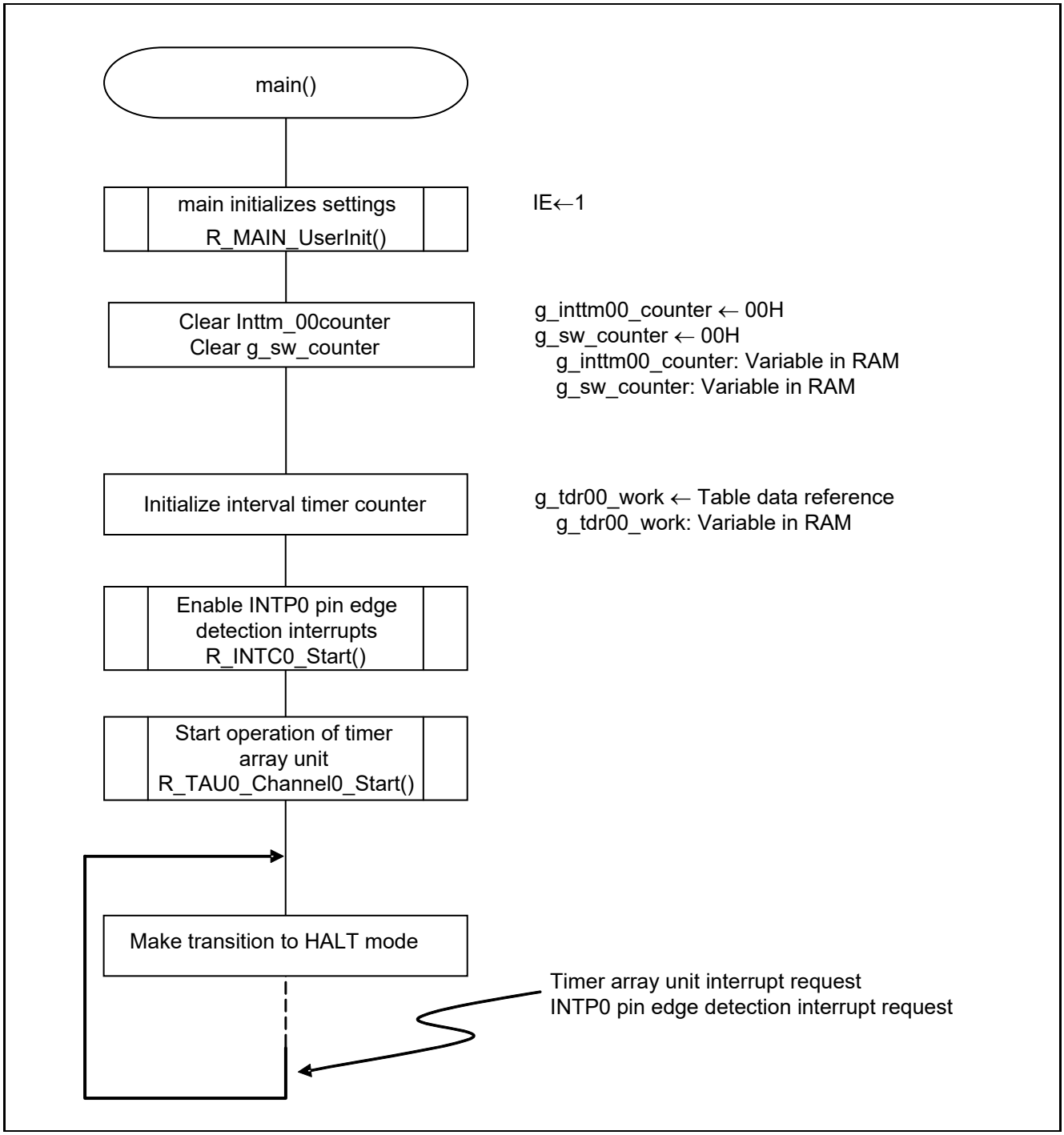


Figure 5.8 Main Processing

5.7.8 Main initializes settings

Figure 5.10 shows the flowchart for the main initializes settings.

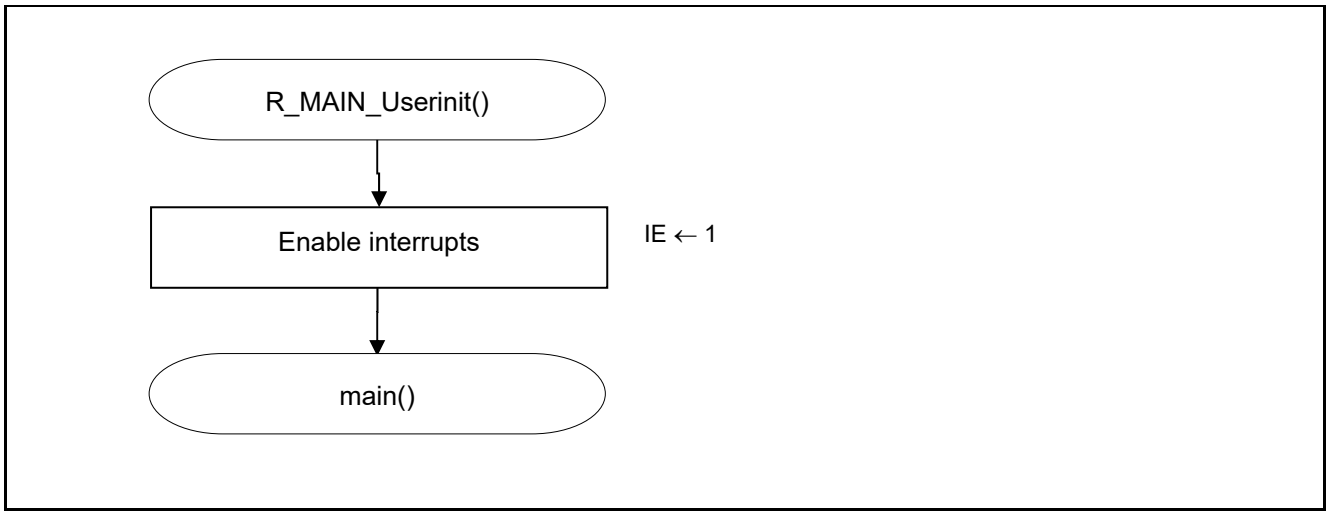


Figure 5.9 Main initializes settings

5.7.9 INTP0 Operation Start

Figure 5.10 shows the flowchart for starting INTP0 operation.

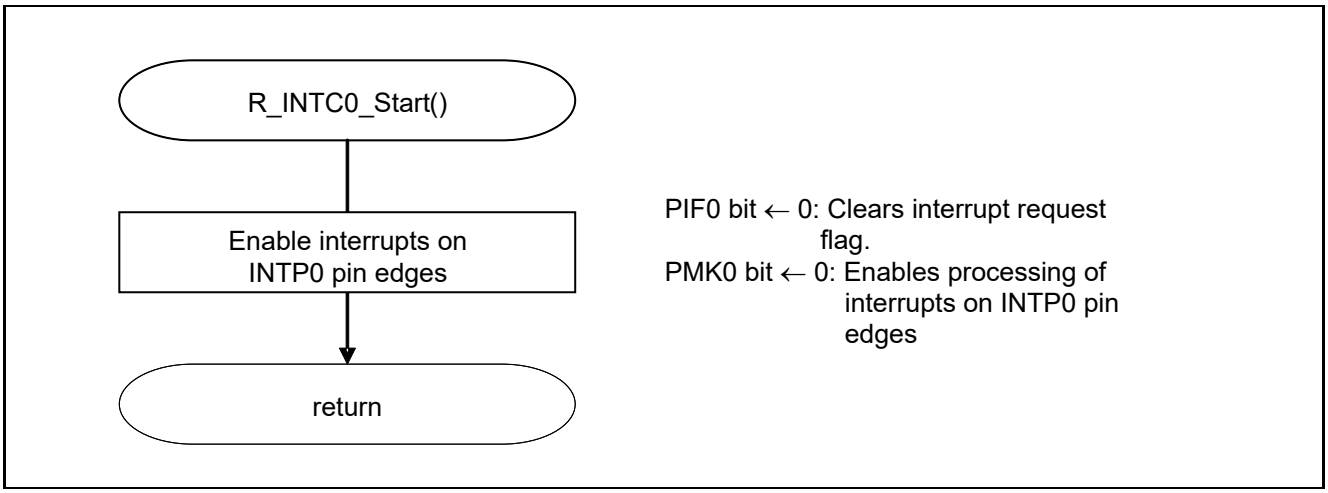


Figure 5.10 INTP0 Operation Start

Setup for INTP0 Interrupts

- Interrupt request flag register (IF0L)
  Clear interrupt request flag.
- Interrupt mask flag register (MK0L)
  Clear interrupt mask.

Symbol: IF0L

7	6	5	4	3	2	1	0
TMIF00	TMIF01H	SREIF0	SRIF0	STIF0 CSIIF00 IICIF00	PIF1	PIF0	WDTIIF
x	x	x	x	x	x	**0**	x

Bit 1

PIF0	Interrupt request flag
0	**No interrupt request signal is generated**
1	Interrupt request is generated, interrupt request status

Symbol: MK0L

7	6	5	4	3	2	1	0
TMMK00	TMMK01H	SREMK0	SRMK0	STMK0 CDIMK00 IICMK00	PMK1	PMK0	WDTIMK
x	x	x	x	x	x	**0**	x

Bit 2

PMK0	Interrupt processing control
0	**Enables interrupt processing.**
1	Disables interrupt processing.

Caution: For detailed information about setting the registers, see RL78/G10 User's Manual: Hardware.

5.7.10 Timer Array Unit 0 Operation Start

Figure 5.9 shows the flowchart for starting timer array unit operation.

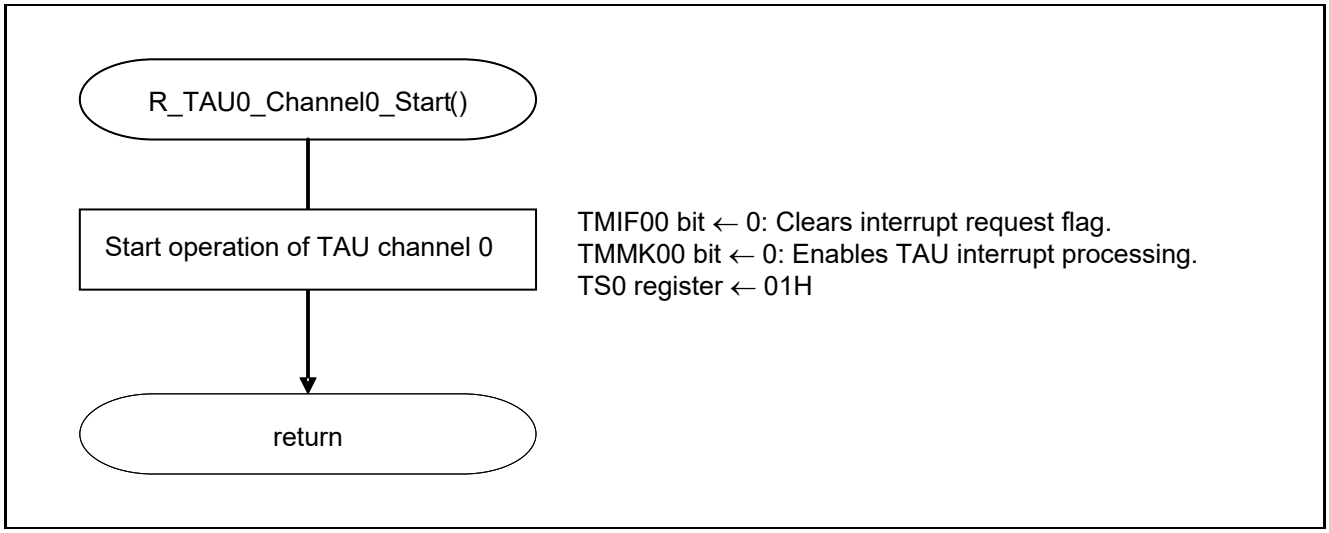


Figure 5.9 Timer Array Unit 0 Operation Start

Configuring the timer interrupt

- Interrupt request flag register (IF0L)
  Clear the interrupt request flag.
- Interrupt mask flag register (MK0L)
  Enable interrupt processing.

Symbol: IF0H

7	6	5	4	3	2	1	0
TMIF00	TMIF00H	SREIF0	SRIF0	STIF0 CSIIF00 IICIF00	PIF1	PIF0	WDTIF
0	x	x	x	x	x	x	x

Bit 7

TMIF00	Interrupt request flag
0	**No interrupt request signal is generated.**
1	Interrupt request is generated, interrupt request status

Symbol: MK0H

7	6	5	4	3	2	1	0
TMMK00	TMMK00H	SREMK0	SRMK0	STMK0 CSIMK00 IICMK00	PMK1	PMK0	WDTIMK
0	x	x	x	x	x	x	x

Bit7

TMMK00	Interrupt processing control
0	**Enables interrupt processing.**
1	Disables interrupt processing.

Caution: For details on the register setup procedures, refer to RL78/G10 User's Manual: Hardware.

5.7.11 INTTM00 Interrupt Processing

Figure 5.10 shows the flowchart for INTTM00 interrupt processing.

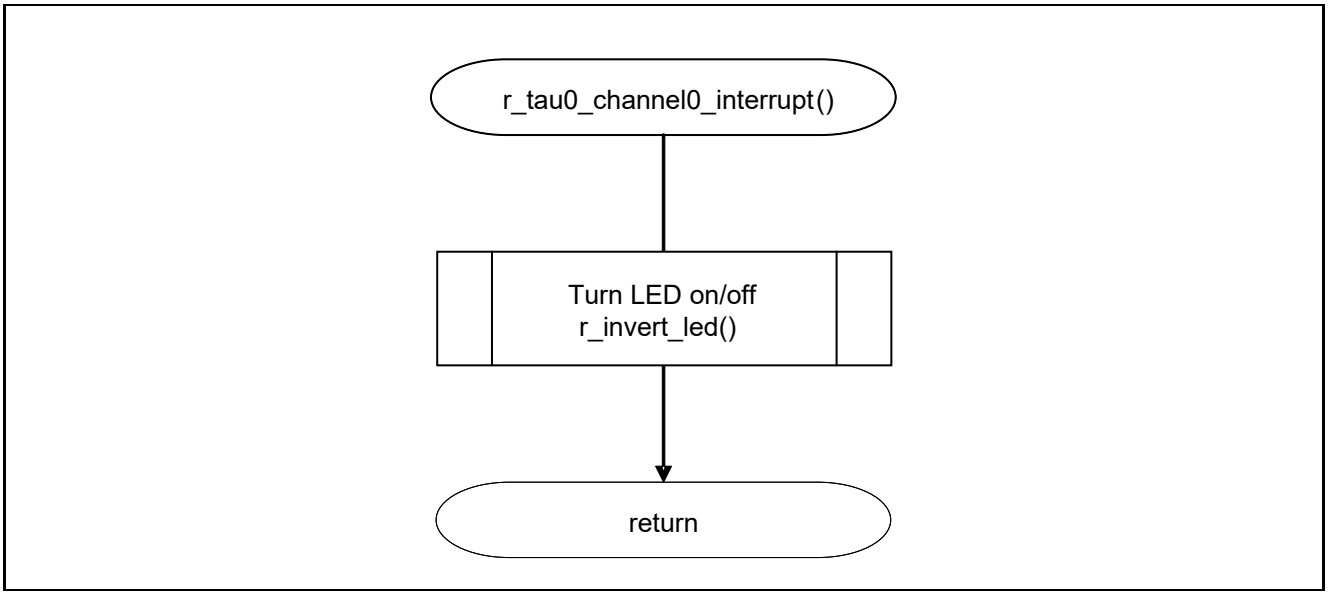


Figure 5.10 INTTM00 Interrupt Processing

5.7.12 LED Turn-On/Off Processing

Figure 5.113 shows the flowchart for LED turn-on/off processing.

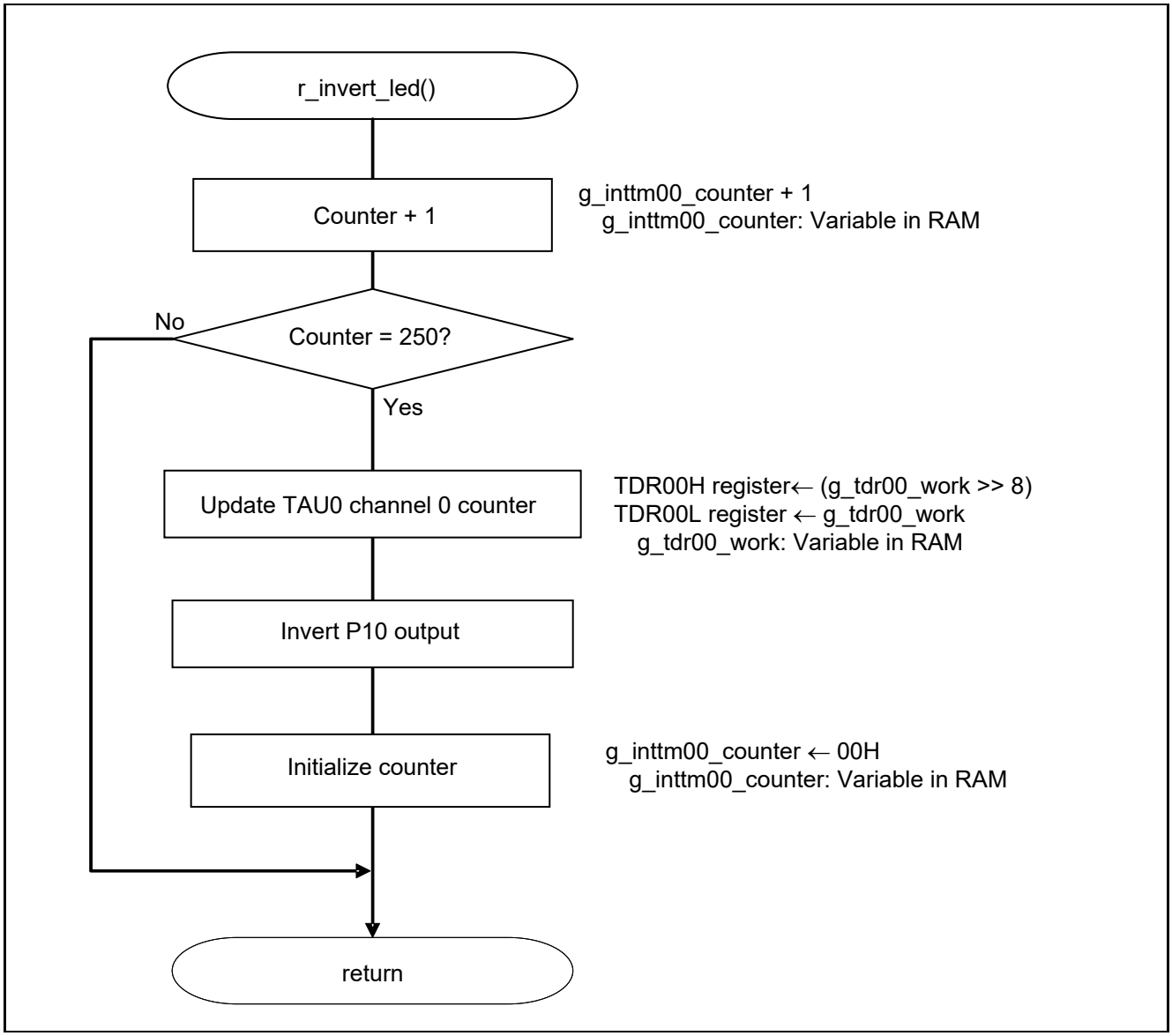


Figure 5.113 Checking Whether 500 ms Have Elapsed

5.7.13 INTP0 Interrupt Processing

Figures 5.14 and 5.15 show the flowchart for INTP0 interrupt processing.

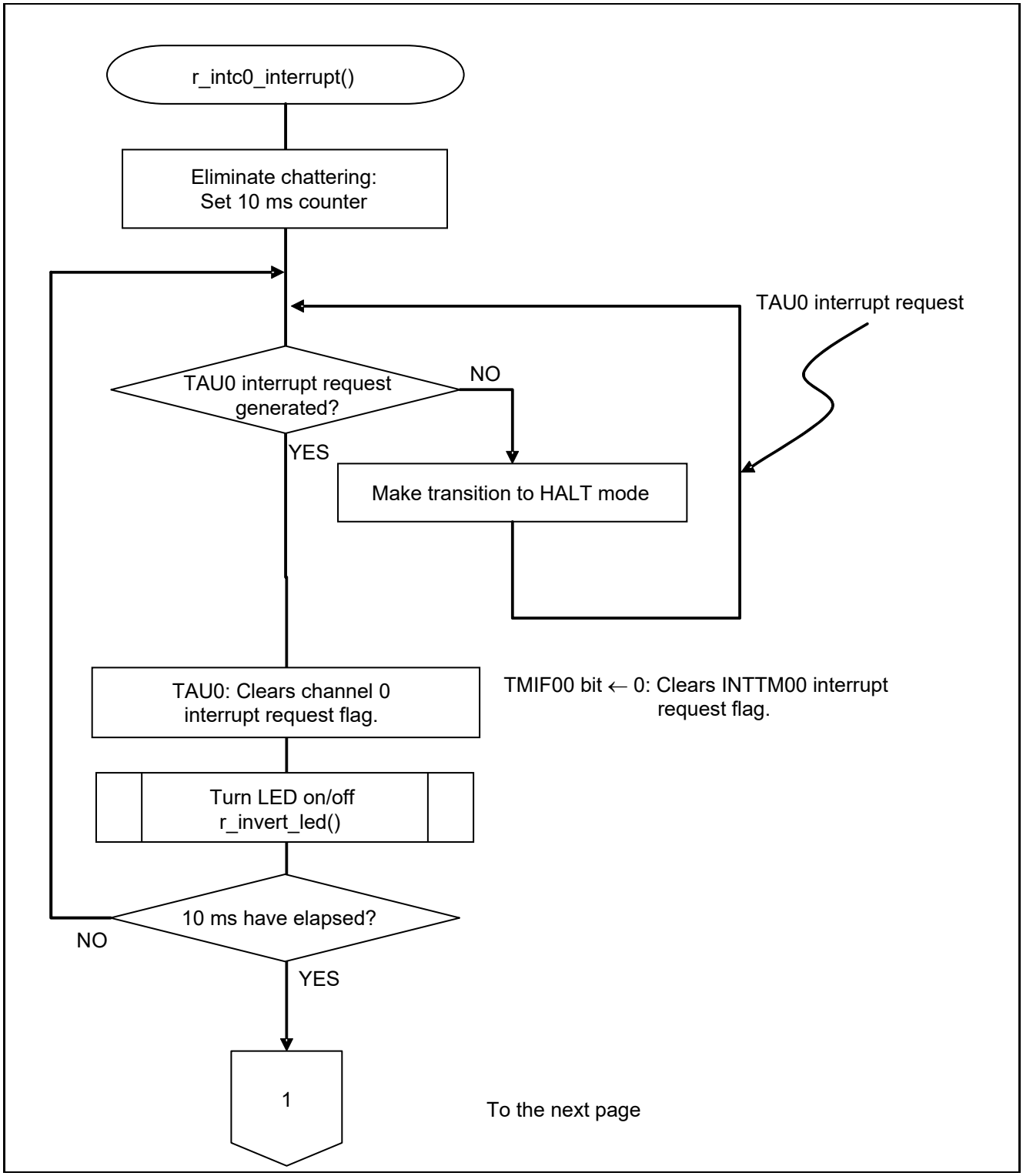


Figure 5.12 INTP0 Interrupt Processing (1/2)

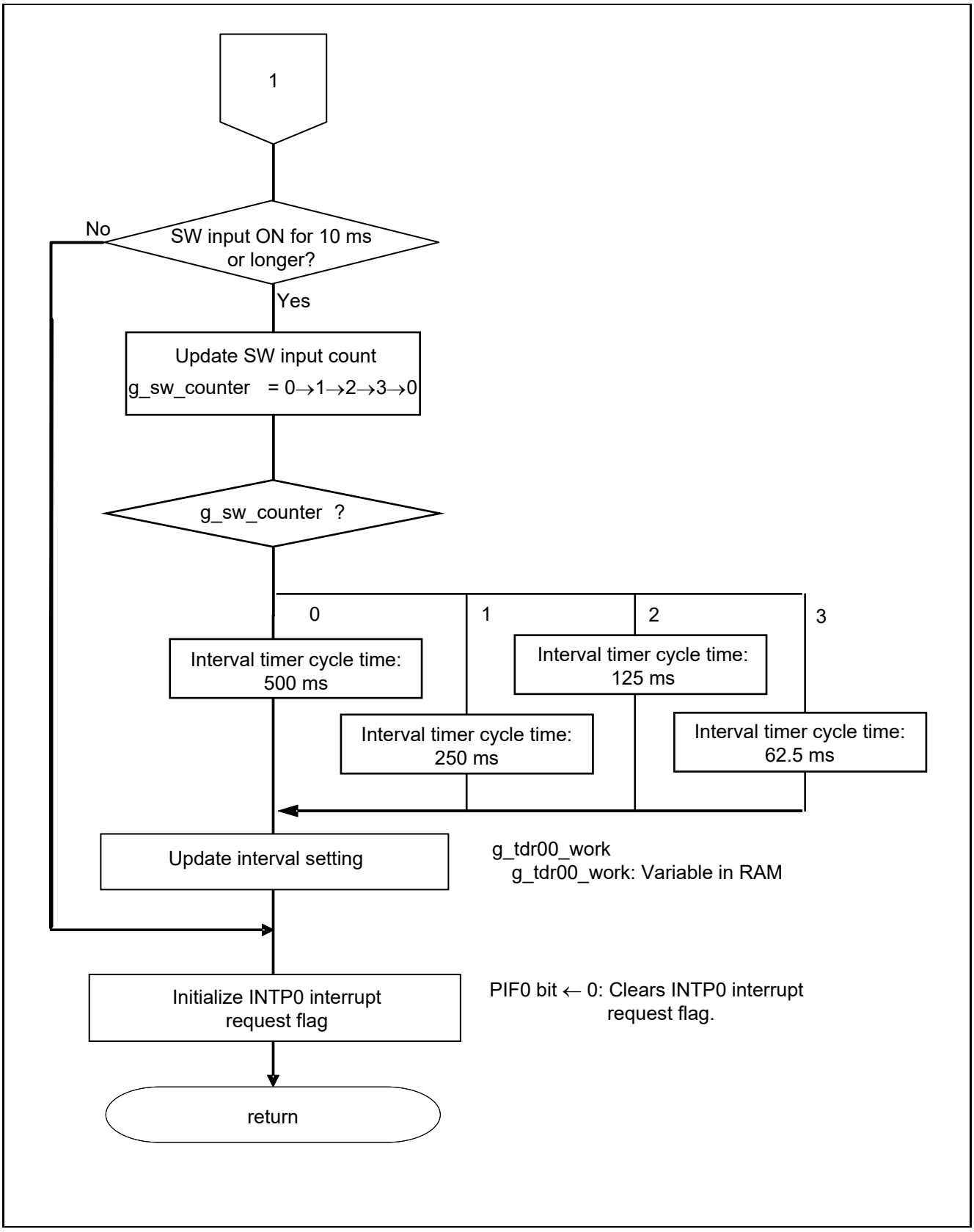


Figure 5.13 INTP0 Interrupt Processing (2/2)

6. Sample Code

The sample code is available on the Renesas Electronics Website.

7. Documents for Reference

User's Manual:

RL78/G10 User's Manual: Hardware (R01UH0384E)

RL78 Family User's Manual: Software (R01US0015E)

The latest version can be downloaded from the Renesas Electronics website.

Technical Updates/Technical News

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REVISION HISTORY	RL78/G10 Timer Array Unit (Interval Timer) CC-RL

Rev.	Date	Description	
		Page	Summary
1.00	May. 12, 2017	—	First edition issued

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

   Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

   ¾ The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

   The state of the product is undefined at the moment when power is supplied.

   ¾ The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
   In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.
   In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

   Access to reserved addresses is prohibited.

   ¾ The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

   After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

   ¾ When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

   Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

   ¾ The characteristics of Microprocessing unit or Microcontroller unit products in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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Renesas Electronics America Inc.
2801 Scott Boulevard Santa Clara, CA 95050-2549, U.S.A.
Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited
9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3
Tel: +1-905-237-2004

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-585-100, Fax: +44-1628-585-900

Renesas Electronics Europe GmbH
Arcadiastrasse 10, 40472 Düsseldorf, Germany
Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100191, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, P. R. China 200333
Tel: +86-21-2226-0888, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited
Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2265-6688, Fax: +852 2886-9022

Renesas Electronics Taiwan Co., Ltd.
13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan
Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 1207, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics India Pvt. Ltd.
No.777C, 100 Feet Road, HAL II Stage, Indiranagar, Bangalore, India
Tel: +91-80-67208700, Fax: +91-80-67208777

Renesas Electronics Korea Co., Ltd.
12F., 234 Teheran-ro, Gangnam-Gu, Seoul, 135-080, Korea
Tel: +82-2-558-3737, Fax: +82-2-558-5141

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