RL78 Family

RL78 Flash Programmer (RL78 Protocol B)

Introduction
This application note describes how to write the program to the internal flash memory of the RL78 microcontroller that supports the RL78 Protocol B.

Target Device
RL78/G23

When applying the sample program covered in this application note to another microcomputer, modify the program according to the specifications for the target microcomputer and conduct an extensive evaluation of the modified program.

Related Documents
Documents related to this application note are listed below, refer to the following documents as well.

- RL78 Family RL78 Microcontroller (RL78 Protocol B) Serial Programming Guide (R01AN6332)
- RL78 Family Renesas Flash Driver RL78 Type 01 User’s Manual (R20UT4830)
- RL78 Family Renesas Flash Driver RL78 Type 01 SC version (Code Flash) (R20AN0653)
- RL78 Family Renesas Flash Driver RL78 Type 01 SC version (Data Flash) (R20AN0654)
- RL78/G23-128p Fast Prototyping Board User’s Manual (R20UT4870)
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1. **Overview**

This sample program is the firmware for writing the program to the flash memory in the RL78 microcontroller. The sample program has the following features.

- The writing target RL78 microcontroller (target MCU) conforms to the RL78 Protocol B.
- Serial programming of the RL78 Protocol B is used for writing.
- The program file (write data) conforms to the Motorola S-format.
- The sample program supports “online mode” for writing with a host PC and “offline mode” for writing without a host PC.
- The sample program realizes the flash programmer in combination with the evaluation board (RL78/G23-128p Fast Prototyping Board) containing RL78/G23.
1.1 Online Mode
In online mode, the flash memory in the target MCU is written by commands from the host PC through a USB connection between the host PC and the evaluation board (RL78/G23-128p Fast Prototyping Board).

Figure 1-1  Online mode

1.2 Offline Mode
In offline mode, the flash memory in the target MCU is written by pushing the switch (SW1) on the evaluation board (RL78/G23-128p Fast Prototyping Board).

Before writing, make a connection between the host PC and the evaluation board through a USB, and then download the settings and program file by commands.

The program file data is stored in the write data section in the RL78/G23 code flash memory.

Figure 1-2  Offline mode
2. Development Environments

The operation of the sample program provided with this application note has been tested under the following conditions.

Table 2-1  Operation Confirmation Conditions

<table>
<thead>
<tr>
<th>Item</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>MCU</td>
<td>Part No.: R7F100GSN2DFB (Internal memory: Code Flash 768KB, RAM 48KB, Data Flash 8KB)</td>
</tr>
<tr>
<td>Evaluation board (Programmer board)</td>
<td>RL78/G23-128p Fast Prototyping Board Part No.: RTK7RLG230CNSN000BJ Note: Set VDD to 1.8 V or higher because this sample program runs in HS mode (high-speed on-chip oscillator frequency: 32 MHz).</td>
</tr>
<tr>
<td>Emulator</td>
<td>E2 emulator Lite or E2 emulator is required to debug the sample program. The USB on the RL78/G23-128p Fast Prototyping Board is used for communication between the host PC and RL78/G23. Therefore, it cannot be used for debugging (RL78 COM port debug tool).</td>
</tr>
<tr>
<td>CS+ Integrated development environment</td>
<td>Renesas Electronics Corp. CS+ for CC V8.09.00</td>
</tr>
<tr>
<td></td>
<td>C compiler Renesas Electronics Corp. CC-RL V1.12.00</td>
</tr>
<tr>
<td>e2studio Integrated development environment</td>
<td>Renesas Electronics Corp. e2studio V2023-01</td>
</tr>
<tr>
<td></td>
<td>C compiler Renesas Electronics Corp. CC-RL V1.12.00</td>
</tr>
<tr>
<td>IAR Integrated development environment</td>
<td>IAR Systems Corp. IAR Embedded Workbench for Renesas RL78 V4.21</td>
</tr>
<tr>
<td></td>
<td>C compiler IAR Systems Corp. IAR C/C++ Compiler for Renesas RL78 V 4.21.1</td>
</tr>
<tr>
<td>Flash driver</td>
<td>Renesas Electronics Corp. Renesas Flash Driver RL78 Type01</td>
</tr>
</tbody>
</table>

2.1 Renesas Flash Driver RL78 Type01

Renesas Flash Driver RL78 Type 01 (hereafter called RFD RL78 Type 01) is software for reprogramming the flash memory in the RL78/G2x.

For details, refer to Renesas Flash Driver RL78 Type01 Use’s manual (R20UT4830).

Renesas Flash Driver RL78 Type01 (RFD) can be downloaded from the following URL.

  https://www.renesas.com/document/scd/renesas-flash-driver-rl78-type-01-rl78g23

Remark

There are RFD using Smart Configurator (SC) version and RFD not using SC version (The above URL). RFD using SC version is included in this sample program.
3. External Specification

3.1 Host Communication Specifications

This section describes the specifications for communication between the host PC and the programmer board.

Connect the USB connector (for USB-UART conversion) on the programmer board (RL78/G23-128p Fast Prototyping Board) to the host PC.

This sample program works based on the following settings for communication between the host PC and the programmer board.

<table>
<thead>
<tr>
<th>Table 3-1 Host communication settings</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Item</strong></td>
</tr>
<tr>
<td>Data length (bit)</td>
</tr>
<tr>
<td>Data transfer direction</td>
</tr>
<tr>
<td>Parity bit</td>
</tr>
<tr>
<td>Communication speed (bps)</td>
</tr>
<tr>
<td>Stop bit (bit)</td>
</tr>
<tr>
<td>Flow control</td>
</tr>
</tbody>
</table>

3.1.1 Specifications Common to All Commands

Command communication is performed according to the following specifications.

- The programmer board sends a command prompt '>' to the host PC after a preparation for reception is completed.
- The programmer board sends a command input echo back.
- Send a linefeed character (CRLF:0x0D,0x0A) from the host PC when executing a command.
- The programmer board sends "PASS" when command execution is successful.
- The programmer board sends "ERROR:XX" when command execution fails. XX is expressed as a 2-digit hexadecimal number. For details, see Table 3-7 and .

The following commands of the host PC are available for this sample program.

<table>
<thead>
<tr>
<th>Table 3-2 List of Host PC Commands</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Command</strong></td>
</tr>
<tr>
<td>setup</td>
</tr>
<tr>
<td>lod</td>
</tr>
<tr>
<td>ep</td>
</tr>
</tbody>
</table>
3.1.2 setup Command

This command makes operation settings of the programmer board.

This command enables the setting of communication mode with the target MCU.

This command must be executed in advance before the program file is written to the target MCU. The setting made by this command is stored in the data flash memory of RL78/G23 on the programmer board.

Table 3-3 shows the details of the option of the setup command. Figure 3-1 shows an example of usage of the setup command.

<table>
<thead>
<tr>
<th>Option</th>
<th>Setting</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>-if</td>
<td>uart1</td>
<td>uart1: Performs communication with the target MCU using a single-line UART (TOOL0).</td>
</tr>
<tr>
<td></td>
<td></td>
<td>uart2: Performs communication with the target MCU using a dedicated UART (TOOL0, TOOLTxD, TOOLRxD). When omitted: It is the same result as when the uart1 is specified.</td>
</tr>
</tbody>
</table>

Figure 3-1 Terminal Software Screen: Usage example of setup command

> setup -if uart1
PASS
>


3.1.3 lod Command

This command stores the program file data in the data write section of the code flash memory in the RL78/G23 on the programmer board for offline mode. For details of section, see 5.4 Section Settings.

The lod command must be executed before writing data to the target MCU in offline mode.

After the lod command is sent, send a Motorola S-format file after the message "Please send a motorola-s file." is received from the programmer board. The data record of Motorola S-format file must be sorted in ascending order of address.

When sending a file using terminal software such as TeraTerm, send it in binary mode.

Figure 3-2 shows an example of usage of the lod command.

![Terminal Software Screen: Usage example of lod command](image)
3.1.4 ep Command

This command erases and writes data to the target MCU in online mode while receiving a Motorola S-format file from the host PC.

Checksum (CRC check) can be optionally added, performing erase all, write, and checksum in this order.

The flash memory erasing range covers the entire area of the code flash and data flash memories.

The flash memory write/verify range covers the area including the transmitted Motorola S-format data.

The write operation is made in the memory size of the internal flash. When data is less than the memory size, the area without data is filled with 0xFF.

The flash memory checksum range covers the entire area of the code flash and data flash memories. (The data flash is included in the range for the data flash-mounted microcontroller only.)

After the ep command is sent, send a Motorola S-format file after the message "Please send a motorola-s file." and "Connect" are received from the programmer board. When sending a file using terminal software such as TeraTerm, send it in binary mode.

The RL78/G23 on the programmer board performs communication setting processing with the target MCU while receiving the file data from the host PC. The RL78/G23 on the programmer board sends "PASS" to the host PC when communication setting processing with the target MCU is done. After that, erasing the flash memory of the target MCU and writing the data to the flash memory of the target MCU are executed. The RL78/G23 on the programmer board sends "PASS" to the host PC when erasing and writing is done.

When the option for checksum is specified, checksum is executed after writing. The RL78/G23 on the programmer board sends the checksum value of the code flash and data flash received from the target MCU to the host PC.

Table 3-4 describes the option of the ep command. Figure 3-3 shows an example of usage of the ep command.

<table>
<thead>
<tr>
<th>Option</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>-s</td>
<td>With this option is specified, checksum (CRC check) is additionally executed in online mode.</td>
</tr>
</tbody>
</table>
Figure 3-3  Terminal Software Screen: Usage example of ep command

```plaintext
> ep
Please send a motorola-s file.

Connect
<Sends a Motorola S-format file>
PASS
Erase, Program
PASS
> ep -s
Please send a motorola-s file.

Connect
<Sends a Motorola S-format file>
PASS
Erase, Program
PASS
Checksum
Code Flash: 0x1234
Data Flash: 0xABCD
PASS
>
```
3.2 Writing in Online Mode

Writing the program file data to the target MCU is started by execution of ep command.

The flow of writing is shown below.

① Execute the setup command.
② Execute the ep command.

3.3 Writing in Offline Mode

Push the switch (SW1) on the programmer board (RL78/G23-128p Fast Prototyping Board) to start erasing and writing data without using a host PC.

The flow of writing is shown below.

① Execute the setup command.
② Execute the lod command.
③ Push the “SW1” switch.

Note. ① and ② are executed with the programmer board and the host PC connected.

3.3.1 Executing Writing

Push the switch (SW1) to start erasing and writing data.

Table 3-5 Flash Memory Operating Range in Offline Mode

<table>
<thead>
<tr>
<th>Operation</th>
<th>Operating Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>Erasure</td>
<td>Entire area of the code flash memory and data flash memory</td>
</tr>
<tr>
<td>Writing</td>
<td>Entire area of the code flash memory and data flash memory</td>
</tr>
<tr>
<td></td>
<td>A value of 0xFF is written to areas where no data is present in the program file</td>
</tr>
<tr>
<td></td>
<td>data of Motorola S-format.</td>
</tr>
</tbody>
</table>

3.4 LED Display Specifications

Table 3-6 below shows the relationship between LED display and programmer board operating state.

Table 3-6 LED Display and Operating State

<table>
<thead>
<tr>
<th>LED Display</th>
<th>Operating Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>LED1</td>
<td></td>
</tr>
<tr>
<td>(Indicator of Single-line UART / Dedicated UART)</td>
<td>Lighting-off: UART is stopped.</td>
</tr>
<tr>
<td></td>
<td>Lighting-on: UART is operating in Single UART</td>
</tr>
<tr>
<td></td>
<td>Blinking: UART is operating in Dedicated UART</td>
</tr>
<tr>
<td>LED2</td>
<td></td>
</tr>
<tr>
<td>(Operation indicator)</td>
<td>Lighting-off: Rewriting is stopped.</td>
</tr>
<tr>
<td></td>
<td>Lighting-on: Rewriting is operating.</td>
</tr>
<tr>
<td></td>
<td>Blinking: Error has occurred.</td>
</tr>
</tbody>
</table>
### 3.5 Error Code Specifications

If command execution fails, the programmer board sends an error message (ERROR:XX) to the host PC. XX means the 2-digit hexadecimal error code.

Table 3-7 shows the list of error codes.

<table>
<thead>
<tr>
<th>Error Code (Hexadecimal)</th>
<th>Description</th>
</tr>
</thead>
</table>
| 04                       | Command number error  
                           | This error occurs when a command number error of the RL78 Protocol B status code is received from the target MCU. |
| 15                       | NACK  
                           | This error occurs when a NACK of the RL78 Protocol B status code is received from the target MCU. |
| 1A                       | Erasure error  
                           | This error occurs when an erasure error of the RL78 Protocol B status code is received from the target MCU. |
| 1B                       | Blank error  
                           | This error occurs when a blank error of the RL78 Protocol B status code is received from the target MCU. |
| 1C                       | Write error  
                           | This error occurs when a write error of the RL78 Protocol B status code is received from the target MCU. |
| F9                       | Incorrect command or parameter data  
                           | This error occurs when an incorrect command or parameter data is received from the host PC. |
| FA                       | Buffer overrun error  
                           | This error occurs when the data is received from the host PC and the buffer overruns.  
                           | Confirm that the flow control with the host PC is set to Software (Xon/Xoff). |
| FB                       | Invalid Motorola S-format data  
                           | This error occurs if Motorola S-format data sent by the lod command or ep command is invalid.  
                           | This error occurs even when Motorola S-format data is not in ascending order of address. |
| FC                       | Target MCU communication timeout  
                           | This error occurs if a timeout occurs during communication between the programmer board and the target MCU. |
| FD                       | Invalid address of Motorola S-format file  
                           | This error occurs if an out-of-range address is included in the Motorola S-format file received by the lod command. |
| FE                       | Command communication data error  
                           | This error occurs if an invalid packet format is received from the target MCU. |
| FF                       | System error  
                           | This error occurs if the program does not work correctly. |
3.6 Flowcharts

3.6.1 Main Loop (main function)

Figure 3-4 shows the operation of main loop.

![Main Loop Flowchart](image-url)
3.6.2 Flow of Writing in Online Mode

Figure 3-5 shows the flow of writing processing by executing ep command.

Figure 3-5 Online Mode
3.6.3 Flow of Writing in Offline Mode

Figure 3-6 shows the flow of writing processing by pushing the “SW1” switch.

Figure 3-6 Offline Mode

Initial Communications
According to setup command setting, do the following:
・ Transition to programming mode
  (Determine whether data flash-mounted or not based on the response code of mode setting.)

Write after erase (Code flash)

Data flash-mounted product? No Yes

Write after erase (Data flash)

Output low level from RESET
4. Hardware Descriptions

4.1 Example of Hardware Configuration

Figure 4-1 shows an example of the hardware configuration used in the application note.

Note 1. This simplified circuit diagram was created to show an overview of connections only. When actually designing your circuit, make sure the design includes appropriate pin handling and meets electrical characteristic requirements (connect each input-only port to VDD or VSS through a resistor.)

Note 2. Connect any pins whose name begins with EVSS to VSS, and any pins whose name begins with EVDD to VDD, respectively.

Note 3. VDD must not be lower than the reset release voltage (VLVD0) that is specified for the LVD0.
4.2 Target Interface Specifications

The following figures show how to connect the programmer board (RL78/G23) to the target MCU.

4.2.1 Single Line UART

![Single Line UART Diagram]

Figure 4-2 Single Line UART

4.2.2 Dedicated UART

![Dedicated UART Diagram]

Figure 4-3 Dedicated UART
4.3 List of Pins to be Used

Table 4-1 lists the pins to be used and their functions.

<table>
<thead>
<tr>
<th>Pin Name</th>
<th>I/O</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>P12/TxD0</td>
<td>Output</td>
<td>Host PC communication transmit pin (UART0)</td>
</tr>
<tr>
<td>P11/RxD0</td>
<td>Input</td>
<td>Host PC communication receive pin (UART0)</td>
</tr>
<tr>
<td>P13/TxD2</td>
<td>Output</td>
<td>Target interface communication transmit pin (UART2)</td>
</tr>
<tr>
<td>P14/RxD2</td>
<td>Input</td>
<td>Target interface communication receive pin (UART2)</td>
</tr>
<tr>
<td>P50, P51</td>
<td>Output</td>
<td>Output (to LED1/LED2) pins</td>
</tr>
<tr>
<td>P24</td>
<td>Output</td>
<td>Target MCU &quot;RESET&quot; control pin</td>
</tr>
<tr>
<td>P137/INTP0</td>
<td>Input</td>
<td>SW1 interrupt input pin</td>
</tr>
</tbody>
</table>

Caution: In this application note, only the used pins are processed. When actually designing your circuit, make sure the design includes sufficient pin processing and meets electrical characteristic requirements.

4.4 Setting of RL78/G23-128p Fast Prototyping Board

When using the E2 emulator Lite or the E2 emulator for debugging, make the following settings for the RL78/G23-128p Fast Prototyping Board.

- Cut the cut-patterns "TOOL0_USB", "RESET", and "T_RESET".
- Short-circuit (between 2 and 3 pins) pin headers "J15", "J16", and "J19".

Set the operating voltages of VDD and EVDD with "J20".

For details, refer to RL78/G23-128p Fast Prototyping Board User’s Manual (R20UT4870).
5. Software explanation

5.1 Folder Structure

Figure 5-1 shows the folder structure of the sample program.

![Folder Structure of Sample Program](image)

**Remark**

This sample program uses source files generated by Smart Configurator (SC). When source files are regenerated by Smart Configurator, download each Renesas Flash Driver RL78 Type01 SC version of ④ from Renesas Electronics website and copy them to the folder where Smart Configurator refers to. If source files are regenerated without copying each Renesas Flash Driver RL78 Type01 SC version, files under the folders ④ are deleted. If you do not regenerate codes in Smart Configurator, downloading and copying are not required.
[Procedure]

1. Download the followings from Renesas Electronics website and unzip them in any folder.
   - RL78 Family Renesas Flash Driver RL78 Type 01 SC version (Flash Common) - Sample Code
   - RL78 Family Renesas Flash Driver RL78 Type 01 SC version (Code Flash) - Sample Code
   - RL78 Family Renesas Flash Driver RL78 Type 01 SC version (Data Flash) - Sample Code

2. Start Smart Configurator and click “Components” tab.

   Figure 5-2  Select Smart Configurator “Components” tab

   ![Select Smart Configurator “Components” tab](image)

3. Click “addition Add component” button to open “New Component” dialog.

   Figure 5-3  Smart Configurator “Add component” button

   ![Smart Configurator “Add component” button](image)

Figure 5-4  Smart Configurator “Configure general settings”

5. Click “Module Download” in “Preferences” dialog.

Figure 5-5  Smart Configurator “Preferences” dialog (1/2)
6. Click “Location(RL78)” in “Preferences” dialog.

Figure 5-6  Smart Configurator “Preferences” dialog (2/2)

7. Copy “.xml” files and “.zip” files unzipped in Step 1 to the folder opened in Windows Explorer.

Figure 5-7  Copy Renesas Flash Driver RL78 Type01 (SC version)


9. Restart Smart Configurator before regeneration to make Smart Configurator recognize Renesas Flash Driver RL78 Type01 copied in Step 7.
5.2 Setting of Option Byte

Table 5-1 shows the option byte settings used in this sample program.

<table>
<thead>
<tr>
<th>Address</th>
<th>Setting Value</th>
<th>Content</th>
</tr>
</thead>
<tbody>
<tr>
<td>000C0H/040C0H</td>
<td>01101110B</td>
<td>Disables the watchdog timer</td>
</tr>
<tr>
<td>000C1H/040C1H</td>
<td>11111111B</td>
<td>LVD0 detection voltage: Reset mode</td>
</tr>
<tr>
<td></td>
<td></td>
<td>At rising edge TYP. 1.90 V (1.84 V ~ 1.95 V)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>At falling edge TYP. 1.86 V (1.80 V ~ 1.91 V)</td>
</tr>
<tr>
<td>000C2H/040C2H</td>
<td>11101000B</td>
<td>HS mode, High-speed on-chip oscillator clock: 32MHz</td>
</tr>
<tr>
<td>000C3H/040C3H</td>
<td>10000101B</td>
<td>Enables on-chip debugging</td>
</tr>
</tbody>
</table>

RL78/G23’s option bytes consist of user option bytes (000C0H to 000C2H) and on-chip debug option byte (000C3H).

5.3 On-chip Debug Security ID

To protect against third parties reading the contents of memory, RL78/G23 has an area in the range from 000C4H to 000CDH to hold the security ID code for on-chip debugging.

5.4 Section Settings

Table 5-2 shows the CC-RL section settings.

- Sections beginning with "." are reserved by the compiler.
- Sections beginning with "RFD_" or "SMP_" are defined by the specifications of the Renesas Flash Driver RL78 Type01(RFD) and its sample program.
- Sections "FP_DATA" and "FP_CODE" are prepared for storing data to be written to the target MCU.
- Though reserved section names of IAR vary, IAR has a similar section configuration.
<table>
<thead>
<tr>
<th>Section Name</th>
<th>Address</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>.vect</td>
<td>0x00000</td>
<td>Interrupt vector table</td>
</tr>
<tr>
<td>.option_byte</td>
<td>0x000C0</td>
<td>Section specific for user option byte and on-chip debugging specification</td>
</tr>
<tr>
<td>.security_id</td>
<td>0x000C4</td>
<td>Section specific for security ID specification</td>
</tr>
<tr>
<td>.const</td>
<td>0x03000</td>
<td>ROM data (allocated to the near area)</td>
</tr>
<tr>
<td>.text</td>
<td>(Automatic allocation)</td>
<td>Section for code (allocated to the near area)</td>
</tr>
<tr>
<td>.RLIB</td>
<td>(Automatic allocation)</td>
<td>Section for code of runtime libraries</td>
</tr>
<tr>
<td>.SLIB</td>
<td>(Automatic allocation)</td>
<td>Section for code of standard libraries</td>
</tr>
<tr>
<td>.textf</td>
<td>(Automatic allocation)</td>
<td>Section for code (allocated to the far area)</td>
</tr>
<tr>
<td>.constf</td>
<td>(Automatic allocation)</td>
<td>ROM data (allocated to the far area)</td>
</tr>
<tr>
<td>.data</td>
<td>(Automatic allocation)</td>
<td>Section for near initialized data (with initial value)</td>
</tr>
<tr>
<td>.sdata</td>
<td>(Automatic allocation)</td>
<td>Section for initialized data (with initial value, variable allocated to saddr)</td>
</tr>
<tr>
<td>RFD_DATA_n</td>
<td>(Automatic allocation)</td>
<td>Section for RFD: Data section for initialized global variables</td>
</tr>
<tr>
<td>RFD_CMN_f</td>
<td>(Automatic allocation)</td>
<td>Section for RFD: Program section of API functions used in common for flash memory control</td>
</tr>
<tr>
<td>RFD_CF_f</td>
<td>(Automatic allocation)</td>
<td>Section for RFD: Program section of API functions for code flash memory control</td>
</tr>
<tr>
<td>RFD_DF_f</td>
<td>(Automatic allocation)</td>
<td>Section for RFD: Data section for initialized global variables</td>
</tr>
<tr>
<td>SMP_CMN_f</td>
<td>(Automatic allocation)</td>
<td>Section for RFD sample: Program section of sample functions used in common for flash memory control</td>
</tr>
<tr>
<td>SMP_CF_f</td>
<td>(Automatic allocation)</td>
<td>Section for RFD sample: Program section of sample functions for code flash memory control</td>
</tr>
<tr>
<td>.monitor2</td>
<td>0x1FE00</td>
<td>Debug monitor area</td>
</tr>
<tr>
<td>FP_DATA</td>
<td>0x34000</td>
<td>Section for write data to data flash: Save binary data extracted from the Motorola S-format file.</td>
</tr>
<tr>
<td>FP_CODE</td>
<td>0x36000</td>
<td>Section for write data to code flash: Save binary data extracted from the Motorola S-format file.</td>
</tr>
<tr>
<td>FP_CODE_END</td>
<td>0xB5FFF</td>
<td>End address of Section for write data to code flash</td>
</tr>
<tr>
<td>.dataR</td>
<td>0xF3F00</td>
<td>Section for near initialized data (with initial value) (Data copied from ROM area)</td>
</tr>
<tr>
<td>.bss</td>
<td>(Automatic allocation)</td>
<td>Section for data area (without initial value, allocated to the near area)</td>
</tr>
<tr>
<td>RFD_DATA_nR</td>
<td>(Automatic allocation)</td>
<td>Section for RFD: Data section for initialized global variables (Data copied from ROM area)</td>
</tr>
<tr>
<td>RFD_CMN_fR</td>
<td>(Automatic allocation)</td>
<td>Section for RFD: Program section of API functions used in common for flash memory control (Program copied from ROM area)</td>
</tr>
<tr>
<td>RFD_CF_fR</td>
<td>(Automatic allocation)</td>
<td>Section for RFD: Program section of API functions for code flash memory control (Program copied from ROM area)</td>
</tr>
<tr>
<td>SMP_CMN_fR</td>
<td>(Automatic allocation)</td>
<td>Section for RFD sample: Program section of sample functions used in common for flash memory control (Program copied from ROM area)</td>
</tr>
<tr>
<td>SMP_CF_fR</td>
<td>(Automatic allocation)</td>
<td>Section for RFD sample: Program section of sample functions for code flash memory control (Program copied from ROM area)</td>
</tr>
<tr>
<td>.sdataR</td>
<td>0xFFE20</td>
<td>Section for initialized data (with initial value, variable allocated to saddr) (Data copied from ROM area)</td>
</tr>
<tr>
<td>.sbss</td>
<td>(Automatic allocation)</td>
<td>Section for data area (without initial value, variable allocated to saddr)</td>
</tr>
</tbody>
</table>
5.4.1 Data Flash Area (0xF1000 - 0xF2FFF, 8KB)

The information set by the setup command is stored at the start address (0xF1000) of the data flash memory as a COMMAND_DATA structure.

The programmer board reads the information set by the setup command from the data flash memory during the startup.

Table 5-3 shows the specifications of the COMMAND_DATA structure.

<table>
<thead>
<tr>
<th>Member</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>TERMINAL_COMMAND com</td>
<td>Temporary variable (Type of command received from the host PC)</td>
</tr>
<tr>
<td>UART_IF uart_if</td>
<td>setup command setting (Communication mode)</td>
</tr>
<tr>
<td></td>
<td>UART_1LINE: Single line UART (Initial setting)</td>
</tr>
<tr>
<td></td>
<td>UART_2LINE: Dedicated UART</td>
</tr>
<tr>
<td>uint8_t checksum</td>
<td>Temporary variable (used by ep command)</td>
</tr>
</tbody>
</table>
5.5 Smart Configurator Settings

The peripherals of RL78/G23 on the programmer board set by Smart Configurator are shown below.

(1) Initialize the I/O Port.
   - Set P24, P50 and P51 to an output port.

(2) Initialize the Serial Array Unit.
   - Use UART0. (P12 is used as TXD0, P11 is used as RXD0)
   - Set the Operation clock to CK00, set the Clock source to fCLK/2.
   - Set the Transfer mode to Single transfer mode.
   - Set the Data length to 8 bits.
   - Set the Transfer direction to LSB.
   - Set the Parity to None.
   - Set the Stop bit length to 1 bit.
   - Set the Transfer data level to Non-reverse.
   - Set the Transfer rate to 115200bps.

   - Use UART2. (P13 is used as TXD2, P14 is used as RXD2)
   - Set the Operation clock to CK10, set the Clock source to fCLK/2.
   - Set the Transfer mode to Single transfer mode.
   - Set the Data length to 8 bits.
   - Set the Transfer direction to LSB.
   - Set the Parity to None.
   - Set the Stop bit length to 2 bits (Transmission) and 1 bit (Reception).
   - Set the Transfer data level to Non-reverse.
   - Set the Transfer rate to 115200bps.
   - Set the Transmit end interrupt priority (INTST2) to Level 2.
   - Set the Reception end interrupt priority (INTSR2) to Level 2.

(3) Initialize the External Interrupt
   - Use INTP0.
   - Set the Valid edge to Falling edge.
(4) Initialize the Timer Array Unit
- Use TAU0_1 as Interval Timer by 8 bit counter mode.
- Set the Operation clock to CK02.
- Set the Clock source to fCLK/2.
- Set the Operation mode to Lower 8 bits.
- Set the Interval value (lower 8 bits) to 240ns.
- Check the Use End of timer channel 1 count, generate an interrupt (INTTM01) and set the Priority to Level 2.

- Use TAU0_3 as Interval Timer by 8 bit counter mode.
- Set the Operation clock to CK03.
- Set the Clock source to fCLK/2^8.
- Set the Operation mode to Lower 8 bits.
- Set the Interval value (lower 8 bits) to 1ms.
- Check the Use End of timer channel 3 count, generate an interrupt (INTTM03)

- Use TAU0_4 as Interval Timer by 16 bit counter mode.
- Set the Operation clock to CK00.
- Set the Clock source to fCLK/2^8.
- Set the Operation mode to Lower 8 bits.
- Set the Interval value (16 bits) to 500ms.
- Check the Use End of timer channel 4 count, generate an interrupt (INTTM04)

- Use TAU1_1 as Interval Timer by 8 bit counter mode.
- Set the Operation clock to CK12.
- Set the Clock source to fCLK/2.
- Set the Operation mode to Lower 8 bits.
- Set the Interval value (lower 8 bits) to 1us.
- Check the Use End of timer channel 1 count, generate an interrupt (INTTM11)

- Use TAU1_3 as Interval Timer by 8 bit counter mode.
- Set the Operation clock to CK10.
- Set the Clock source to fCLK.
- Set the Operation mode to Lower 8 bits.
- Set the Interval value (lower 8 bits) to 90us.
- Check the Use End of timer channel 3 count, generate an interrupt (INTTM13) and set the Priority to Level 2.

(5) Initialize the Voltage Detector
- Set the Operation mode to Reset mode.
- Set the Reset generation level (VLVD0) to 1.86V.
### 5.6 List of Functions

Table 5-4 shows the sample program functions.

<table>
<thead>
<tr>
<th>Function Name</th>
<th>Outline</th>
</tr>
</thead>
<tbody>
<tr>
<td>r_Terminal_Init_Flash</td>
<td>Initializes Renesas Flash Driver RL78 Type01.</td>
</tr>
<tr>
<td>r_Terminal_Command_Init</td>
<td>Initializes the command setting information.</td>
</tr>
<tr>
<td>r_Terminal_Command_Read</td>
<td>Acquires the command setting information of the setup command.</td>
</tr>
<tr>
<td>r_Terminal_Command_Recvieve</td>
<td>Receives the command from the host PC.</td>
</tr>
<tr>
<td>r_Terminal_Command_SETUP</td>
<td>setup command processing.</td>
</tr>
<tr>
<td>r_Terminal_Command_LOD</td>
<td>lod command processing.</td>
</tr>
<tr>
<td>r_Terminal_Command_EP</td>
<td>ep command processing.</td>
</tr>
<tr>
<td>r_Terminal_Command_SW</td>
<td>Writing processing in offline mode.</td>
</tr>
<tr>
<td>r_Terminal_Send_Error_Message</td>
<td>Sends error message to the host PC.</td>
</tr>
<tr>
<td>r_Terminal_Clear_Write_Buffer</td>
<td>Clears the buffer for write data.</td>
</tr>
<tr>
<td>r_Decord_SRecord</td>
<td>Analyzes Motorola S-format file.</td>
</tr>
<tr>
<td>R_Config_PORT_Dev_Reset</td>
<td>Reset control of the target MCU.</td>
</tr>
<tr>
<td>R_Config_UART01_Terminal_Recev_Through</td>
<td>Discards transmit data of the host PC.</td>
</tr>
<tr>
<td>R_Config_UART01_Terminal_Set_Echo</td>
<td>Echo back setting for the communication with the host PC.</td>
</tr>
<tr>
<td>R_Config_UART01_Terminal_Data_Process_Start</td>
<td>Receives 1 line from the host PC.</td>
</tr>
<tr>
<td>R_Config_UART01_Terminal_Data_Process_End</td>
<td>Termination setting of 1 line reception from the host PC.</td>
</tr>
<tr>
<td>r_FP_CMD_Erase_Program_B</td>
<td>Executes the Write and erase command</td>
</tr>
<tr>
<td>r_FP_CMD_Erase_B</td>
<td>Executes the Write and erase command (processing of erase)</td>
</tr>
<tr>
<td>r_FP_CMD_Program_B</td>
<td>Executes the Write and erase command (processing of write)</td>
</tr>
<tr>
<td>r_FP_CMD_Verify_B</td>
<td>Executes the Write and erase command (processing of verify)</td>
</tr>
<tr>
<td>r_FP_CMD_Check_CRC_B</td>
<td>Executes the CRC check.</td>
</tr>
</tbody>
</table>
5.7 Specification of Functions

The function specifications of the sample code are shown below.

<table>
<thead>
<tr>
<th>Function</th>
<th>Outline</th>
<th>Declaration</th>
<th>Argument</th>
<th>Return Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>r_Terminal_Init_Flash</td>
<td>Initializes Renesas Flash Driver RL78 Type01.</td>
<td>int r_Terminal_Init_Flash(void)</td>
<td>None</td>
<td>RFD status code</td>
<td>Initializes Renesas Flash Driver RL78 Type01 (RFD).</td>
</tr>
<tr>
<td>r_Terminal_Command_Init</td>
<td>Initializes the command setting information.</td>
<td>void r_Terminal_Command_Init(COMMAND_DATA * com_data)</td>
<td>COMMAND_DATA * com_data: Command setting information</td>
<td>None</td>
<td>Initializes “com_data” specified by the argument.</td>
</tr>
<tr>
<td>r_Terminal_Command_Read</td>
<td>Acquires the command setting information of the setup command.</td>
<td>void r_Terminal_Command_Read(COMMAND_DATA * com_data)</td>
<td>COMMAND_DATA * com_data: Command setting information</td>
<td>None</td>
<td>Acquires the command setting information of the setup command from the data flash and set it to the “com_data” specified by the argument.</td>
</tr>
<tr>
<td>r_Terminal_Command_Recieve</td>
<td>Receives the command from the host PC.</td>
<td>void r_Terminal_Command_Recieve(COMMAND_DATA * com_data)</td>
<td>COMMAND_DATA * com_data: Command setting information</td>
<td>None</td>
<td>Receives the command line transferred from the host PC and set it to the “com_data” specified by the argument as the command information.</td>
</tr>
<tr>
<td>r_Terminal_Command_SETUP</td>
<td>setup command processing.</td>
<td>void r_Terminal_Command_SETUP(COMMAND_DATA * com_data)</td>
<td>COMMAND_DATA * com_data: Command setting information</td>
<td>None</td>
<td>Stores the command setting information specified by the setup command in the data flash.</td>
</tr>
</tbody>
</table>
### r_Terminal_Command_LOD

**Outline**  
 lod command processing.

**Declaration**  
 int r_Terminal_Command_LOD(COMMAND_DATA * com_data)

**Argument**  
 COMMAND_DATA * com_data: Command setting information

**Return Value**  
 0: Normal end  
 1: Abnormal end

**Description**  
 Erases the FP_CODE and FP_DATA sections in the code flash memory, receives and analyzes the Motorola S-format file from the host PC, and then stores the acquired binary data in the FP_CODE and FP_DATA sections.

### r_Terminal_Command_EP

**Outline**  
 ep command processing.

**Declaration**  
 int r_Terminal_Command_EP(COMMAND_DATA * com_data)

**Argument**  
 COMMAND_DATA * com_data: Command setting information

**Return Value**  
 0: Normal end  
 1: Abnormal end

**Description**  
 Executes the ep command.  
 For the flow of the processing, refer to 3.6.2 Flow of Writing in Online Mode.

### r_Terminal_Command_SW

**Outline**  
 Writing processing in offline mode.

**Declaration**  
 int r_Terminal_Command_SW(COMMAND_DATA * com_data)

**Argument**  
 COMMAND_DATA * com_data: Command setting information

**Return Value**  
 0: Normal end  
 1: Abnormal end

**Description**  
 Executes the writing processing in the Offline mode.  
 For the flow of the processing, refer to 3.6.3 Flow of Writing in Offline Mode.

### r_Terminal_Send_Error_Message

**Outline**  
 Sends error message to the host PC.

**Declaration**  
 void r_Terminal_Send_Error_Message(uint8_t err)

**Argument**  
 uint8_t err: Error code

**Return Value**  
 None

**Description**  
 Sends the error message to the host PC.

### r_Terminal_Clear_Write_Buffer

**Outline**  
 Clears the buffer for write data.

**Declaration**  
 void r_Terminal_Clear_Write_Buffer(void)

**Argument**  
 None

**Return Value**  
 None

**Description**  
 Initializes the buffer for write data (global variable) used by the lod command and the ep command with 0xFF.
### r_Decord_SRecord

<table>
<thead>
<tr>
<th>Outline</th>
<th>Analyzes Motorola S-format file.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Declaration</td>
<td>void r_Decord_SRecord(uint8_t * srecord_str, uint16_t len, SRECORD_DATA * srecord_data)</td>
</tr>
</tbody>
</table>
| Argument | uint8_t * srecord_str: Record data storage buffer  
uint16_t len: Length of record data  
SRECORD_DATA * srecord_data: Record data information |
| Return Value | None |
| Description | Analyzes 1 line of the Motorola S-format file and acquires the record type, address, data and data size as the record data information. |

### R_Config_PORT_Dev_Reset

<table>
<thead>
<tr>
<th>Outline</th>
<th>Reset control of the target MCU.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Declaration</td>
<td>void R_Config_PORT_Dev_Reset(RESET_CONTROL reset)</td>
</tr>
<tr>
<td>Argument</td>
<td>None</td>
</tr>
</tbody>
</table>
| Return Value | RESET_CONTROL reset: Reset control  
RESET_ENABLE: RESET is active.  
RESET_DISABLE: RESET is inactive. |
| Description | Controls the reset of the target MCU. |

### R_Config_UART01_Terminal_Recv_Through

<table>
<thead>
<tr>
<th>Outline</th>
<th>Discards transmit data of the host PC.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Declaration</td>
<td>void R_Config_UART01_Terminal_Recv_Through(const uint16_t timeout)</td>
</tr>
<tr>
<td>Argument</td>
<td>const uint16_t timeout: Timeout time (ms)</td>
</tr>
<tr>
<td>Return Value</td>
<td>None</td>
</tr>
<tr>
<td>Description</td>
<td>Discards data transmitted from the host PC during the specified time-out period. Used to discard Motorola S-format data when the lod command or ep command results in an abnormal termination.</td>
</tr>
</tbody>
</table>

### R_Config_UART01_Terminal_Set_Echo

<table>
<thead>
<tr>
<th>Outline</th>
<th>Echo back setting for the communication with the host PC.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Declaration</td>
<td>void R_Config_UART01_Terminal_Set_Echo(uint8_t echo)</td>
</tr>
<tr>
<td>Argument</td>
<td>uint8_t echo: Echo back setting (Valid = 1, Invalid = 0)</td>
</tr>
<tr>
<td>Return Value</td>
<td>None</td>
</tr>
<tr>
<td>Description</td>
<td>Specifies whether to enable or disable echo back during communication with the host PC.</td>
</tr>
</tbody>
</table>
### R_Config_UART01_Terminal_Data_Process_Start

<table>
<thead>
<tr>
<th>Outline</th>
<th>Receives 1 line from the host PC.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Declaration</td>
<td>MD_STATUS R_Config_UART01_Terminal_Data_Process_Start(uint8_t ** rx_buf, uint16_t * rx_num, uint8_t * sw)</td>
</tr>
<tr>
<td>Argument</td>
<td>uint8_t ** rx_buf: Receive buffer</td>
</tr>
<tr>
<td></td>
<td>uint16_t * rx_num: Number of received characters</td>
</tr>
<tr>
<td></td>
<td>uint8_t * sw: Flag for SW1 (Pushed = 1, Not pushed = 0)</td>
</tr>
<tr>
<td>Return Value</td>
<td>MD_OK: Normal end</td>
</tr>
<tr>
<td>Description</td>
<td>Receives single-line data from the host PC.</td>
</tr>
<tr>
<td></td>
<td>Before receiving the next-line data, execute</td>
</tr>
<tr>
<td></td>
<td>R_Config_UART01_Terminal_Data_Process_End() once.</td>
</tr>
</tbody>
</table>

### R_Config_UART01_Terminal_Data_Process_End

<table>
<thead>
<tr>
<th>Outline</th>
<th>Termination setting of 1 line reception from the host PC.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Declaration</td>
<td>void R_Config_UART01_Terminal_Data_Process_End(void)</td>
</tr>
<tr>
<td>Argument</td>
<td>None</td>
</tr>
<tr>
<td>Return Value</td>
<td>None</td>
</tr>
<tr>
<td>Description</td>
<td>Always used together with R_Config_UART01_Terminal_Data_Process_Start().</td>
</tr>
<tr>
<td></td>
<td>Execute this function after executing</td>
</tr>
<tr>
<td></td>
<td>R_Config_UART01_Terminal_Data_Process_Start().</td>
</tr>
</tbody>
</table>

### r_FP_CMD_Erase_Program_B

<table>
<thead>
<tr>
<th>Outline</th>
<th>Executes the Write and erase command</th>
</tr>
</thead>
<tbody>
<tr>
<td>Declaration</td>
<td>uint8_t r_FP_CMD_Erase_Program_B(const uint8_t cmd, const uint8_t __far * data, const uint32_t data_len)</td>
</tr>
<tr>
<td>Argument</td>
<td>const uint8_t cmd: Command data</td>
</tr>
<tr>
<td></td>
<td>uint32_t * rom: ROM size (Code flash size)</td>
</tr>
<tr>
<td>Return Value</td>
<td>0: Normal end</td>
</tr>
<tr>
<td></td>
<td>Other than 0: Abnormal end (Refer to 3.5 Error Code Specifications)</td>
</tr>
<tr>
<td>Description</td>
<td>Executes the Write and erase command of the RL78 Protocol B.</td>
</tr>
</tbody>
</table>

### r_FP_CMD_Erase_B

<table>
<thead>
<tr>
<th>Outline</th>
<th>Executes the Write and erase command (processing of erase)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Declaration</td>
<td>uint8_t r_FP_CMD_Erase_B(const uint8_t cmd, uint32_t * rom)</td>
</tr>
<tr>
<td>Argument</td>
<td>const uint8_t cmd: Command data</td>
</tr>
<tr>
<td></td>
<td>uint32_t * rom: ROM size (Code flash size)</td>
</tr>
<tr>
<td>Return Value</td>
<td>0: Normal end</td>
</tr>
<tr>
<td></td>
<td>Other than 0: Abnormal end (Refer to 3.5 Error Code Specifications)</td>
</tr>
<tr>
<td>Description</td>
<td>Performs the erase processing in the Write and erase of the RL78 Protocol B.</td>
</tr>
</tbody>
</table>
### r_FP_CMD_Program_B

**Outline**
Executes the Write and erase command (processing of write)

**Declaration**
```c
uint8_t r_FP_CMD_Program_B(const uint8_t __far * data, const uint32_t data_len)
```

**Argument**
- `const uint8_t * data`: Write data
- `const uint32_t data_len`: Size of write data

**Return Value**
- `0`: Normal end
- Other than `0`: Abnormal end (Refer to 3.5 Error Code Specifications)

**Description**
Performs the write processing in the Write and erase of the RL78 Protocol B.
(This function is executed after `r_FP_CMD_Erase_B`.)

### r_FP_CMD_Verify_B

**Outline**
Executes the Write and erase command (processing of verify)

**Declaration**
```c
uint8_t r_FP_CMD_Verify_B(uint32_t rom)
```

**Argument**
- `uint32_t rom`: ROM size (Code flash size)

**Return Value**
- `0`: Normal end
- Other than `0`: Abnormal end (Refer to 3.5 Error Code Specifications)

**Description**
Performs the last ACK receive processing in the Write and erase of the RL78 Protocol B.
(This function is executed after `r_FP_CMD_Program_B`.)

### r_FP_CMD_Check_CRC_B

**Outline**
Executes the CRC check command

**Declaration**
```c
uint8_t r_FP_CMD_Check_CRC_B(const uint8_t cmd, uint16_t * crc)
```

**Argument**
- `const uint8_t cmd`: Command data
- `uint16_t * crc`: CRC data

**Return Value**
- `0`: Normal end
- Other than `0`: Abnormal end (Refer to 3.5 Error Code Specifications)

**Description**
Executes the CRC check command of the RL78 Protocol B.
6. Reference Documents

RL78/G23 User’s Manual: Hardware (R01UH0896)
RL78 Family RL78 Microcontroller (RL78 Protocol B) Serial Programming Guide (R01AN6332)
RL78 Family Renesas Flash Driver RL78 Type 01 User's Manual (R20UT4830)
RL78 Family Renesas Flash Driver RL78 Type 01 SC version (Code Flash) (R20AN0653)
RL78 Family Renesas Flash Driver RL78 Type 01 SC version (Data Flash) (R20AN0654)
RL78/G23-128p Fast Prototyping Board User's Manual (R20UT4870)

The latest versions can be downloaded from the Renesas Electronics website.

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## Revision History

<table>
<thead>
<tr>
<th>Rev.</th>
<th>Date</th>
<th>Description</th>
<th>Page</th>
<th>Summary</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.00</td>
<td>Feb 16, 2023</td>
<td></td>
<td>-</td>
<td>First edition</td>
</tr>
</tbody>
</table>
General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)
   A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on
   The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state
   Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins
   Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals
   After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin
   Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between $V_{IL}$ (Max.) and $V_{IH}$ (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between $V_{IL}$ (Max.) and $V_{IH}$ (Min.).

7. Prohibition of access to reserved addresses
   Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products
   Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.
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