

RZ/V2L

Release Note for RZ/V Flexible Software Package V1.1.0

Introduction

This is the release note for RZ/V2L Flexible Software Package V1.1.0 running on Arm® Cortex®-M33 core of RZ/V2L.

Target Device

RZ/V2L

Contents

1. Release Notes	2
2. Tools	2
3. Features Added	2
4. Third Party Software	2
5. Features Modified	2
6. Features Removed	2
7. Known Issues	3
Revision History	4

1. Release Notes

Flexible Software Package (FSP) for Renesas RZ/V MPU version 1.1.0.

Download the FSP for this release from [here](#).

Refer to the [RZ/V2L Getting Started with Flexible Software Package](#) for setup instructions, hardware details, and related links.

When connecting to RZ/V2L SMARC EVK using J-Link DLL, be sure to write bl2_bp-smarc-rzv2l_pmic.srec and fip-smarc-rzv2l_pmic.srec created in RZ/V2L Cortex-M33 Multi-OS Package.

Refer to the [RZ/V Release Note for RZ/V Multi-OS Package V1.10](#).

2. Tools

- e² studio: e² studio 2023-01
- GCC Compiler: 9-2019-q4-major
- Other Tools: Libgen Update for GNU ARM Embedded Toolchains

3. Features Added

- Controller Area Network – Flexible Data (r_canfd)
- Interrupt Controller (r_intc_irq)
- Temperature/Humidity Sensor HS300X (rm_hs300x)
- Temperature/Humidity Sensor HS400X (rm_hs400x)
- Gas Sensor ZMOD4410, ZMOD4510 (rm_zmod4xxx)
- Heart Rate, Blood Oxygen Concentration, Pulse Oximetry, Proximity, Light and Color Sensor OB1203 (rm_ob1203)

4. Third Party Software

These third party software solutions are included alongside RZ/V2L FSP:

- [Amazon FreeRTOS Kernel: 10.4.3-LTS-Patch-2](#)
- [Arm CMSIS5: 5.7.0](#)
- [OpenAMP/open-amp: v2018.10](#)
- [OpenAMP/libmetal: v2018.10](#)
- [Segger J-Link: 7.84a](#)

5. Features Modified

- Improved the accuracy of software delay carried out by R_BSP_SoftwareDelay.
- Warning message became displayed when invalid MHU channel was specified by Smart Configurator.

6. Features Removed

None

7. Bug Fixes

- r_rsipi
 - Fixed the issue that transfer cannot be performed if receiving or transmitting processing is performed continuously after executing 1-byte transmission processing.
 - Fixed the issue that SSL signal level keep function is enabled when slave mode is selected. This is the feature which is not available in slave mode.
- r_riic_master
 - Fixed the issue that communication with 10-bit address devices was failed.
 - Fixed the issue that parameter check was not performed even if parameter check setting is enabled in Smart Configurator.
- r_gtm
 - Fixed the issue that the period calculation in interval timer mode was wrong and so, it did not work with the correct period.
 - Fixed the issue that the first interrupt wasn't fired at the specified period because the counter is not initialized when the driver is reopened.
- r_scif_uart
 - Fixed the issue that data lost in the case of low baudrate.
- r_gpt
 - Fixed the issue that the GPT counter period was not set correctly.
 - Fixed the issue that all channels of GPT stopped when one channel was closed while multiple channels of GPT were running.

8. Known Issues

None

Revision History

Rev.	Date	Description	
		Page	Summary
1.0	14.Jan.22	-	-
1.1	31.Jan.23	2	Update section 2, 4, 5 and 7 in accordance with the update in RZ/V2 FSP v1.1.0.

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity.

Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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