
D2 Audio DSP

D2 Audio Customization GUI v3 Sound Processing and Audio Algorithms

Abstract

The D2 Audio Customization GUI v3 software design tool provides complete design flexibility enabling the user to define and build a complete audio sound processing system. Drag-and-drop inserting of individual audio processing blocks builds a customized audio path signal flow.

An extensive library of sound processing blocks enables a rich and powerful palette of programmable audio functions for virtually any consumer audio application. Highly flexible configuration supports accuracy of audio adjustment and control precision for premium performance consumer audio solutions. This application note provides a high level summary of these audio algorithms.

Contents

1. Overview	2
1.1 Interactive Control and Real-Time Graphing	4
1.2 Audio Processing Block Controls	5
1.3 Third Party Virtualization and Enhancements	5
1.4 Dynamic Register Addressing Architecture	5
2. Revision History	5

List of Figures

Figure 1. D2 Audio Customization GUI v3 Block Library and Signal Flow	4
Figure 2. Magnitude and Phase Graphing of Multiple Audio Blocks	4

Related Literature

For a full list of related documents, visit our website:

- [Audio IC](#) pages

1. Overview

The D2 Audio Customization GUI v3 software provides an easy-to-use graphical interface allowing you to build a customized audio signal flow and control with D2 Audio DSP Digital Audio Processor based amplifiers. Individual audio algorithms are supported through a library of audio processing blocks provided with the D2 Audio Customization GUI v3 program.

The designer builds the audio path signal flow by clicking each desired block from the audio library and dragging it into the signal flow workspace. [Figure 1 on page 4](#) shows an example that includes the block library adjacent to the signal flow window.

As many multiples desired of the same block type can be added, and there is no limit to the order of their placement or how they are interconnected. Each block that is dragged into the signal flow becomes a unique instance of that block's algorithm into the project's system firmware, complete with its own dedicated controls and registers.

The audio blocks supported in D2 Audio Customization GUI v3 are listed [Table 1](#). This table provides a brief overview of the audio algorithm function. Details and equations for each parameter are provided in the D2 Audio Customization GUI v3 User's Manual.

Table 1. D2 Audio Customization GUI v3 - Audio Algorithm Blocks

Audio Blocks	Audio Processing Function
Volume	Volume control blocks provide level and trim adjustments within the signal flow. Continuous adjustment through programmable gain ranges supports attenuation to -100dB and gain to +24dB. A single 24-bit register value provides gain setting and also supports selectable audio phase inversion.
Shared Volume	Shared Volume blocks implement multiple channels of level attenuation. The number of channels is configurable and a single 24-bit register value equally controls all channel levels. Volume is continuously adjustable from unity gain to -100dB.
Mixers	Mixers provide individually-adjustable inputs that are summed together and passed to their output. Each input mixing level is controlled with its own 24-bit register that provides continuous adjustment from unity gain to -100dB, along with full audio path cut-off and optional input phase inversion supporting sum and difference mixers. Audio block choices include 2-input, 4-input, and configurable N-input mixers accommodating as many inputs as desired.
Router	Routers perform independent channel routing assignment, connecting any input to any output. Number of channels is configurable with up to 64 inputs and 12 outputs.
Stereo A/B Switch	The A/B Switch provides stereo routing selection to switching either of two pairs of stereo inputs to its output. It operates as a double-pole, double-throw type of switch to the audio flow.
Tone Control	Tone Controls are shelving filters providing independent of gain and frequency adjustment for bass and treble tone settings. Frequency and gain are continuously and independently adjustable for both the bass and treble settings, supporting gain ranges of -14dB to +14dB.
Parametric Equalizers	Parametric Equalizer (EQ) blocks provide an adjustable bandpass or band-reject frequency response. With frequency-domain parameter settings of frequency, gain, and bandwidth or Q, parameters are continuously and independently adjustable. EQs are provided as individual audio blocks, and as blocks with groups of 3-Band and 5-Band EQs.
Biquad Filter (Frequency Domain Configuration)	The Biquad block is a frequency-domain parameter entry biquad filter implemented as a second-order biquad algorithm, providing configurable high-pass, low-pass, and all-pass filtering functions. First or second filter order can be selected, and parameter setting entries of frequency and damping coefficient are continuously adjustable. Bypass and polarity phase inversion is also supported.
Biquad Filter (z-Domain Configuration)	The z-Domain Biquad is a second order biquad digital algorithm that operates from direct entry of z-transform coefficients. The filter supports individual user entry coefficients enabling nearly any second order filter synthesis per cascable block.
Filter - Crossover	The Crossover Filter blocks provide high-pass or low-pass filtering using frequency domain adjustment settings. Blocks are implemented from two cascaded second-order biquad elements, with selections that directly implement Linkwitz-Riley, Butterworth, or Bessel filter presets. Slope setting is adjustable from 6, 12, 18, or 24dB per octave, frequency and damping coefficients are continuously adjustable, and bypass, active or mute functions are supported.
FIR Filter	The FIR filter is a configurable n-tap finite impulse response filter implementation. The number of taps and their coefficient values are defined in the audio signal flow simply through a user-generated list of tap coefficients for the FIR structure.

Table 1. D2 Audio Customization GUI v3 - Audio Algorithm Blocks (Continued)

Audio Blocks	Audio Processing Function
Excursion Control	Excursion Control is a specialized algorithm that dynamically controls audio based on frequency and level. Excursion Control boosts the low frequency response to compensate for physically-limited low-frequency capabilities of small loudspeakers and subwoofers at low listening levels. As listening levels rise, it dynamically adjusts its boost enabling an optimum sub-woofer listening experience at all loudness levels.
Delay	Delay Blocks introduce an adjustable delay of a channel's audio data. Buffer size is fully user-configurable, supporting adjustment over a range as small as milliseconds, to over 1 second.
Compressor	Compressors provide threshold-dependent level adjustments, implementing dynamic attenuation at configurable rates as the control signal level increases. Headroom level is configurable, supporting adjustable threshold ranges from -94dB to +36dB. Ratio, threshold, and attack/release times are also fully adjustable. Compressors incorporate a side chain input for algorithm control, supporting compressing or limiting operation from inputs independent of the processed channel audio signal flow.
Compressor/Expander	The Expander Compressor implements dynamic low level signal expansion, or an upward compression to audio levels. The Expander Compressor operates similar to the Compressor Blocks, but implements dynamic low level signal expansion for an upward compression to audio levels. Adjustable settings include gate threshold and ratio, expansion threshold, ratio, expansion gain limit, and attack and release times.
Envelope Voltage Controlled Amplifier	The Envelope VCA processes its control input to establish an amplitude envelope signal representing the audio path input level. The Envelope VCA operates similar to the compressor block, but uses its side chain input to establish an amplitude envelope of the audio level. Controls are similar to that of the Compressor Block.
Reverb	The Reverb Block is a 2-channel stereo reverb processor. It provides adjustable reverb time and damping settings, and its built-in mixer includes adjustments for both dry and wet audio levels.
Chime Generator	The Chime Generator contains three oscillators, each with adjustable frequency and gain. When triggered, the oscillators initially produce full programmed output levels that then decrease at their programmable decay time rate.
RMS Level Meter	RMS Meters provide a real-time indication of the signal levels through the audio processing path. Visible in the D2 Audio Customization GUI v3 signal flow, they provide continuous level indication. Measured data in the meter's registers can also be read by system controllers for monitoring levels in a final production system.
Fade-Pan	The Fade-Pan control provides level adjustment of four input channels to four output channels. Implementation includes a Rear/Front fade adjustment, and a Left/Right balance adjustment. Controls are continuously adjustable, providing unity gain at mid-point settings, and attenuating output levels of its channels as the respective control is adjusted away from that channel's direction.
Mono Mixer	A Mono Mixer is a threshold-gating mixer that routes the sum of either or both of its two inputs into its output, based on a control level input. This threshold-controlled mixing supports processing of input audio that can be mono on both Left and Right, or containing content on only one of Left or Right. This summed audio then passes equally to stereo processing and output system channels.
Dither Generator	The Dither Block generates a random noise (dither) pattern at a shaped-spectrum low level. This noise is available to sum into the audio path using a mixer, pushing up low-order bits of low-level audio. This process enables improved uniform audio quality when digital bit depth truncation is required because of interfacing equipment. Audio signal flow data is normally processed and output in digital format as 24 bits.
Harmonics Generator	The Harmonics Generator provides a harmonic spectrum content from audio presented to its input. Harmonic order and amplitude is programmable, supporting customized audio processing features.
Compound Blocks	Compound blocks are not specific algorithms, but are user-built from combinations of individual algorithm blocks or with nesting of other user-built blocks. Therefore, each compound block contains unique instances of every individual algorithm used in its building. Compound blocks are available for use in the same, or future projects. They can also be user-encrypted for IP protection.

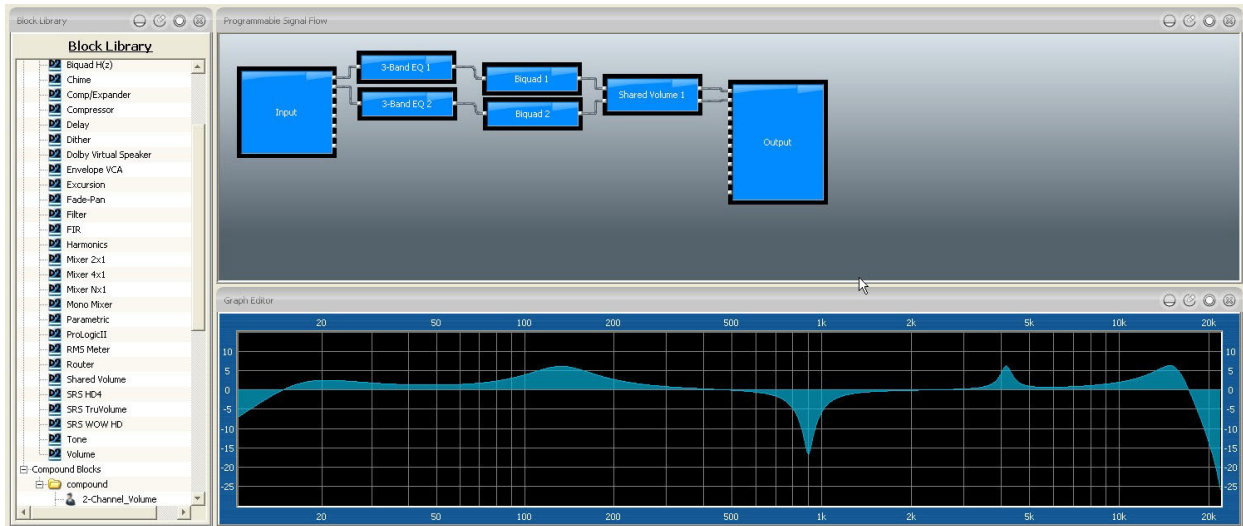


Figure 1. D2 Audio Customization GUI v3 Block Library and Signal Flow

1.1 Interactive Control and Real-Time Graphing

D2 Audio Customization GUI v3 provides dedicated controls for each audio block. Selecting the block within the audio signal flow also opens and displays that block’s control panel. Depending on the audio function, adjustment can be made by dragging and moving a slider or knob, selecting a pull-down choice menu, or entering a numeric value. All entry methods are real-time, and the connected hardware immediately receives its register commands to adjust parameter settings as you make the on-screen adjustment.

In addition to control knobs and buttons, D2 Audio Customization GUI v3 provides a flexible and useful graphical display of the actual audio response by simply selecting an audio block within the signal flow.

The graphic display shows either magnitude or phase of the selected block as shown in [Figure 2](#). Also, the program supports series or parallel audio path responses for multiple selected blocks, enabling system-level performance display.

The graphic display does more than provide indication. The block’s controls are integrated into the display graph, where dragging of the graph’s points causes actual real-time adjustment of parameter settings, duplicating the adjustment entries that are supported on the block’s control panels.

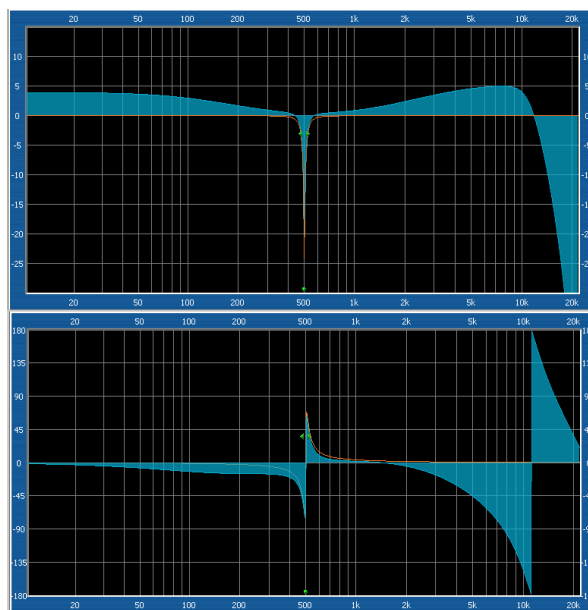


Figure 2. Magnitude and Phase Graphing of Multiple Audio Blocks

1.2 Audio Processing Block Controls

Each audio processing block is assigned its own registers providing adjustment controls for the parameters associated with its audio function. Some blocks use one register only, while other blocks with multiple control settings can use multiple registers for each control. The parameter equations for all of the audio blocks are provided in the D2 Audio Customization GUI v3 User's Manual.

1.3 Third Party Virtualization and Enhancements

Enhancement processing and virtualization algorithms from third-party technology providers are available to add to D2 Audio Customization GUI v3. As permitted through license agreements from the providers, these algorithms are supplied to the designer where they integrate into the D2 Audio Customization GUI v3 software, and appear as additional audio block choices within the Block Library. When in the Block Library, they are added to a project by simply dragging into the signal flow as with any other audio block.

Controls and graphical adjustments are included for these blocks. Register definitions are also included for each of the user controls associated with these blocks.

1.4 Dynamic Register Addressing Architecture

D2 Audio Customization GUI v3 supports building of any signal flow with no restriction of the order of occurrence of any audio block, or any limit to repeated deleting or addition during signal flow editing. As audio algorithm blocks are edited, added, or removed, the user-space memory addresses for each register changes.

However, each instance of each block has its own unique label identifier where that identifier is clearly known and visible on the signal flow workspace.

Because each and every algorithm is assigned its own dedicated register for its parameter settings, the D2 Audio Customization GUI v3 generates a variable-to-address mapping for each build of each project. This mapping is provided as a text file in a header file format that can be directly included within a system controller's software build. As multiple iterations of a signal flow are created during the design process, a new header file is created matching each revision. Simply including the header file within the system controller compile automatically passes these new register addresses without need for repeated system code editing.

2. Revision History

Rev.	Date	Description
0.00	May.23.19	Initial release

Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
 - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
 - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.
6. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
11. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.4.0-1 November 2017)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:
www.renesas.com/contact/

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.