To our customers,

---

**Old Company Name in Catalogs and Other Documents**

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

---

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.
Notice

1. All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas Electronics products listed herein, please confirm the latest product information with a Renesas Electronics sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas Electronics such as that disclosed through our website.

2. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.

3. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part.

4. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.

5. When exporting the products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations. You should not use Renesas Electronics products or the technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations.

6. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.

7. Renesas Electronics products are classified according to the following three quality grades: “Standard”, “High Quality”, and “Specific”. The recommended applications for each Renesas Electronics product depends on the product’s quality grade, as indicated below. You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application categorized as “Specific” without the prior written consent of Renesas Electronics. Further, you may not use any Renesas Electronics product for any application for which it is not intended without the prior written consent of Renesas Electronics. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for an application categorized as “Specific” or for which the product is not intended where you have failed to obtain the prior written consent of Renesas Electronics. The quality grade of each Renesas Electronics product is “Standard” unless otherwise expressly specified in a Renesas Electronics data sheets or data books, etc.

   “Standard”: Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots.

   “High Quality”: Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; safety equipment; and medical equipment not specifically designed for life support.

   “Specific”: Aircraft; aerospace equipment; submersible repeaters; nuclear reactor control systems; medical equipment or systems for life support (e.g. artificial life support devices or systems), surgical implantations, or healthcare intervention (e.g. excision, etc.), and any other applications or purposes that pose a direct threat to human life.

8. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.

9. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.

10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.

11. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of Renesas Electronics.

12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

(Note 1) “Renesas Electronics” as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.
1.0 Abstract
In pulse width modulation mode, choose functions from those listed in Table 1. Operations of the circled items are described below.

Table 1. Choosed functions

<table>
<thead>
<tr>
<th>Item</th>
<th>Set-up</th>
</tr>
</thead>
<tbody>
<tr>
<td>Count source</td>
<td>Internal count source (f1 / f8 / f32 / fC32)</td>
</tr>
<tr>
<td>PWM mode</td>
<td>16-bit PWM</td>
</tr>
<tr>
<td></td>
<td>8-bit PWM</td>
</tr>
<tr>
<td>Count start condition</td>
<td>External trigger input (falling edge of input signal to the TAiIN pin)</td>
</tr>
<tr>
<td></td>
<td>External trigger input (rising edge of input signal to the TAiIN pin)</td>
</tr>
<tr>
<td></td>
<td>Timer overflow (TB2/TAj/TAk overflow)</td>
</tr>
</tbody>
</table>

Note: j = i – 1, but j = 4 when i = 0; k = i + 1, but k = 0 when i = 4.

2.0 Introduction
Operation
(1) If the TAiIN pin input level changes from “L” to “H” with the count start flag set to “1”, the counter performs a down count on the count source. Also, the TAiOUT pin outputs an “H” level.
(2) The TAiOUT pin output level changes from “H” to “L” when a set time period elapses. At this time, the timer Ai interrupt request bit goes to “1”.
(3) The counter reloads the content of the reload register every time PWM pulses are output for one cycle, and continues counting.
(4) Setting the count start flag to “0” causes the counter to hold its value and to stop. Also, the TAiOUT outputs an “L” level.

Note: The period of PWM pulses becomes (2^16 – 1)/fi, and the “H” level pulse width becomes n/fi. If the timer Ai register is set to “0000”, the pulse width modulator does not work, and the TAiOUT pin outputs “L” level, therefore the timer Ai interrupt request is not generated.

(f1 : frequency of the count source f1, f8, f32, fC32; n : value of the timer)

Figure 1 shows the operation timing

Figure 1. Operation timing of pulse width modulation mode, 16-bit PWM mode
## 3.0 Set-up procedure

### Selecting PWM mode and functions

<table>
<thead>
<tr>
<th>b7/b6</th>
<th>Count source</th>
<th>Count source period</th>
<th>f(XIN)</th>
<th>f(XCIN)</th>
<th>fC32</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 0</td>
<td>t1</td>
<td>62.5ns</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>0 1</td>
<td>t0</td>
<td>500ns</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 0</td>
<td>t12</td>
<td>2us</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1 1</td>
<td>5/32</td>
<td>976.56us</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

- **Timer Ai mode register (i=0 to 4)** [Address 0396'h to 039A'h]
  - **TAiMR (i=0 to 4)**
  - **External trigger select bit**
    - 1 : Rising edge of TAi pin's input signal (Note 1)
  - **Trigger select bit**
    - 1 : Selected by event/trigger select register
  - **16/8-bit PWM mode select bit**
    - 0 : Functions as a 16-bit pulse width modulator

### Clearing timer Ai interrupt request bit

Refer to 'Precaution for Timer A (pulse width modulation mode)'

- **Timer Ai interrupt control register** [Address 0055'h to 0059'h]
  - **TAiIC (i=0 to 4)**
  - **Interrupt request bit**

### Setting event/trigger select bit

- **One-shot start flag** [Address 0382'h]
  - **ONSF**
- **Timer Ai event/trigger select bit**
  - 0 0 : Input on TAiIN is selected (Note 2)

### Setting PWM pulse’s “H” level width

- **Timer register** [Address 0387'h to 038F'h]
  - **Timer A0** [Address 0387'h to 0388'h]
  - **Timer A1** [Address 0389'h to 038A'h]
  - **Timer A2** [Address 038B'h to 038C'h]
  - **Timer A3** [Address 038D'h to 038E'h]
  - **Timer A4** [Address 038F'h to 038E'h]

### Setting clock prescaler reset flag

(This function is effective when fc32 is selected as the count source. Reset the prescaler for generating fc32 by dividing the XcIN by 32.)

- **Clock prescaler reset flag** [Address 0381'h]
  - **CPSRF**

### Setting count starts flag

- **Count start flag** [Address 0380'h]
  - **TABSR**
  - **Timer A0** count start flag
  - **Timer A1** count start flag
  - **Timer A2** count start flag
  - **Timer A3** count start flag
  - **Timer A4** count start flag

**Start count**
4.0 Programming Code

;******************************************************
; M16C/62A Program Collection
; FILE NAME : rjj05b0039_src.a30
; CPU : M16C/62A Group
; FUNCTION : Operation of Timer A
; (pulse width modulation mode, 16-bit PWM mode)
; HISTORY : 2003.05.16  Ver 1.00
;
; Copyright(C)2003, Renesas Technology Corp.
; Copyright(C)2003, Renesas Solutions Corp.
; All rights reserved.
;******************************************************
;******************************************************************************
; Include
;******************************************************************************
.LIST OFF ;Stops outputting lines to the assembler list file
.INCLUDE sfr62a.inc ;Reads the file that defined SFR
.LIST ON ;Starts outputting lines to the assembler list file
;
;******************************************************************************
; Symbol definition
;******************************************************************************
ROM_TOP .EQU 0F8000H ;Start address of ROM
FIXED_VECT_TOP .EQU 0FFFDCH ;Start address of fixed vector
;
;******************************************************************************
; Program area
;******************************************************************************
;******************************************************************************
; Start up
;******************************************************************************

.SECTION PROGRAM, CODE ;Declares section name and section type
.ORG ROM_TOP ;Declares start address
RESET:

MOV.B #03H, prcr ;Removes protect
;Set processor mode registers 0 and 1
MOV.B #00000000B, pm0 ;Single-chip mode
MOV.B #00000000B, pm1 ;No expansion, No wait
MOV.B #00010000B, cm0 ;Xin-Xout High
MOV.B #00100000B, cm1 ;Xout High, Main clock is No division
MOV.B #00H, prcr ;Protects all registers
Operation of Timer A (pulse width modulation mode, 16-bit PWM mode)

MOV.B #01011111B, ta1mr ; Selecting PWM mode and functions
MOV.B #00000000B, ta1ic ; Clearing timerA1 interrupt request bit
MOV.B #00000000B, trgsr ; Setting event/trigger select bit
BCLR pd7_3 ; (Note) Set the corresponding port direction register to 0
MOV.W #2000, ta1 ; Setting PWM pulse's "H" level width (1msec @16MHz, f8)
MOV.B #00000000B, cpsrf ; Setting clock prescaler reset flag
MOV.B #0000010B, tabsr ; Setting count starts flag

MAIN:
JMP MAIN

Dummy interrupt processing program

Setting of fixed vector

;=============================================================================
;       TimerA (pulse width modulation mode, 16-bit PWM mode selected)
;=============================================================================

MOV.B #00000000B, ta1ic ; Clearing timerA1 interrupt request bit
MOV.B #00000000B, trgsr ; Setting event/trigger select bit
BCLR pd7_3 ; (Note) Set the corresponding port direction register to 0
MOV.W #2000, ta1 ; Setting PWM pulse's "H" level width (1msec @16MHz, f8)
MOV.B #00000000B, cpsrf ; Setting clock prescaler reset flag
MOV.B #0000010B, tabsr ; Setting count starts flag

MAIN:
JMP MAIN

Dummy interrupt processing program

Setting of fixed vector

;=============================================================================
;       Dummy interrupt processing program
;=============================================================================

;=============================================================================
;       Setting of fixed vector
;=============================================================================

; SECTION F_VECT, ROMDATA
; .ORG FIXED_VECT_TOP

; .LWORD dummy ; Undefined instruction interrupt vector
; .LWORD dummy ; Overflow (INT0 instruction) interrupt vector
; .LWORD dummy ; BRK instruction interrupt vector
; .LWORD dummy ; Address match interrupt vector
; .LWORD dummy ; Single-step interrupt vector
; .LWORD dummy ; Watchdog timer interrupt vector
; .LWORD dummy ; DBC interrupt vector
; .LWORD dummy ; NMI interrupt vector
; .LWORD RESET ; Sets reset vector

; .END
5.0 Reference
Renesas Technology Corporation Semiconductor Home page
http://www.renesas.com/

Technical Support
E-mail: support_apl@renesas.com

Data Sheet
M16C/62A group Rev. C.1
(Use the latest version on the Home page: http://www.renesas.com/)

User's Manual
M16C/62A group Rev. 1.0
(Use the latest version on the Home page: http://www.renesas.com/)
Keep safety first in your circuit designs!

- Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

- These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corporation product best suited to the customer’s application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corporation or a third party.
- Renesas Technology Corporation assumes no responsibility for any damage, or infringement of any third-party’s rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
- All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor for the latest product information before purchasing a product listed herein. The information described here may contain technical inaccuracies or typographical errors.
- Renesas Technology Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
- Please also pay attention to information published by Renesas Technology Corporation by various means, including the Renesas Technology Corporation Semiconductor home page (http://www.renesas.com).
- When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
- Renesas Technology Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- The prior written approval of Renesas Technology Corporation is necessary to reprint or reproduce in whole or in part these materials.
- If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination. Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
- Please contact Renesas Technology Corporation for further details on these materials or the products contained therein.