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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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M16C/Tiny Series

Operation of Timer A (Event Counter Mode, Reload Type)

1. Abstract

In event counter mode, choose functions from those listed in Table 1. Operations of the checked items are described below.

Table 1. Chosen Functions

Item	Set-up	
	Count source	Yes
		Input signal to TAI _{IN} (counting rising edges)
		Timer overflow (TB2 overflow /TAj overflow)
Pulse output function	Yes	No pulses output
		Pulses output
Count operation type	Yes	Reload type
		Free-run type
Factor for switching between up and down	Yes	Content of up/down flag
		Input signal to TAI _{OUT}

Note: $j = i - 1$, but $j = 4$ when $i = 0$.

2. Introduction

The explanation of this issue is applied to the following condition:

Applicable MCU: M16C/26, M16C/26A, M16C/28, M16C/29 Group

This program can also be used when operating other microcomputers within the M16C family, provided they have the same SFR (Special Function Registers) as the M16C/26, M16C/26A, M16C/28, M16C/29 microcomputers. However, some functions may have been modified.

Refer to the User's Manual for details. Use functions covered in this Application Note only after careful evaluation.

3. Operation of Timer A

- (1) Setting the count start flag to "1" causes the counter to count the falling edges of the count source.
- (2) If an underflow occurs, the content of the reload register is reloaded, and the count continues. At this time, the timer Ai interrupt request bit goes to "1".
- (3) If switching from an up count to a down count or vice versa while a count is in progress, the switch takes effect from the next effective edge of the count source.
- (4) Setting the count start flag to "0" causes the counter to hold its value and to stop.
- (5) If an overflow occurs, the content of the reload register is reloaded, and the count continues. At this time, the timer Ai interrupt request bit goes to "1".

Figure 1 shows the operation timing of event counter mode, reload type selected.

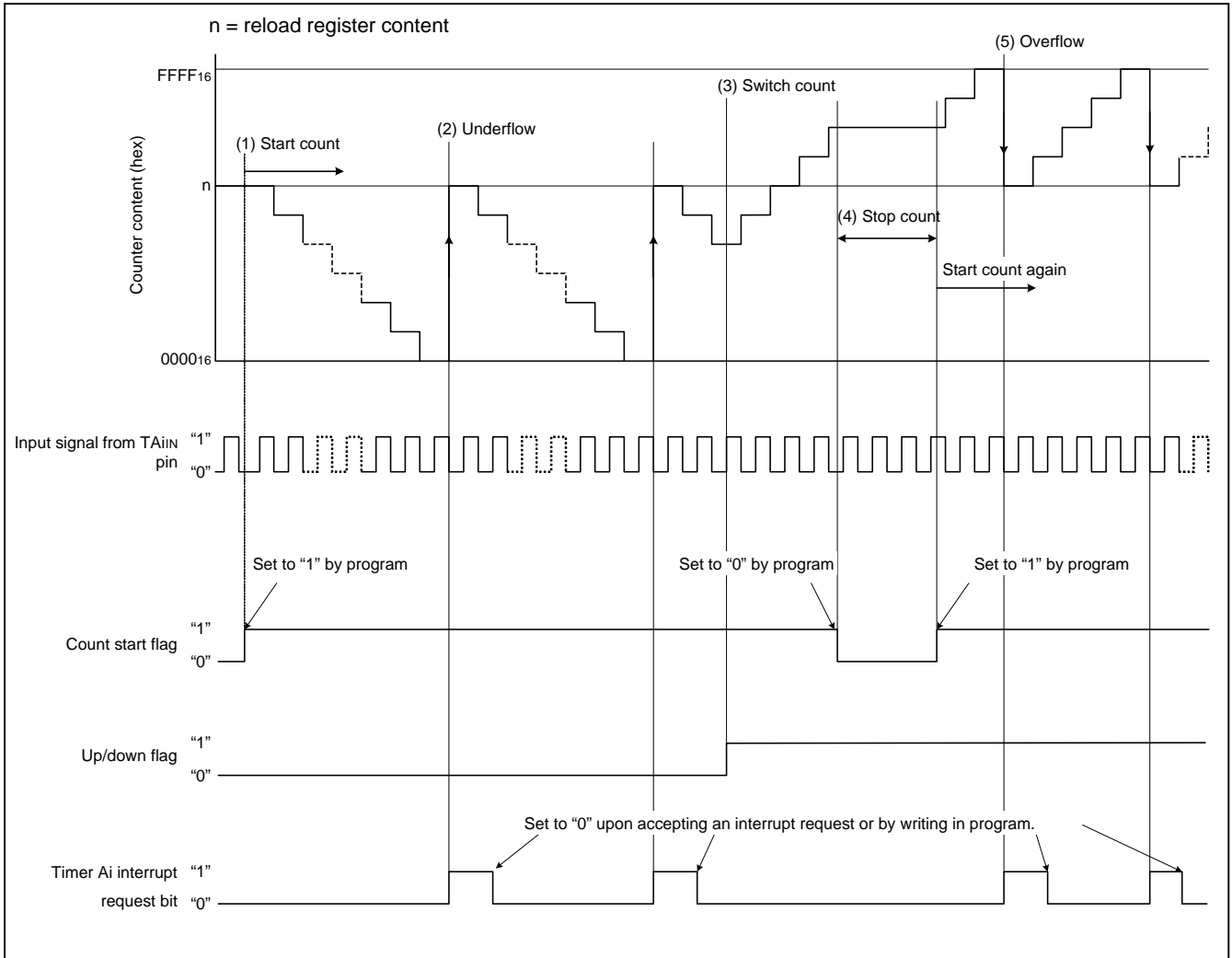
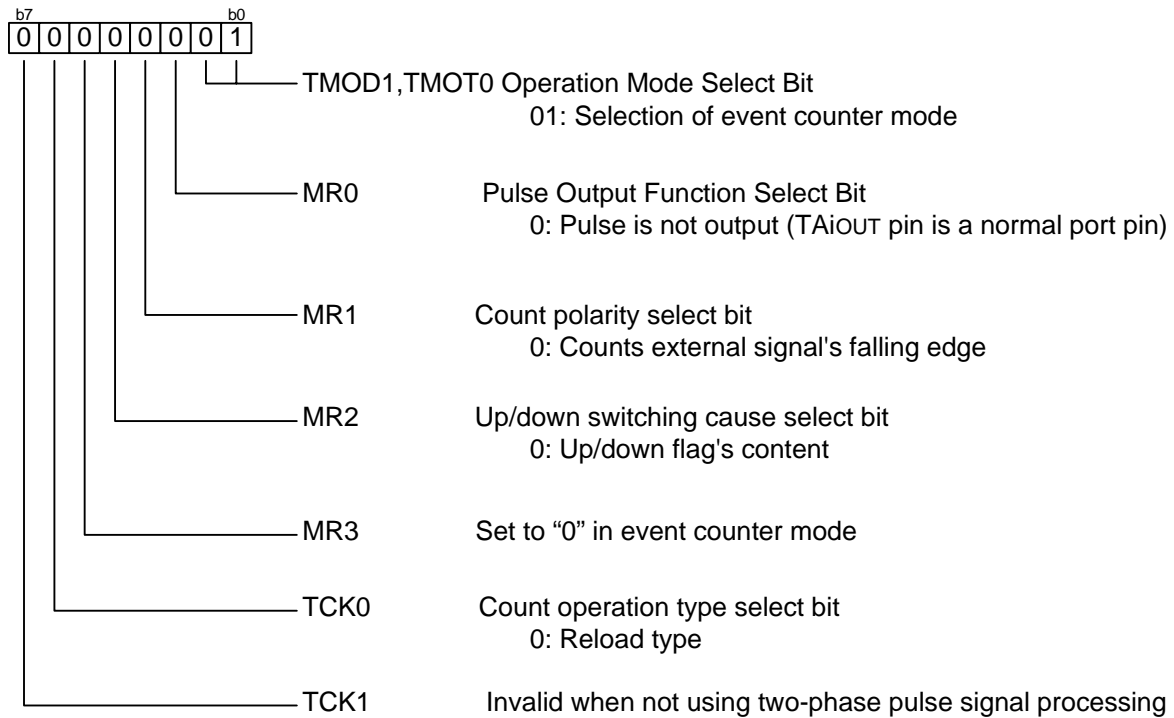


Figure 1. Operation Timing of Event Counter Mode, Reload Type Selected

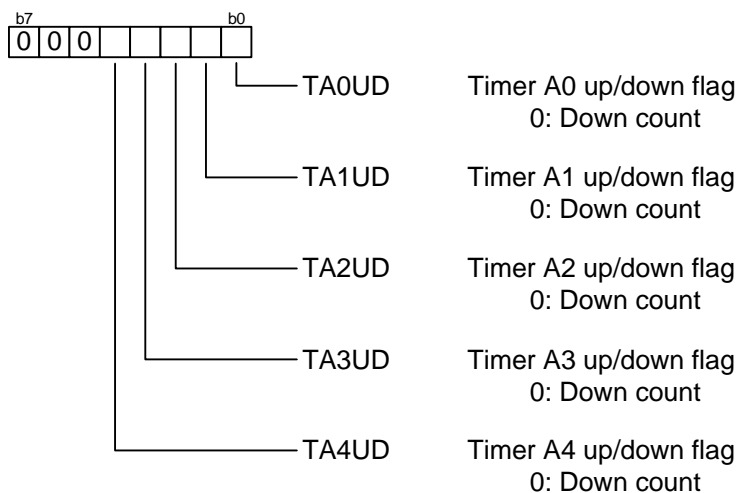
3.1 Register Setting

To enable the operation defined in “Section 3. Operation of timer A”, the following register settings must be taken place step by step. For detail configuration of each register, please refer to M16C/26 Group hardware manual, M16C/26A Group hardware manual, M16C/28 Group hardware manual, M16C/29 Group hardware manual.

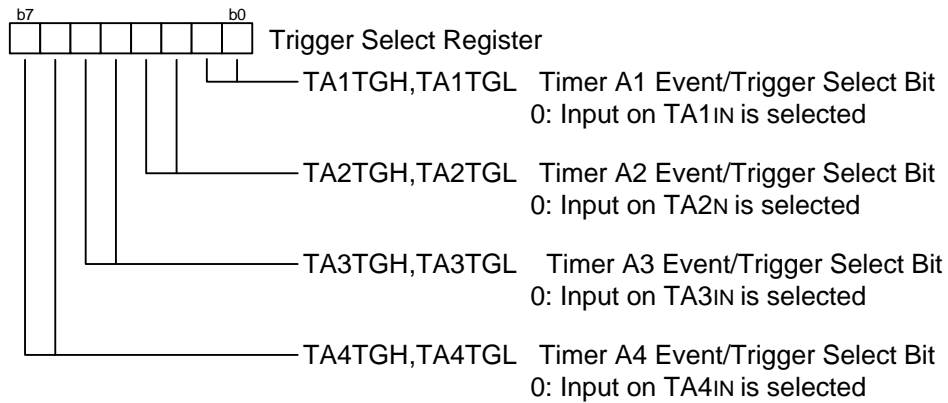
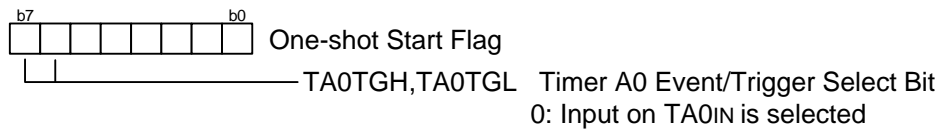
(1) Setting timer Ai mode register (i=0 to 4)



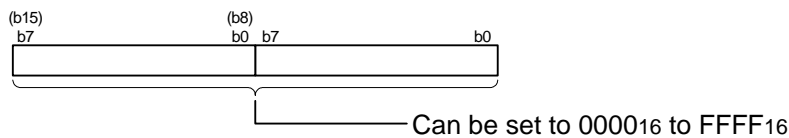
(2) Setting up/down flag



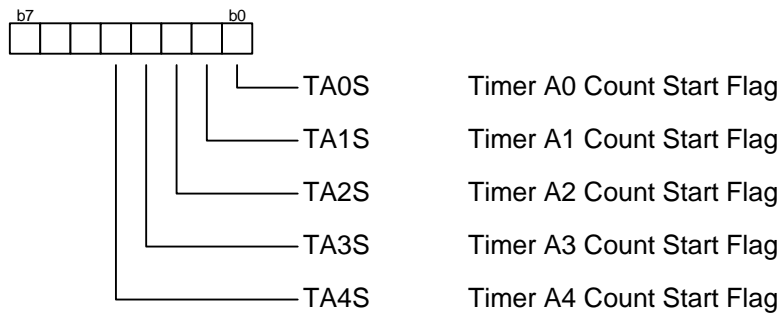
(3) Setting one-shot start flag and trigger select register



(4) Setting timer Ai register (i=0 to 4)



(5) Setting count start flag



4. Sample Program

```

/*****
 *
 * FILE NAME :
 * CPU      : M16C/Tiny series
 * Function  : Operation of Timer A
 *            (Event Counter Mode, Reload Type)
 * Version   : 1.00
 *
 * Copyright (C)2004, Renesas Technology Corp.
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 *
 *****/
/*****
 * include file
 *****/
#include "sfr28.h"

/*****
 * main
 *****/
void main(void) {

    ta0mr = 0x01; /* Selection of event counter mode
                  Pulse output function select bit (0:Pulse is not output)
                  Counts external signal's falling edge
                  Up/down flag's content
                  Count operation type select bit (0:Reload type)
                  */

    udf = 0; /* Setting up/down flag */

    onsf = 0;

    ta0 = 0x7fff; /* Counter value on event counter mode (down count) */

    ta0s = 1; /* TimerA0 count start */

    while (1) {
    }
}

```

5. Reference

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Hardware Manual

M16C/26, M16C/26A, M16C/28, M16C/29 Group Hardware Manual

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REVISION HISTORY

Rev.	Date	Description	
		Page	Summary
1.00	2005.05.20	-	First edition issued

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