To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.
Notice

1. All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas Electronics products listed herein, please confirm the latest product information with a Renesas Electronics sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas Electronics such as that disclosed through our website.

2. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.

3. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part.

4. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.

5. When exporting the products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations. You should not use Renesas Electronics products or the technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations.

6. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.

7. Renesas Electronics products are classified according to the following three quality grades: “Standard”, “High Quality”, and “Specific”. The recommended applications for each Renesas Electronics product depends on the product’s quality grade, as indicated below. You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application categorized as “Specific” without the prior written consent of Renesas Electronics. Further, you may not use any Renesas Electronics product for any application for which it is not intended without the prior written consent of Renesas Electronics. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for an application categorized as “Specific” or for which the product is not intended where you have failed to obtain the prior written consent of Renesas Electronics. The quality grade of each Renesas Electronics product is “Standard” unless otherwise expressly specified in a Renesas Electronics data sheets or data books, etc.

- “Standard”: Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots.
- “High Quality”: Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; safety equipment; and medical equipment not specifically designed for life support.
- “Specific”: Aircraft; aerospace equipment; submersible repeaters; nuclear reactor control systems; medical equipment or systems for life support (e.g. artificial life support devices or systems), surgical implantations, or healthcare intervention (e.g. excision, etc.), and any other applications or purposes that pose a direct threat to human life.

8. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.

9. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.

10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.

11. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of Renesas Electronics.

12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

(Note 1) “Renesas Electronics” as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.
1.0 Abstract
In timer mode, choose functions from those listed in Table 1. Operations of the circled items are described below.

Table 1. Chosen functions

<table>
<thead>
<tr>
<th>Item</th>
<th>Set-up</th>
<th>Item</th>
<th>Set-up</th>
</tr>
</thead>
<tbody>
<tr>
<td>Count source</td>
<td>Input signal to TAIN (counting falling edges)</td>
<td>Pulse output function</td>
<td>No pulses output</td>
</tr>
<tr>
<td></td>
<td>Input signal to TAIN (counting rising edges)</td>
<td>Count operation type</td>
<td>Reload type</td>
</tr>
<tr>
<td></td>
<td>Timer overflow (TB2/TAj overflow)</td>
<td>Factor for switching between up and down</td>
<td>Content of up/down flag</td>
</tr>
</tbody>
</table>

Note: \( j = i - 1 \), but \( j = 4 \) when \( i = 0 \).

2.0 Introduction

Operation

1. Setting the count start flag to “1” causes the counter to count the falling edges of the count source.
2. If an underflow occurs, the content of the reload register is reloaded, and the count continues. At this time, the timer Ai interrupt request bit goes to “1”.
3. If switching from an up count to a down count or vice versa while a count is in progress, the switch takes effect from the next effective edge of the count source.
4. Setting the count start flag to “0” causes the counter to hold its value and to stop.
5. If an overflow occurs, the content of the reload register is reloaded, and the count continues. At this time, the timer Ai interrupt request bit goes to “1”.

Note
- When not using pulse output, do not select TAINOUT output function with the function select register A and B.

Figure 1 shows the operation timing

![Figure 1. Operation timing of event counter mode, reload type](image-url)
3.0 Set-up procedure

Selecting event counter mode and functions

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

Timer Ai mode register (i=0 to 4) [Address 0356 to 035A]
TAiMR (i=0 to 4)

Selection of event counter mode
- Count polarity select bit
- Up/down switching cause select bit
- Up/down flag's content
- (Must always be “0” in event counter mode)
- Count operation type select bit
- Reload type
- When not using the 2-phase pulse signal processing function, set the select bit to “0”.

Setting up/down flag

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

Up/down flag [Address 0344]
UDF

- Timer A0 up/down flag
- Timer A1 up/down flag
- Timer A2 up/down flag
- Timer A3 up/down flag
- Timer A4 up/down flag

When not using the 2-phase pulse signal processing function, set the select bit to “0”.

Setting one-shot start flag and trigger select register

<table>
<thead>
<tr>
<th>bit 7</th>
<th>bit 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

One-shot start flag [Address 0342]
ONSF

- Timer A0 event/trigger select bit
- Timer A1 event/trigger select bit
- Timer A2 event/trigger select bit
- Timer A3 event/trigger select bit
- Timer A4 event/trigger select bit

Note: Set the corresponding function select register A to I/O port and port direction register to “0”.

Setting divide ratio

Can be set to 0000 to FFFF

Setting count start flag

Count start flag [Address 0340]
TABSR

- Timer A0 count start flag
- Timer A1 count start flag
- Timer A2 count start flag
- Timer A3 count start flag
- Timer A4 count start flag

Start count
4.0 Programming Code

;******************************************************************************
; M16C/80 Program Collection
; FILE NAME : rjj05b0125_src.a30
; CPU : M16C/80 Group
; FUNCTION : Operation of Timer A
;               (event counter mode, reload type)
; HISTORY : 2003.06.16  Ver 1.00
; Copyright(C)2003, Renesas Technology Corp.
; Copyright(C)2003, Renesas Solutions Corp.
; All rights reserved.
;******************************************************************************
;******************************************************************************
; Include
;******************************************************************************
.LIST OFF    ;Stops outputting lines to the assembler list file
.INCLUDE sfr80100.inc ;Reads the file that defined SFR
.LIST ON     ;Starts outputting lines to the assembler list file
;******************************************************************************
;******************************************************************************
; Symbol definition
;******************************************************************************
ROM_TOP .EQU OFFC000H ;Start address of ROM
FIXED_VECT_TOP .EQU OFFFDCH ;Start address of fixed vector
C_CNT_TA_EV .EQU (08000H-1H);Counter value on event counter mode (down count)
;******************************************************************************
; Program area
;******************************************************************************
;=============================================================================
; Start up
;=============================================================================
.SECTION PROGRAM, CODE ;Declares section name and section type.
.ORG ROM_TOP ;Declares start address.
RESET:
; Sets Processor mode, System clock and Main clock division
MOV.B #03H, prcr ;Removes protect
MOV.B #10000000B, pm0 ;Single-chip mode
MOV.B #11000000B, pm1 ;Flash memory version
MOV.B #00001000B, cm0 ;Xcin-Xcout High
MOV.B #00100000B, cm1 ;Xin-Xout High
MOV.B #00010010B, mcd ;No division mode
MOV.B #00H, prcr ;Protects all registers
;
Operation of Timer A (event counter mode, reload type)

; Setting event counter mode and functions
MOV.B #00000001B, ta0mr
    ; Selection of event counter mode
    ; This bit is invalid in M16C/80 series
    ; Count polarity select bit
    ; (0: Counts external signal's falling edge)
    ; Up/down switching cause select bit (0: Up/down flag's content)
    ; Must always be "0" in event counter mode
    ; Count operation type select bit (0: Reload type)
    ; When not using the 2-phase pulse signal processing function,
    ; set the select bit to "0"

; Setting up/down flag
MOV.B #00000000B, udf
    ; TimerA0 up/down flag (0: Down count)
    ; When not using the 2-phase pulse signal processing function,
    ; set the select bit to "0"

; Setting one-shot start flag and trigger select register
MOV.B #00000000B, onsf
    ; Timer A0 event/trigger select bit
    ; (00: Input on TA0IN is selected) (Note)
    ; (Note) Set the corresponding function select register A to I/O port
    ; and port direction register to "0"
BCLR pd7_1 ; Port P7 direction register
BCLR ps1_1 ; Port P7 is I/O port

; Setting divide ratio
MOV.W #C_CNT_TA_EV, ta0
; Setting counter start flag
MOV.B #00000001B, tabsr

MAIN:
JMP MAIN

; Dummy interrupt processing program
; Setting of fixed vector

dummy:
REIT

; Setting of fixed vector
.SECTION F_VECT, ROMDATA
.ORG FIXED_VECT_TOP

.LWORD dummy ; Undefined instruction
.LWORD dummy ; Overflow
.LWORD dummy ; BRK instruction execution
.LWORD dummy ; Address match
.LWORD dummy ;
.LWORD dummy ; Watchdog timer
.LWORD dummy ;
.LWORD dummy ; NMI
.LWORD RESET ; Reset

.END
5.0 Reference
Renesas Technology Corporation Semiconductor Home page
http://www.renesas.com/

Technical Support
E-mail: support_apl@renesas.com

Data Sheet
M16C/80 group Rev. E3
(Use the latest version on the Home page: http://www.renesas.com/)

User's Manual
M16C/80 group Rev. B
(Use the latest version on the Home page: http://www.renesas.com/)
Keep safety first in your circuit designs!

- Renesas Technology Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

- These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corporation product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corporation or a third party.
- Renesas Technology Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
- All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor for the latest product information before purchasing a product listed herein. The information described here may contain technical inaccuracies or typographical errors.
  - Renesas Technology Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
  - Please also pay attention to information published by Renesas Technology Corporation by various means, including the Renesas Technology Corporation Semiconductor home page (http://www.renesas.com).
- When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
- Renesas Technology Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corporation or an authorized Renesas Technology Corporation product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or underwater repeater use.
- The prior written approval of Renesas Technology Corporation is necessary to reprint or reproduce in whole or in part these materials.
- If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination. Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
- Please contact Renesas Technology Corporation for further details on these materials or the products contained therein.