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RENESAS

H8/300L

Emulating SCI using I/O Port (portSCI)

Introduction

Multi-channel communications with various external devices may be required in some applications. A simple means of communication is to use the serial port. However due to the limited serial ports available, there may be a need to implement the communication using I/O port.

In this document, an asynchronous communication channel using two I/O lines is implemented. The transmit and receive links have been established with the PC at 9600 bps.

Target Device

SLP - H8/38024

Contents

1. Theory

1.1 Overview

The software for Simulated Serial Communication Interface is written in C for easy portability. The H8/38024 microcontroller is used as the target in this application note.

The UART protocol used for this Application Note is 1 Start bit, 8 Data bits, No Parity bit and 1 Stop bit as shown below.

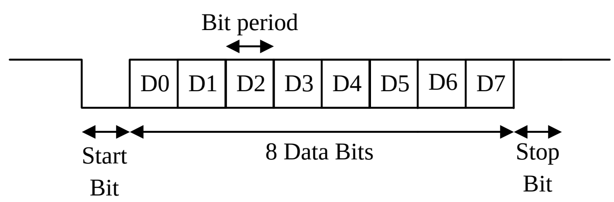

Figure 1 UART protocol

In order to transmit and receive data correctly, the bit period must be accurate. Slight variations would result in accumulation of timing errors and hence data will be decoded wrongly. The bit period is calculated as follows:

Bit period = 1 / Baud Rate

So for a baud rate of 9600bps, the bit period would be:

Bit period = 1 / 9600 = 104.167µs

In order to generate this bit period, either a Timer function or a *for loop* could be used. An accurate value has to be used for the timer register value or the *for loop*.

For transmission, the output port pin is pulled high. A ‘0’ is sent for the start bit followed by 8 data bits, in which the least significant bit (D0) is sent first, and finally a ‘1’ is sent for the stop bit.

The receiving port pin also has to be pulled high. When the signal level goes low, either a start bit is received or unwanted noise causes a drop in voltage level. Hence, a delay of half a bit period is carried out before sampling to verify a ‘0’ for the start bit.

1.2 Implementation

The 38024F CPU Board is used in this application note. Pin 6 of Port 1 (P16) is used as the transmit channel and pin 4 of Port 1 (P14) is used to receive the external serial data. The crystal frequency used is 9.8304Mhz and the baud rate is 9600bps. These can be easily changed to the user's requirement in the C program.

For transmission, P16 is configured as an output with its MOS pulled high. The data is transmitted by calling the *transmit* subroutine.

P14 is configured as an input with its MOS also pulled high. This I/O pin is multiplexed with the IRQ4 activation.

Before receiving any data, P14 is set to accept external interrupt. IRQ4 interrupt is initialized for High-to-Low edge triggering, hence a high-to-low transition at the line after receiving the start bit would generate an interrupt to start IRQ4 interrupt service routine to perform the receive operation.

In the interrupt service routine, P14 is set to function as an input port pin to receive the data stream. The software waits for half a bit period as soon as IRQ4 interrupt occurs to sample the start bit. After detecting the start bit, the *receive* subroutine waits for a bit period to sample each of the 8 data bits. This process is illustrated in Figure 2.

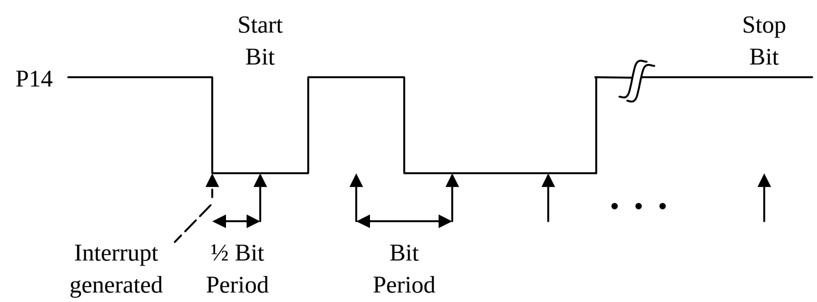

Figure 2 Sampling periods

Timer F is used implement the delay for the bit period. Timer F is initialized as a 16-bit timer and generates an interrupt when a compare match occurs. The calculation for the output compare register value is shown in the following section 1.3.

1.3 Output Compare Register Value Calculation

Timer F is a free running counter with a built-in output compare function. It is initialized to generate an interrupt when a compare match occurs. That is, Timer Control Register F (TCRF) starts incrementing and an interrupt is generated once its value matches that of the value in Output Compare Register FH (OCRFH).

The internal clock is set to Ø/4, by setting bits 2 to 1 of Timer Control Register F (TCRF) as indicated in bold in table 1.

Bit 2 CKSL2	Bit 1 CKSL1	Bit 0 CKSL0	Description
0	0	0	Counting on external event (TMIF) rising/ falling edge
0	0	1	
0	1	0	
0	1	1	Use prohibited
1	0	0	Internal clock: counting on ø/32
1	0	1	Internal clock: counting on ø/16
1	**1**	**0**	**Internal clock: counting on ø/4**
1	1	1	Internal clock: counting on øw/4

Table 1 Clock Selection for Timer F

The output compare register value is calculated as follows:

For: Bit period = 1 / Baud Rate

Ø = Crystal frequency / 2

Internal clock = Ø /4

d = output compare register value

Therefore: d x Internal clock period = Bit period required

$$d \times \frac{1}{\frac{\text{Crystal frequency}}{2 \times 4}} = \frac{1}{\text{Bit period}}$$

$$d = \frac{\text{Crystal frequency}}{8 \times \text{Baud rate}}$$

However, Timer F needs a time of around 25µs before it is initialized and starts incrementing its 16-bit timer counter TCF. This would result in a longer bit period, hence the value needs to be offset. It is found that an offset of 49 is required in this Application Note when a crystal of 9.3204MHz is used.

Therefore, the value to be loaded into OCRFH is d – 49.

Note: This delay for Timer F for initialization is dependent on the crystal value used. User has to change the offset value if a different crystal is used.

1.4 Alternative solution

- Polling for Start bit

Instead of using an interrupt to detect the start bit, the user can use a polling method instead. The user can continuously read the receive pin, P14, to wait for the start bit. An example code is shown below.

```
while(1)
{
   if (RX == 0)
       receive();
}
```

However, this method could only be used when the main program has nothing else to perform as the user needs to continuously check the receive pin.

- Using *for loop* for delay

A *for loop* can be used in place of Timer F to implement the delay required for the bit period. The user would be required to find out the suitable delay value for the *for loop*. An example for such a delay subroutine is shown as follows.

```
void delay (unsigned short d)
{
  for (i = 0; i < d; i ++)
}
```

2. Operation

2.1 Hardware Setup

In order to communicate with an external device, an RS-232 connection has to be setup. A simple serial driver has to be built to condition the signal level between the micon and external target device. The I/O pins have to be pulled HIGH via 10K pull-up resistors.

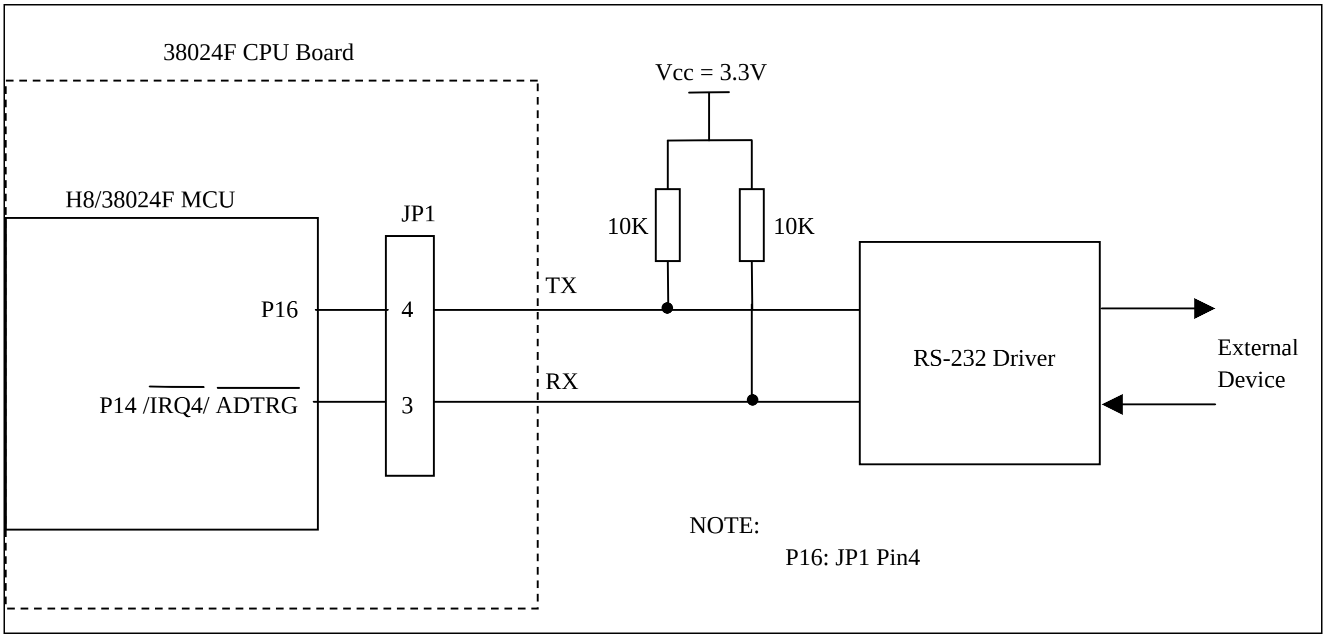

Figure 3 Micon setup with RS-232 driver

A schematic diagram for a RS-232 driver is given in Figure 4.

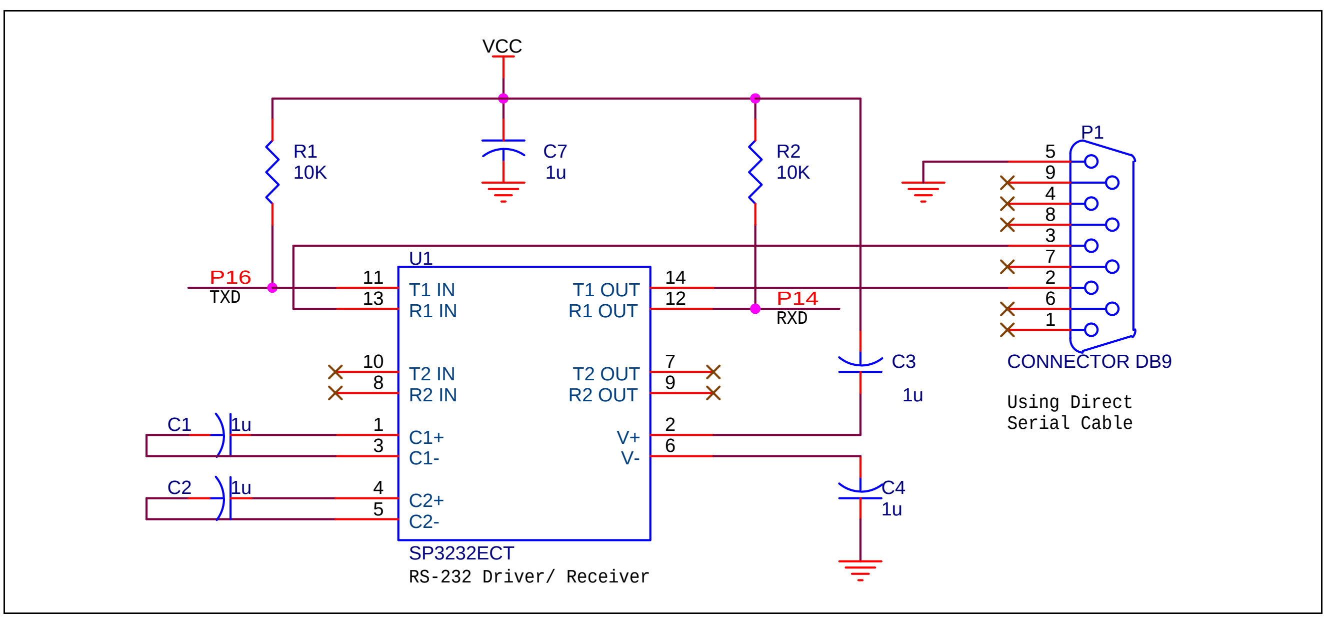

Figure 4 RS-232 driver

2.2 Hyper Terminal Setting

When the user sets up communication between the micon and PC using HyperTerminal, the COM port settings have to be made in accordance with the UART protocol and Baud Rate used in the program.

For a Baud Rate of 9600bps the following settings have to be made to the COM port connected to the RS-232 driver. Select *Properties* in the *File* menu of HyperTerminal window and click on *Configure…* to change the Port Settings.

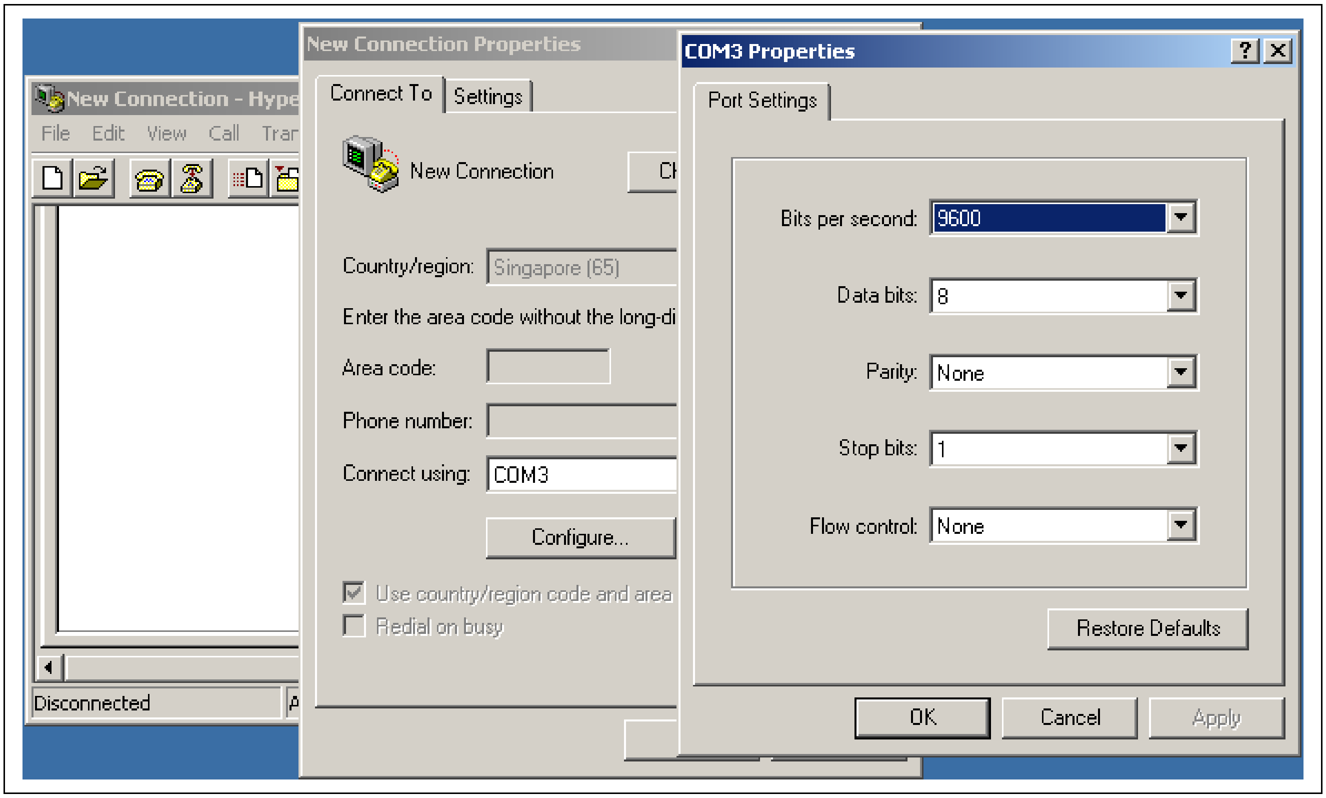

Figure 5 PC HyperTerminal settings

Next, setup the HyperTerminal such that the character keyed in by the user would be shown in the HyperTerminal window. Click on *ASCII Setup…* in the *Settings* tab and click in the check box of *Echo typed characters locally* as shown in Figure 6.

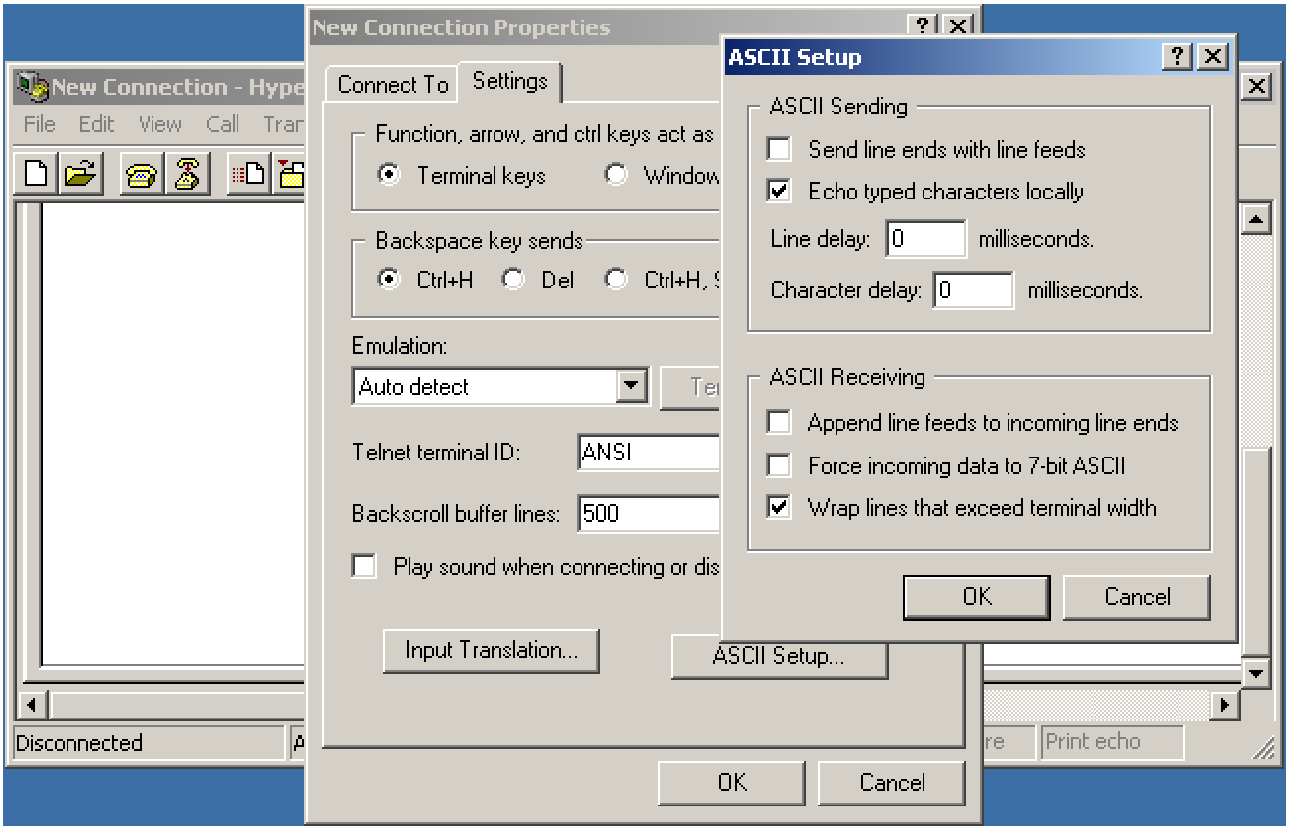

Figure 6 ASCII Setup

By setting the correct baud rate, the user would be able to see the characters **Test** displayed in the HyperTerminal. The user can enter any characters in the hyper terminal and the decoded character would be re- transmitted onto the hyper terminal window for verification. Figure 7 shows an example of the result on the hyper terminal window. If the character is decoded wrongly (when stop bit is not detected), **Er** is transmitted and would be displayed on the Hyper Terminal window.

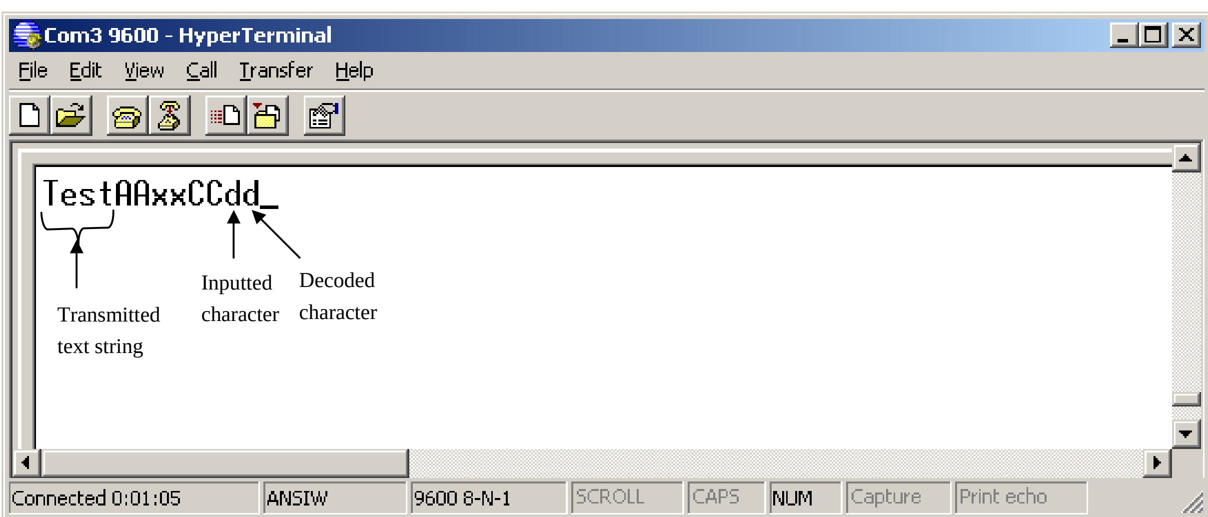

Figure 7 Results displayed on PC HyperTerminal

3. Codes

The following attached code for this Application Note is generated using HEW project generator targeting at H8/38024 micon. The toolchain used is the SLP/TINY toolchain.

Flowcharts are included to illustrate the main functionality and to give a better understanding for the user.

Note: Optimization must be turned off for the program to work

```
/**********************************************************************/
/*                                                                    */
/*  FILE        :Emulate_SCI.c                                           */
/*  DATE        :Mon, Aug 25, 2003                                    */
/*  DESCRIPTION :Main Program                                         */
/*  CPU TYPE    :H8/38024F                                            */
/*                                                                    */
/*  This file is generated by Hitachi Project Generator (Ver.2.1).    */
/*                                                                    */
/**********************************************************************/

//include 38024F IO define header file
#include "iodefine.h"

#include <machine.h>
#include <_h_c_lib.h>

//include additional flag define header file
#include "flagdefine.h"

void initialize (void);
void transmit (unsigned char);
void receive (void);
void transmit_string (void);
void delay (unsigned short);

char *buff_ptr;
static const char TX_buffer[] = "Test";        // Transmit buffer
char RX_buffer;                                // Receive buffer
unsigned int i =0;

//--------------- Main Program ----------------//
void main(void)
{
   initialize();

   transmit_string();

   while(1);
}

//--------- Initialization of Port 1, Timer F & IRQ4 ---------//
void initialize (void)
{
// Initialize Port 1
   P_IO.PUCR1.BIT.PUCR16 = 1;      // P16 MOS pull-up
   TX = 1;
   P_IO.PCR1.BIT.PCR16 = 1;        // P16 as output (TX)

// Initialize IRQ4
   P_SYSCR.IEGR.BIT.IEG4 = 0;      // Interrupt generated at falling edge of
                                   //IRQ4
   P_SYSCR.IENR1.BIT.IEN4 = 1;     // Enables IRQ4
   P_IO.PMR1.BIT.IRQ4 = 1;         // P14 used as IRQ4
```

```
// Initialize Timer F
   P_TMRF.TCRF.BYTE = 0x8E;        // Set TMOFH pin output level to HIGH and
                                   //internal clock of o/4
   P_TMRF.TCSRF.BIT.CCLRH = 1;     //TCF cleared when TCF and OCRF match

}

//--------------- Transmit a character ----------------//

void transmit (unsigned char a)
{
   int i;
   MON_RAM.TX_CHAR.BYTE = a;

// start bit
   TX = 0;
   delay(bit_period);

// 8 data bits
   for (i=0; i<8; i++)
   {
      if (MON_RAM.TX_CHAR.BIT.bit0 == 0)
         TX = 0;

      else
         TX = 1;

      delay(bit_period);
      MON_RAM.TX_CHAR.BYTE = MON_RAM.TX_CHAR.BYTE >> 1;
   }

// stop bit
   TX = 1;
   delay(bit_period);
}

//--------------- Transmit characters in Transmit Buffer ----------------//
void transmit_string (void)
{
   buff_ptr = (char *)&TX_buffer;

   while (  *buff_ptr != 0)
   {
      MON_RAM.TX_CHAR.BYTE = (*buff_ptr++);
      transmit(MON_RAM.TX_CHAR.BYTE);    // call transmit subroutine to tansmit
                                         //each character
   }
}

//--------------- Store characters in RX_CHAR ----------------//
void receive (void)
{
   int j;
```

```
   RX_buffer = 0;

// Receive data bits
   for (j=0; j<8; j++)
   {
      delay(bit_period);
      if (RX == 1)
         MON_RAM.RX_CHAR.BYTE = MON_RAM.RX_CHAR.BYTE | (0x01 << j);

      else
         MON_RAM.RX_CHAR.BYTE = MON_RAM.RX_CHAR.BYTE & rotlc(j,0xFE);
   }

// Receive stop bit
   delay(bit_period);
   if (RX != 1)
   {
      transmit('E');        // error if sampled stop bit='0', transmit 'Er'
      transmit('r');
   }
   else
   {
      RX_buffer = MON_RAM.RX_CHAR.BYTE ;      // save character in receive
                                              //buffer
      transmit(RX_buffer);                    // transmit character in receive
                                              //buffer
   }
   P_IO.PMR1.BIT.IRQ4 = 1;              // P14 used as IRQ4
}

//--------------- Delay function using Timer F ----------------//
void delay (unsigned short d)
{
   d = d-49;          // decrease d to offset for delay during setup of timer

   P_TMRF.OCRF.BYTE.H = d<<8;      // save count (d) in output compare register
   P_TMRF.OCRF.BYTE.L = d;

   P_TMRF.TCSRF.BIT.CMFH = 0;      // Clear compare match flag
   P_TMRF.TCF.BYTE.H = 0;          // Clear counter and start the timer F
   P_TMRF.TCF.BYTE.L = 0;

   while (P_TMRF.TCSRF.BIT.CMFH == 0);   // Loop until a compare match occurs

   P_TMRF.TCSRF.BIT.CMFH = 0;      // Clear compare match flag
}
```

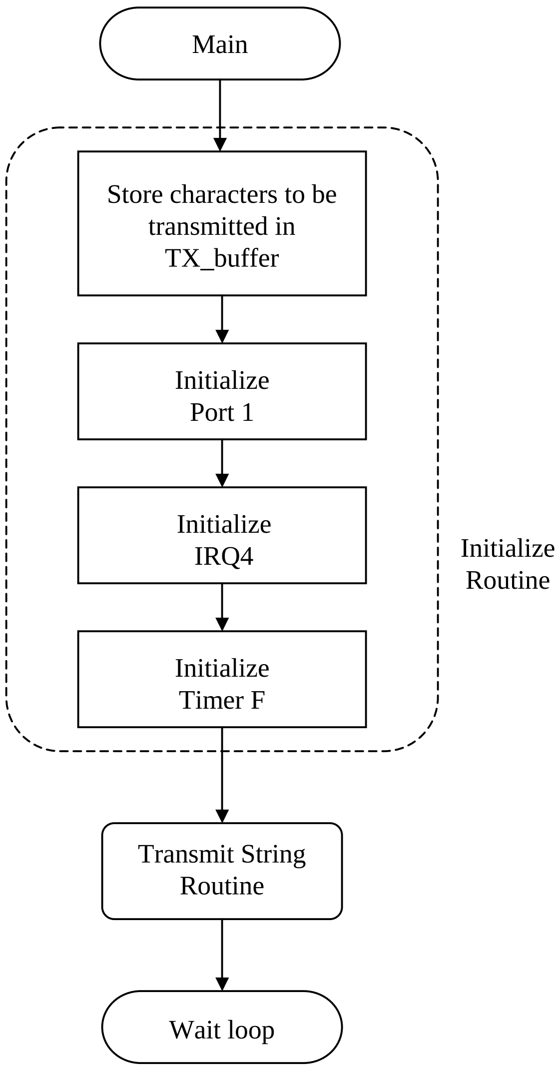

Figure 8 Main Program

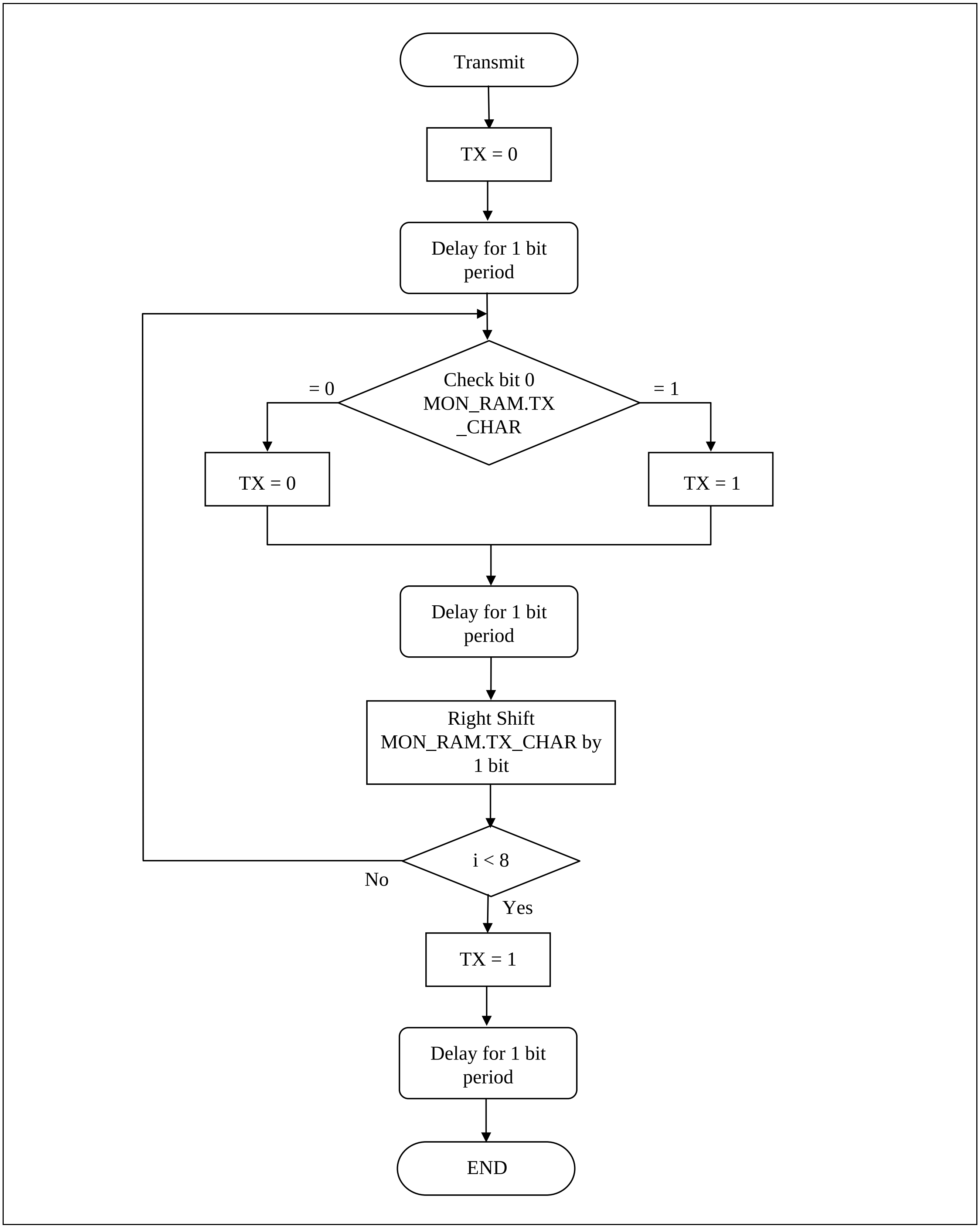

Figure 9 Transmit Routine

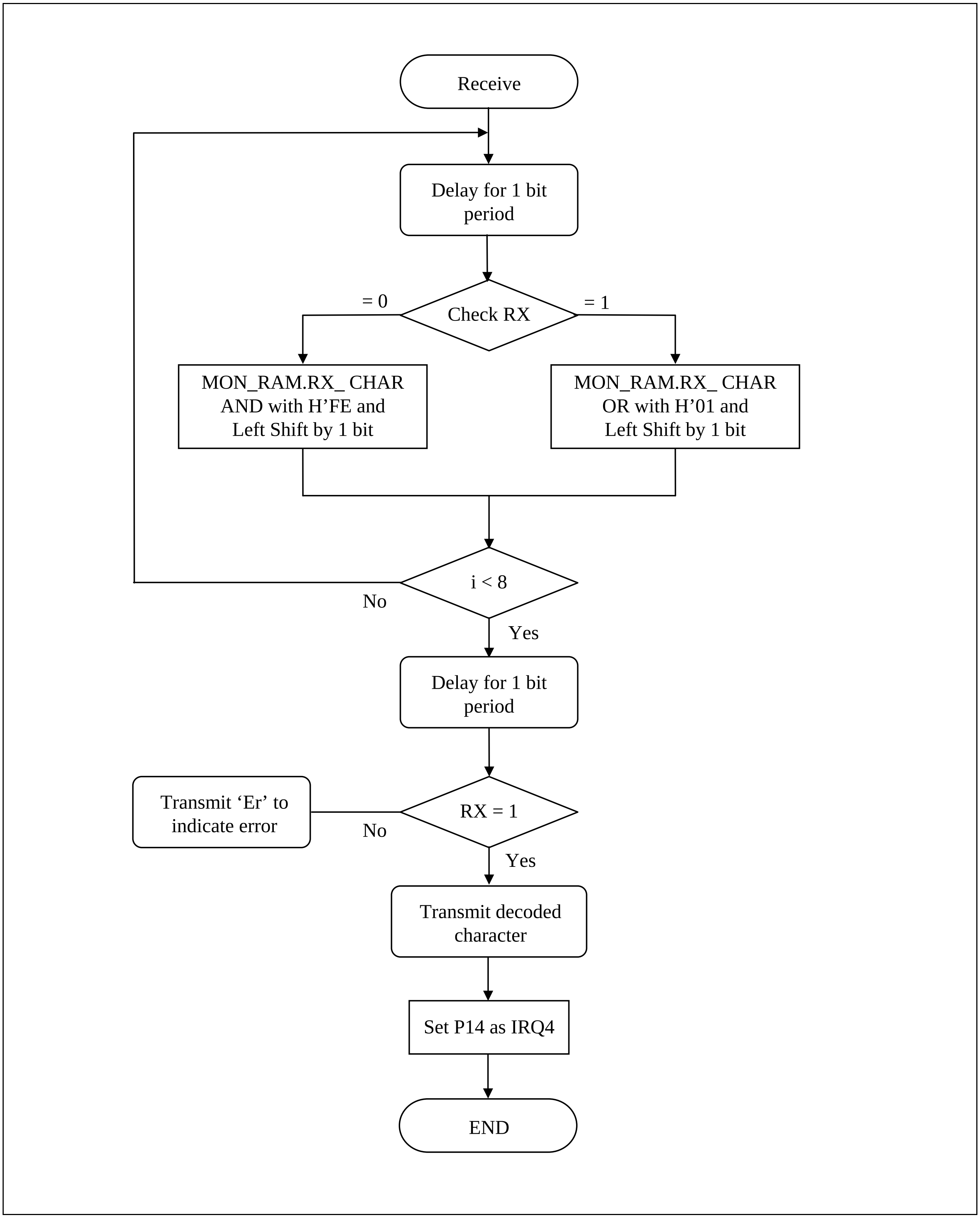

Figure 10 Receive Routine

```
/*********************************************************************/
/*  FILE        :flagdefine.h                                        */
/*  DATE        :Tue, Aug 19, 2003                                   */
/*  DESCRIPTION :Definition of flag                                 */
/*  CPU TYPE    :H8/38024                                            */
/*  Additional header file                                          */
/*                                                                   */
/*********************************************************************/

#define XTAL         9830400L           // for crystal frequency of 9.83204Mhz

#define  BAUD        9600L              // for baud rate of 9600

#define  bit_period  (XTAL / (8*BAUD))  // for internal clk = Ø /4
                                        // NOTE: Ø = XTAL/2
#define  sample      ((bit_period) / 2L)

#define  TX          P_IO.PDR1.BIT.P16  // Port 1 pin 6 as transmit pin
#define  RX          P_IO.PDR1.BIT.P14  // Port 1 pin 4 as receive pin

/************************************************************************/
/*      H8/38024 Flag Definition File                        Ver 1.0      */
/************************************************************************/
struct MON                                                  /*struct MON_RAM*/
{                                                           /*             */
            union {                                         /*             */
                  unsigned char BYTE;                       /*  Byte Access */
                  struct {                                  /*  Bit  Access */
                         unsigned char bit7:1;              /*             */
                         unsigned char bit6:1;              /*             */
                         unsigned char bit5:1;              /*             */
                         unsigned char bit4:1;              /*             */
                         unsigned char bit3:1;              /*             */
                   unsigned char bit2:1;                    /*             */
                   unsigned char bit1:1;                    /*             */
                         unsigned char bit0:1;              /*             */
                         }      BIT;                        /*             */
                  } TX_CHAR;                                /*             */
            union {                                         /*             */
                  unsigned char BYTE;                       /*  Byte Access */
                  struct {                                  /*  Bit  Access */
                         unsigned char bit7:1;              /*             */
                         unsigned char bit6:1;              /*             */
                         unsigned char bit5:1;              /*             */
                         unsigned char bit4:1;              /*             */
                         unsigned char bit3:1;              /*             */
                   unsigned char bit2:1;                    /*             */
                   unsigned char bit1:1;                    /*             */
                         unsigned char bit0:1;              /*             */
                         }      BIT;                        /*             */
                  } RX_CHAR;                                /*             */
};                                                          /*             */
#define MON_RAM    (*(volatile struct MON    *)0xFD20)     /* MON_RAM Addr */
```

```
/***********************************************************************/
/*                                                                     */
/*  FILE        :intprg.c                                              */
/*  DATE        :Mon, Aug 25, 2003                                     */
/*  DESCRIPTION :Interrupt Program                                     */
/*  CPU TYPE    :H8/38024F                                             */
/*                                                                     */
/*  This file is generated by Hitachi Project Generator (Ver.2.1).     */
/*                                                                     */
/***********************************************************************/

// include 38024F IO define header file
#include "iodefine.h"

#include <machine.h>

// include additional flag define  header file
#include "flagdefine.h"

extern void delay (unsigned short);
extern void receive (void);
```

NOTE: Add the following in the IRQ4 vector

```
void INT_IRQ4(void)
{
   set_imask_ccr(1);              // Disable interrupts

   P_IO.PMR1.BIT.IRQ4 = 0;        // P14 used as i/o pin

   P_IO.PCR1.BIT.PCR14 = 0;       // P14 as Input (RX)
   P_IO.PUCR1.BIT.PUCR14 = 1;     // P14 MOS pull-up

// start bit
    delay(sample);           // delay half a bit period to sample at the middle
                             // of each bit

   if (RX == 0)      // Start receive sequence if sampled start bit equals '0'
      receive();

   P_SYSCR.IRR1.BIT.IRRI4 = 0;    // clear interrupt request flag
}
```

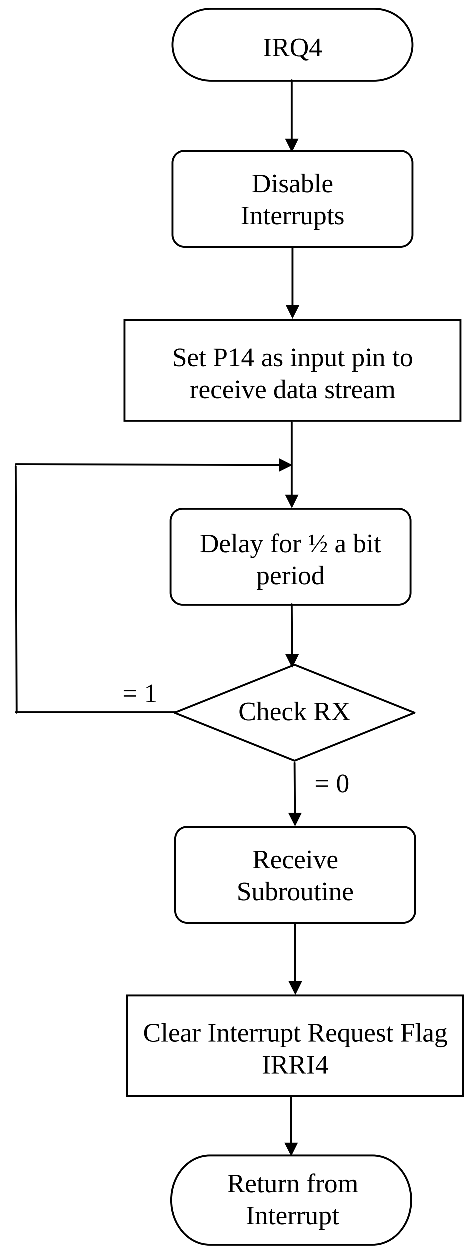

Figure 11 Interrupt Service Routine 4 (IRQ4)

Revision Record

Rev.	Date	Description: Page	Description: Summary
1.00	September.03	—	First edition issued