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## **32176 Group**

### **Combination of DMAC and Serial Interface Reception**

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#### **1. Overview**

The reference sample program combined DMAC and serial interface reception for 32176 group appears on this document.

#### **2. Introduction**

These application examples in this document are used in the following microcomputers and conditions.

- Microcomputer: 32176 Group (M32176FnVFP, M32176FnTFP)
- Operating frequency: 20 to 40 MHz (The sample program is compiled assuming a frequency of 40 MHz.)
- Operating Board: Starter kit for 32176 Group

### 3. Sample program for Combination of DMAC and serial interface reception

#### 3.1 Outline of Sample program

In this sample program, DMA4 is started by receiving serial interface0, received data is transferred on RAM. However DMAC transfer counter can not transfer more than 256 times with 8 bit, so in case more than 256 bit data is need to be received automatically DMA is had to be restarted. (by cascade connection of DMA channel or some way.)

All processing above are operated without software load.

During program operation, counter value of DMA4 is outputted to port 11.

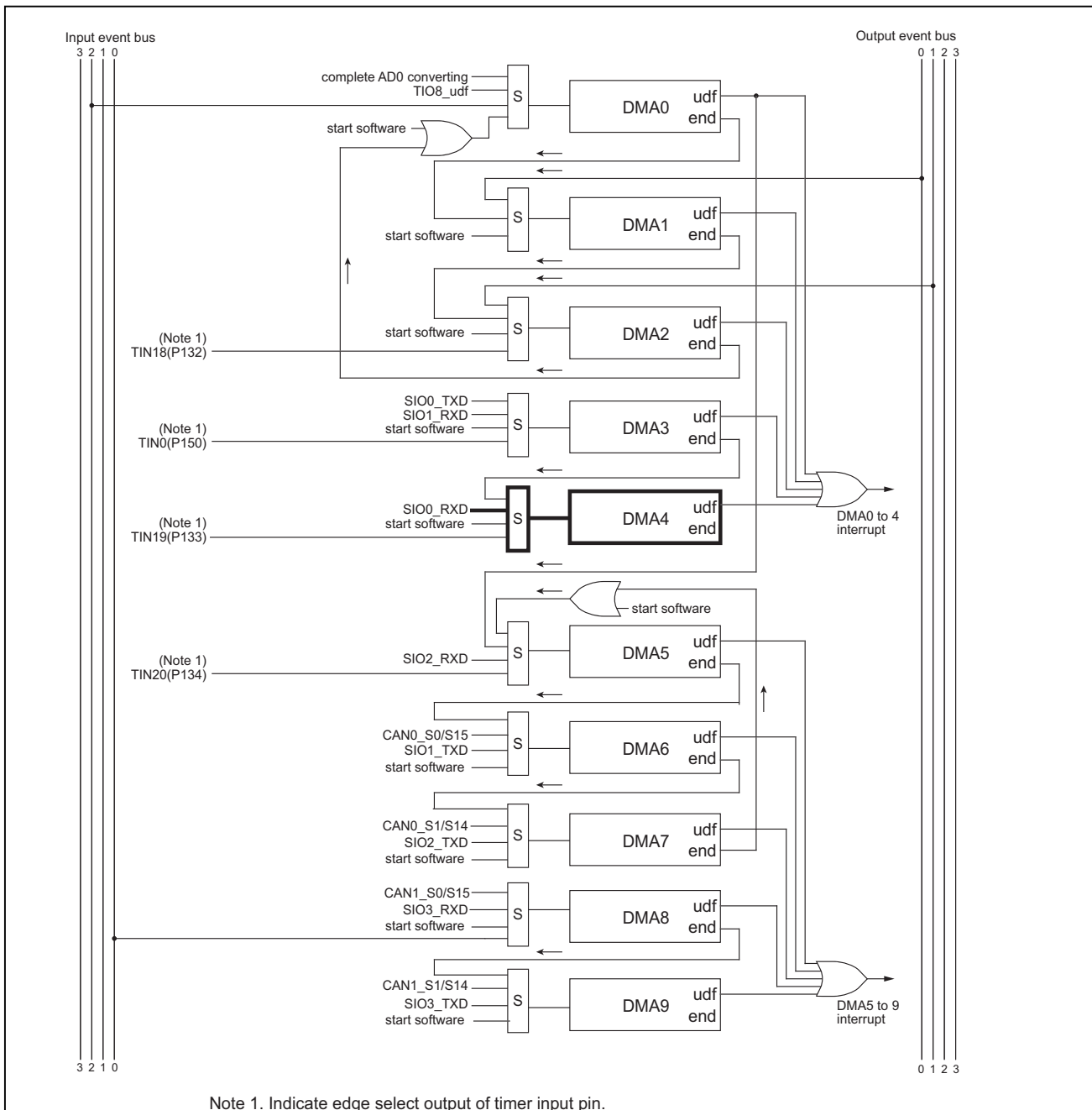


Figure3.1.1 Configuration of Sample for Combination of serial interface reception and DMAC

## 3.2 Description of a reference program

Note: The registers used are indicated as (register name: bit name).

### 3.2.1 Various initialization functions (init\_func())

- (1) Call the port initialization function
- (2) Call the DMAC initialization function
- (3) Call the serial interface initialization function

### 3.2.2 Port initialization functions (port\_init())

- (1) Initial setting of input and output port
  - Set Port Input Permit bit of Port Input Special Function Control Register as permit inputting (PICNT: PIEN0)
  - Initialize P11 Data register (P11DATA)
  - Set P11 Direction Register as output mode. (P11DIR)
  - Set P11 Operation Mode Register as input/output port. (P11MOD)

Note: If a Direction Register is set as output before setting up a Data Register, an unfixed value is outputted until writing will be performed to a Data Register.

### 3.2.3 DMAC initializing Function (dma\_init())

- (1) Interrupt Setting of DMA4
  - Clear DMA4 interrupt request flag disable. (DM04ITST: DMITST4)
  - Set DMA4 interrupt disable. (DM04ITMK: DMITMK4)
- (2) Address Setting of DMA4
  - Set SIO0 receive buffer address to DMA4 source address. (DM4SA )
  - Set internal RAM address to DMA4 destination address. (DM4DA )
  - Set 256 times for numbers of transfer times. (DM4TCT )
- (3) Set DMA4 channel control register (DM0CNT)
  - Set normal mode to DMA4 transfer mode.
  - Clear DMA4 transmit request flag.
  - Set SIO0 to DMA4 transfer request factor.
  - Set DMA4 transfer as enable.
  - Set DMA4 transfer size to 8 bit.
  - Set address increment to DMA4 source address.
  - Set address increment to DMA4 destination address.

### 3.2.4 Serial interface initializing Function (sio0rcv\_init())

- (1) Setting transfer control (S0RCNT, S0TCNT: CDIV, TEN)
  - Set transmission paused.
  - Set baud rate generator count source to f(BCLK)
  - Set reception paused.
- (2) Set port
  - Set port P82 to TXD0, port P83 to RXD0.
- (3) Set transfer data format (S0MOD, S0BAUR)
  - Set to 8-bit UART.
  - Set 1 stop bit to stop length.
  - Set to no parity.
  - Set baud rate as 19200bps. (At 19200bps when CPU clock is 40 MHz.)
- (4) Interrupt setting
  - Set as SIO0 transmit interrupt disable. (ISIO0RXCR: ILEVEL)
  - Set as SIO0 receive complete interrupt. (SI03SEL: ISR0)
  - Set as SIO0 transmit interrupt request disable. (SI03MASK: ROMASK)
- (5) Start reception
  - Set SIO0 to reception enable (S0RCNT: REN)

### 3.2.5 Main Function (main())

- (1) Call the interrupt prohibitive function
- (2) Call the various initialization functions
- (3) Call the interrupt permit function
- (4) Infinite loop outputs DMA transfer counter value to port P11.

### 3.3 Reference sample programs

The sample program for combination of DMAC and serial interface reception is shown below.

Note that the sample program below requires the SFR definition file. The latest SFR definition file can be downloaded from Renesas Technology website. When using the SFR definitions file, adjust the path setting to match the operating computer environment.

#### 3.3.1 dma\_sio\_rcv.c

```

1  /* FILE COMMENT *****
2  *      M32R C Programming          Rev. 1.01
3  *      < Sample Program for 32176 >
4  *      < Serial I/O (UART) DMAC Receive >
5  *
6  *      Copyright (c) 2004 Renesas Technology Corporation
7  *      All Rights Reserved
8  *****/
9  /*****/
10 /*      Include file      */
11 /*****/
12
13 #include          "..\inc\sfr32176_pragma.h"
14
15 /*****/
16 /*      Definition of external reference      */
17 /*****/
18
19 extern void      DisInt( void );          /* Interrupt disable function */
20 extern void      EnInt( void );          /* Interrupt enable function */
21
22 /*****/
23 /*      Function prototype declaration      */
24 /*****/
25
26 void      main(void);          /* Main function */
27 void      init_func(void);     /* Initial setup function */
28 void      port_init(void);     /* Initialize port */
29 void      dma_init(void);      /* Initialize DMA */
30 void      sio0rcv_init( void); /* Initial setup serial I/O */
31
32 /*****/
33 /*      Define macro      */
34 /*****/
35
36 /* Setting serial IO */
37
38 /* 0123 4567          */
39 #define P8MOD_SCI0    0x30u    /* 0011 0000B P8 operation mode register */
40 /* |||| |||+--- P87          */
41 /* |||| ||+---- P86          */
42 /* |||| |+----- P85          */
43 /* |||| +----- P84          */
44 /* |||+----- RXD0          */
45 /* ||+----- TXD0          */
46 /* ++----- don't care          */
47
48 /* 0123 4567          */
49 #define SnTCNT_INI    0x00    /* 0000 0000B SIOn transmit control register */
50 /* |||| |||+--- Disable transmission */
51 /* |||| +++----- don't care          */
52 /* ||+----- f(BCLK)          */
53 /* ++----- don't care          */
54
55 /* 0123 4567          */
56 #define SnMOD_INI     0x20    /* 0010 0000B SIOn mode register */
57 /* |||| |||+--- Sleep function disabled */
58 /* |||| ||+---- Parity inhibited          */
59 /* |||| |+----- don't care(odd)          */
60 /* |||| +----- 1 stop bit          */
61 /* |||+----- Internal clock          */
62 /* +++----- 8-bit UART          */
63

```

## (Combination of DMAC and Serial Interface Reception)

```

64 /* Setting baud rate (Be sure to check actually set value when using) */
65
66 #define XIN          10                /* 10MHz */
67 #define BAUD_19200  (XIN * 2000000 / 16 / 19200 - 1) /* 19200bps */
68
69 /* DMAC setting data */
70
71                                     /* 0123 4567
*/
72 #define DMA4_INIT    0x2d             /* 0010 1101B  DMA4 channel control register */
73                                     /* |||| |||+--- Destination address incremented */
74                                     /* |||| ||+---- Source address fixed */
75                                     /* |||| |+----- Transfer size of 8-bit */
76                                     /* |||| +----- Transfer enabled */
77                                     /* ||+----- 10: Start upon completion of serial I/O
0 reception */
78                                     /* |+----- No transfer request */
79                                     /* +----- Normal mode */
80
81 /*****
82 /*          Global variable          */
83 /*****
84
85         UCHAR   RcvBuf0[256];        /* Receive buffer */
86
87 /*****FUNC COMMENT*****/
88 * Function name : init_func()
89 *-----
90 * Description   : Initialize ICU
91 *-----
92 * Argument     : -
93 *-----
94 * Returns      : -
95 *-----
96 * Notes        :
97 /*****FUNC COMMENT END*****/
98 void init_func(void)
99 {
100     port_init();                    /* Initialize those related to port */
101     dma_init();                     /* Initialize DMA */
102     sio0rcv_init();                /* Initial setup serial I/O */
103 }
104
105 /*****FUNC COMMENT*****/
106 * Function name :port_init()
107 *-----
108 * Description   :Initialize port
109 *-----
110 * Argument     : -
111 *-----
112 * Returns      : -
113 *-----
114 * Notes        :
115 /*****FUNC COMMENT END*****/
116 void port_init(void)
117 {
118     PICNT = PIEN0;                  /* Enable port input */
119
120 /*** LED output port ***/
121
122     P11DATA = 0x00;                 /* Output data (must be set prior to
mode) */
123     P11DIR = 0xff;                  /* P110-P117 : Output mode */
124     P11MOD = 0x00;                  /* P110-P117 : Input/output port */
125 }
126
127 /*****FUNC COMMENT*****/
128 * Function name : dma_init()
129 *-----
130 * Description   : - Initialize DMAC
131 *               : DMA4: Transfer received data of the SIO0 receive
132 *               : buffer to the internal RAM
133 *-----
134 * Argument     : -
135 *-----
136 * Returns      : -

```



```

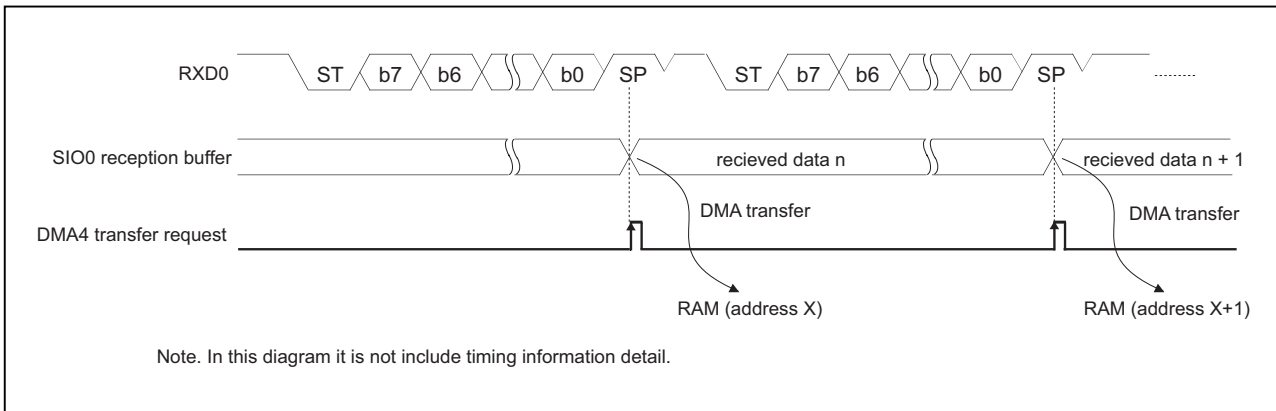
137 *-----
138 * Notes      : Restart is required if the transfers size is 256 bytes or more
139 *""FUNC COMMENT END""*****
140 void dma_init(void)
141 {
142 /** DMA4 initial setting ***/
143
144     DM04ITST = (~DMITST4) & 0xFFu;          /* Clear DMA4 interrupt request */
145     DM04ITMK |= DMITMK4;                    /* Inhibit DMA4 interrupt */
146
147     DM4SA = (USHORT)&SORXB_L;                /* Source address -> SIO0 receive
buffer */
148     DM4DA = (USHORT)&RcvBuf0;                /* Destination address -> RAM buffer */
149
150     DM4TCT = 0xff;                           /* Number of DMA4 transfer (256 times)
*/
151     DM4CNT = DMA4_INIT;                       /* Start upon the completion of the
serial I/O0 reception, enable transfer */
152 }
153
154 /""FUNC COMMENT""*****
155 * Function name : sio0rcv_init()
156 *-----
157 * Description  : Set 8-bit UART reception for SIO0
158 *-----
159 * Argument    : -
160 *-----
161 * Returns     : -
162 *-----
163 * Notes       : Port input function must be enabled
164 *             : No transmission setting has done
165 *             : For M32R/E#1,2,3, PnMOD cannot be accessed for R/M/W
166 *             : The function must be executed while interrupt is inhibited
167 *""FUNC COMMENT END""*****
168 void sio0rcv_init( void)
169 {
170 /** Setting transfer mode */
171
172     SORCNT = 0x00;                            /* Disable reception */
173     SOTCNT = SnTCNT_INI;                       /* f(BCLK) and disable transmission */
174     P8MOD |= P8MOD_SCI0;                       /* Set P8 for SCIO mode */
175     S0MOD = SnMOD_INI;                          /* Set data format */
176     S0BAUR = BAUD_19200;                       /* Set baud rate */
177
178 /** interrupt related settings ***/
179
180     ISIO0RXCR = 7;                             /* Set SIO0 receive interrupt priority
level */
181     SIO3SEL &= ~ISR0;                          /* Select receive-finished interrupt */
182     SIO3MASK &= ~ROMASK;                       /* Enable SIO0 receive interrupt
request */
183
184 /** Starting reception ***/
185
186     SORCNT |= REN;                             /* Enable reception */
187 }
188
189 /""FUNC COMMENT""*****
190 * Function name : main()
191 *-----
192 * Description  : Serial receive data is stored in RAM by DMA transfer
193 *             : - Start DMA4 upon reception of serial I/O0
194 *             : - Receive data is stored in buffer by DMA
195 *             : - Output DMA4's transfer counter
196 *             : (number of receive bytes) to LED (PORT11)
197 *-----
198 * Argument    : -
199 *-----
200 * Returns     : -
201 *-----
202 * Notes       : Interrupt is not actually used
203 *             : LED display is (255-number of receive bytes)
204 *""FUNC COMMENT END""*****
205 void main(void)
206 {
207 /** Initialize microcomputer ***/

```

```
208
209     DisInt();                               /* Disable interrupt */
210
211     init_func();
212
213     EnInt();                                 /* Enable interrupt */
214
215     while(1) {
216         P11DATA = DM4TCT;
217     }
218 }
```

### 3.4 Timing of operation

Timing of operation in this reference program is shown below.



**Figure3.4.1 Timing Diagram for combination sample of serial interface reception and DMAC**

#### 4. Reference Documents

- 32176 Group User's Manual (Rev.1.01)
- M32R Family Software Manual (Rev.1.20)
- M3T-CC32R V.4.30 User's Manual (Compiler)
- M3T-CC32R V.4.30 User's Manual (Assembler)

(Please get the latest one from Renesas Technology Corp. website.)

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		Page	Summary
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