

Application Note

Ultrasonic Qualitative Distance Estimation Sensor

AN-CM-330

Abstract

This application note describes how to create an ultrasonic sensor using [HVPAK](#).

The application note comes complete with design files which can be found in the Reference section.

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1 Terms and Definitions

AC	Alternating current
CMP	Comparator
CNT/DLY	Counter / Delay
DC	Direct current
DFF	D Flip-Flop
GPO	General purpose output
HV	High voltage
LUT	Look-Up Table
OSC	Oscillator
PWM	Pulse width modulation

2 References

For related documents and software, please visit:

<https://www.dialog-semiconductor.com/configurable-mixed-signal>.

Download our free [GreenPAK Designer](#) software [1] to open the .gp files [2] and view the proposed circuit design. Use the [GreenPAK development tools](#) [3] to freeze the design into your own customized IC in a matter of minutes. Find out more in a complete library of application notes [4] featuring design examples as well as explanations of features and blocks within the GreenPAK IC.

- [1] [GreenPAK Designer Software](#), Software Download and User Guide
- [2] [AN-CM-330 Ultrasonic Qualitative Distance Estimation Sensor.gp](#), GreenPAK Design File
- [3] [GreenPAK Development Tools](#), GreenPAK Development Tools Webpage
- [4] [GreenPAK Application Notes](#), GreenPAK Application Notes Webpage
- [5] [SLG47105 Datasheet](#)

Ultrasonic Qualitative Distance Estimation Sensor

3 Introduction

The main goal of this application is to design an ultrasonic qualitative distance estimation sensor using the SLG47105V. The system is designed using the HV macrocells and other internal and external components within the GreenPAK to interact with an ultrasonic sensor.

The applied methodology is based on controlling the duration of the output signal depending on the pulse reflection time. An ultrasonic pulse with the necessary width is generated by the transmitter. When this pulse reaches an obstacle, the reflected wave – echo comes back to the receiver. Then, it is possible to classify the returning echo signal using PWM.

This design can be used in a wide variety of applications, such as devices for visually impaired people, parking assist systems, robotics, warning systems, and others.

4 Interface with Ultrasonic Trigger and Receiver

In this designed system the transmitter sends a trigger pulse to the ultrasonic receiver every 31.25 ms. This signal is amplified by the HV part. The GreenPAK internal components, together with the ACMP, external components with the simplest amplifier stage on a bipolar transistor amplify the signal obtained by the receiver.

The HV part is necessary for increasing the amplitude of the trigger signal. ACMP and external parts with the simplest amplifier stage on a bipolar transistor are used to amplify the receiver signal. As the system keeps measuring every 31.25 ms it becomes easy to see the difference in the distances measured with the sensor.

4.1 Ultrasonic Distance Sensor

The transmitter and receiver are used from the HC-SR04 sensor. They are illustrated with the following [Figure 1](#).



Figure 1 Ultrasonic Transmitter and Receiver

The transmitter is connected to the GreenPAK, which generates the necessary trigger signal (~10 μ s one-shot with frequency 32 Hz). The receiver catches the reflected wave signal, then the PWM output signal is generated as a way of presenting the information of the measured distance.

The block diagram is shown in the [Figure 2](#).

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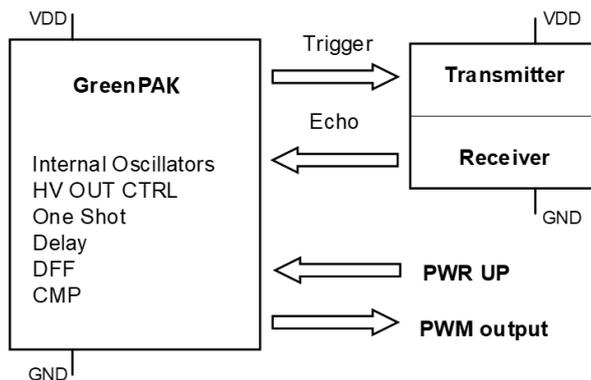


Figure 2 Block Diagram with GreenPAK

The input trigger is generated using One shot and HV OUT CTRL blocks to amplify the amplitude of the signal. The echo signal is filtered and amplified by GreenPAK ACMP and some external components. Then, using the Inverter, Delay, and DFF blocks, the PWM output signal is created. The PWM “HIGH” corresponds to the distance to the detected object. The longer the “HIGH” - the closer the object. As a result, this PWM signal can be used for different methods of notification such as driving motors, LEDs, quantitative distance estimation, and others.

4.2 Circuit Design

There are 4 main blocks in this design: Transmitter, Charge Pump, Receiver, and PWM. To reduce the power consumption of the circuit, all blocks are connected to the “PWR UP” (when PIN 16 is HIGH).

For these blocks, two oscillators with the following properties are used (Figure 3 and Figure 4).

Properties	
OSC0	
Control pin mode:	Force on
OSC power mode:	Force Power On
Clock selector:	OSC
'OSC0' frequency:	2.048 kHz
'CLK' predivider by:	8
'OUT0' second divider by:	8
'OUT1' second divider by:	1

Figure 3 OSC0 Configuration

This oscillator generates a frequency of 2.048 kHz/8/8 (32 Hz).

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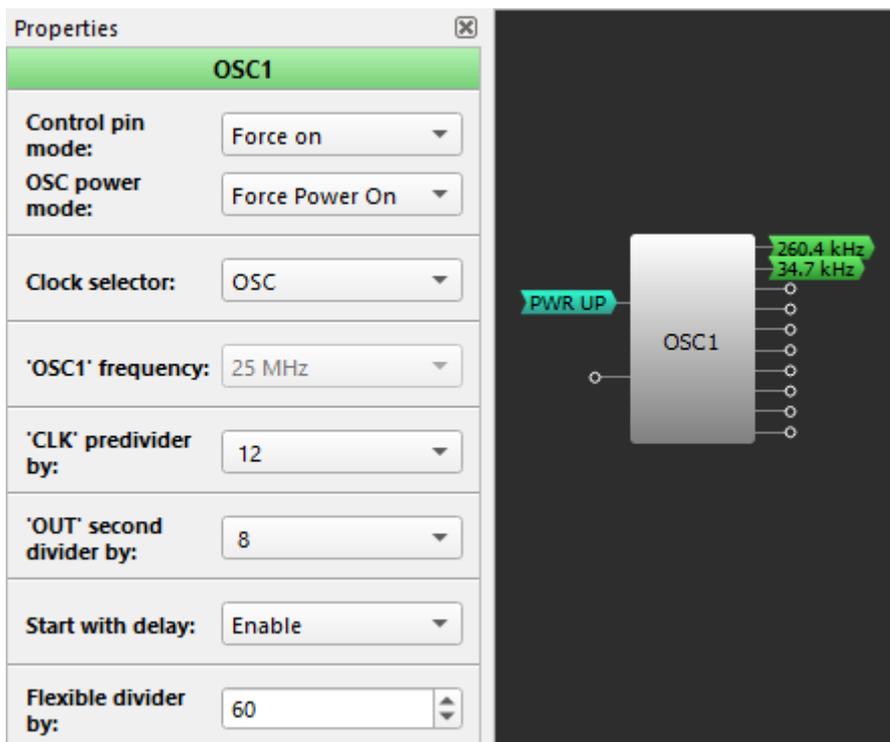


Figure 4 OSC1 Configuration

This oscillator generates two necessary frequencies – 25 MHz/12/8 (~260.4 kHz) and 25 MHz/12/60 (~34.7 kHz).

4.2.1 Transmitter

This block is used to create the trigger signal for the transmitter. The circuit diagram is shown in the Figure 5.

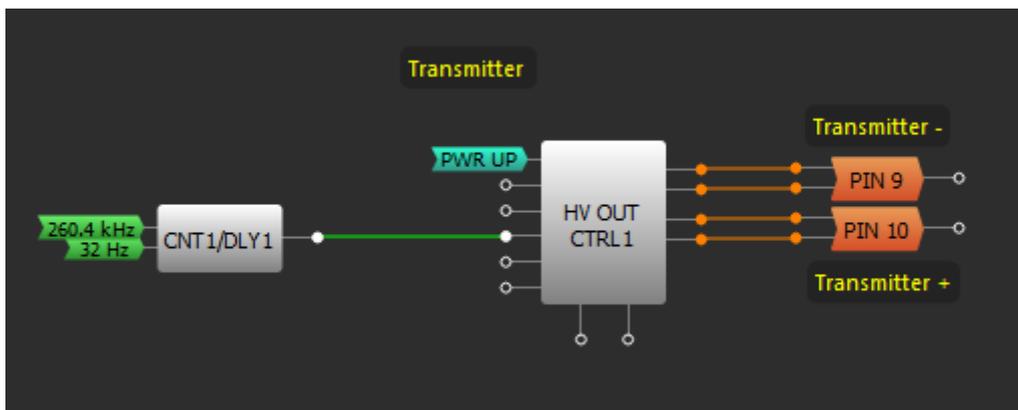


Figure 5 Circuit Diagram of Transmitter Block

The CNT1/DLY1 block is configured as a One shot. This block uses a clock of 260.4 kHz to generate a ~10 us pulse trigger signal with a frequency of 32 Hz (Figure 6). This signal goes to the HV OUT

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CTRL1 block which together with the Charge Pump block amplifies the amplitude to ~ 9 V. The PIN 9 and PIN 10 serve as outputs for the transmitter.

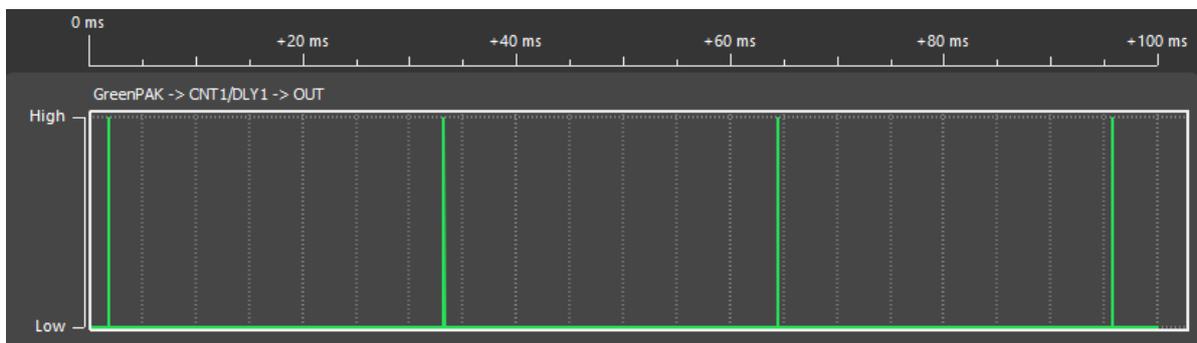


Figure 6 Trigger Signal

4.2.2 Charge Pump

This block is used for amplifying the trigger signal amplitude. The circuit diagram is shown in Figure 7.

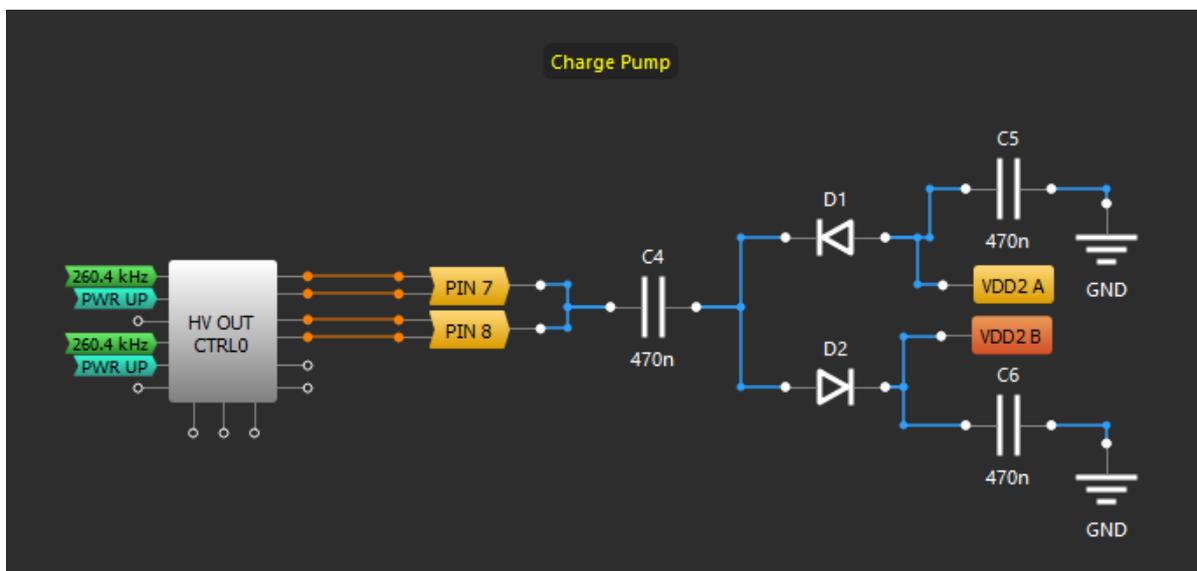


Figure 7 Circuit Diagram of Charge Pump Block

The half-bridge HV OUT CTRL0 is supplied with a 5 V DC voltage (POR). The 260.4 kHz Osc is used to create a square wave signal (AC voltage), which is needed for the Half Wave Voltage Doubler Circuit. The final voltage depends on the voltage drop of the selected diodes D1 and D2 (less than 10 V).

4.2.3 Receiver

This block is used for receiving, amplifying, and filtering the echo signal. The circuit diagram is shown in the Figure 8.

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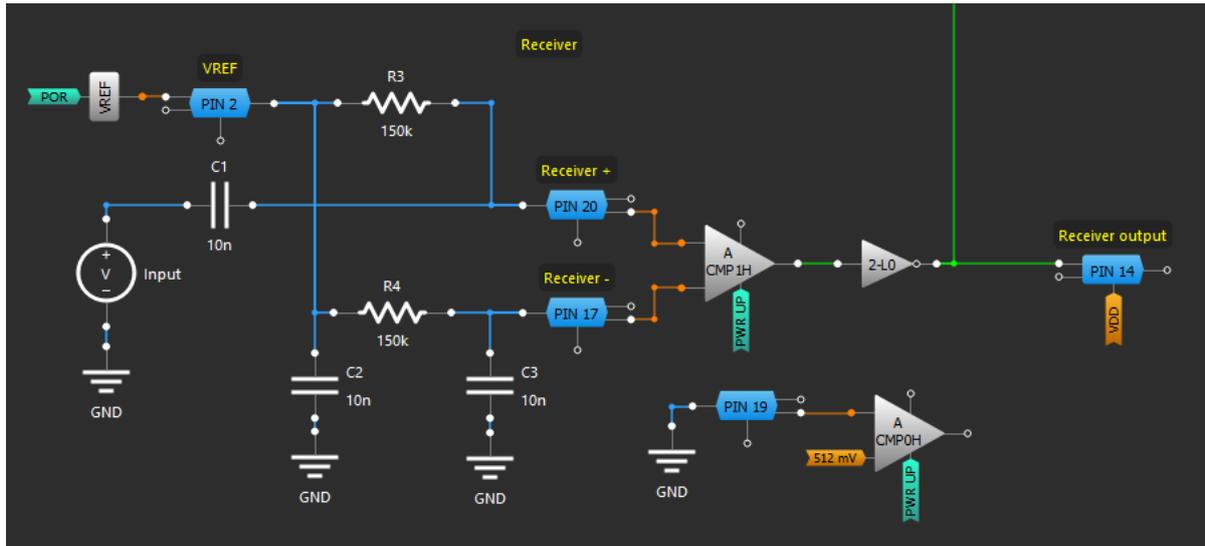


Figure 8 Circuit Diagram of Receiver

Before the GreenPAK, the echo signal goes to the simplest amplifier stage on a bipolar transistor, which amplifies it (Figure 9). Then the echo signal reaches the CMP1 block, where the useful signal is selected. This inverted signal is the receiver's output (PIN 14). The receiver signal (PIN 14, GPIO1) is used only for testing. For the final PCB, the PWM output (PIN 15, SCL/GPIO2) is used.

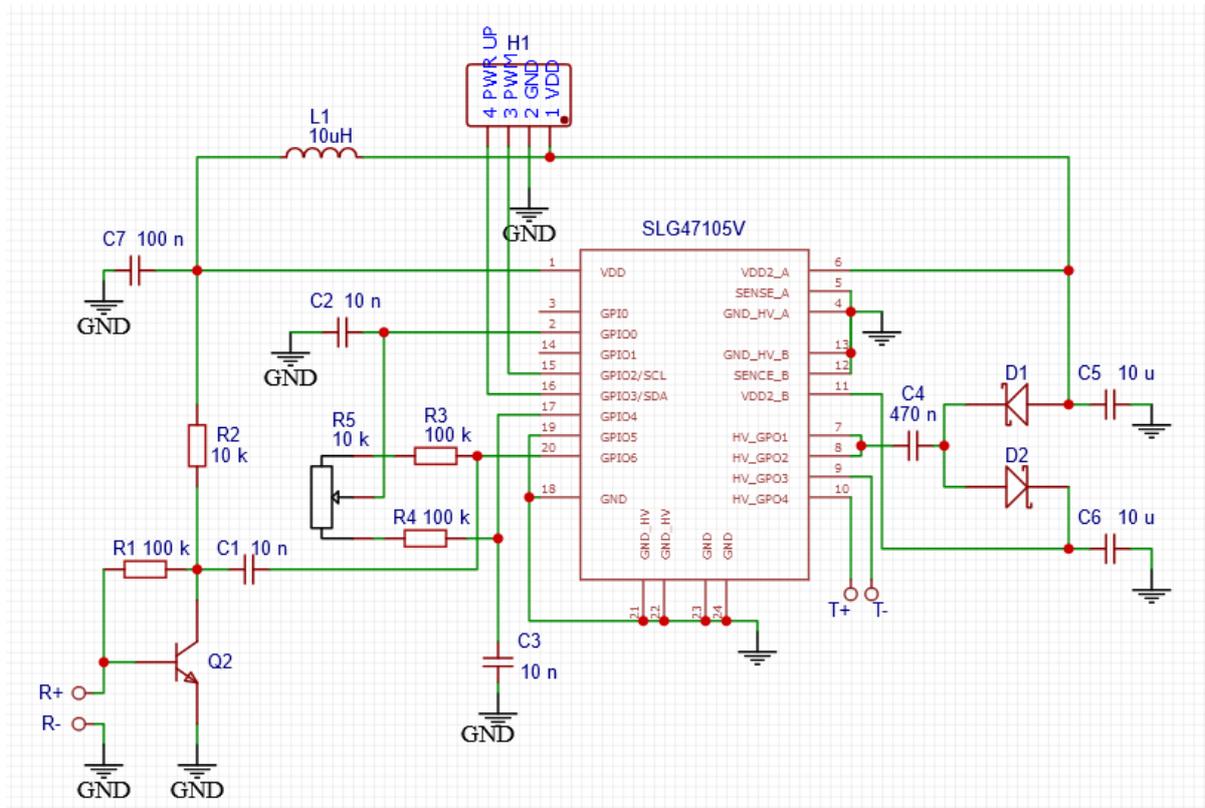


Figure 9 Full Circuit Design

As can be seen in Figure 9, there is a potentiometer R5. This potentiometer is needed to adjust the CMP1 threshold (a few mV) to obtain suitable sensitivity of the sensor.

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4.2.4 PWM

This block is necessary to create a way of the received signal perception. Also, it is needed for further signal processing and its application (Figure 10).

The main idea is to create a square wave signal which is HIGH from the trigger signal to the beginning of the echo signal. As the phase of the echo signal changes depending on the distance to the obstacle, the duration of HIGH PWM also changes.

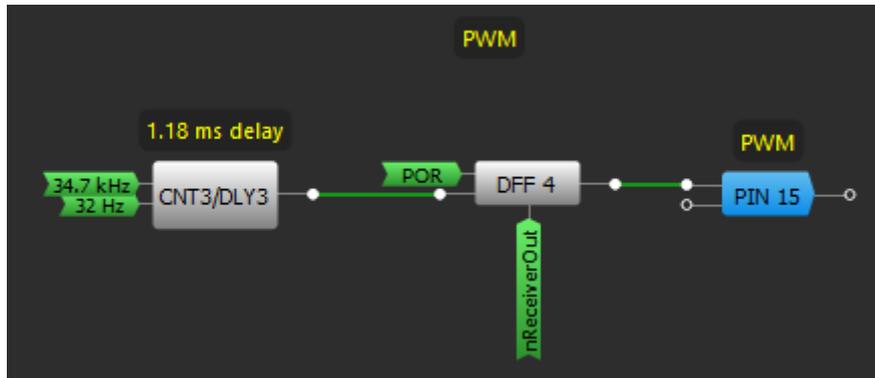


Figure 10 PWM Circuit Design

During testing the circuit, there has been noticed, that the receiver catches the parasitic signal from the transmitter from 0 to ~20 cm. To skip this useless signal, the configured as a Delay CNT3/DLY3 block is added. The typical delay time is about 1.18 ms.

5 PCB Design

Here is a proposed PCB design (Figure 11 and Figure 12) for this application, which is based on the schematic in the Figure 9. Also, there are photos of the working prototype (Figure 13 and Figure 14).

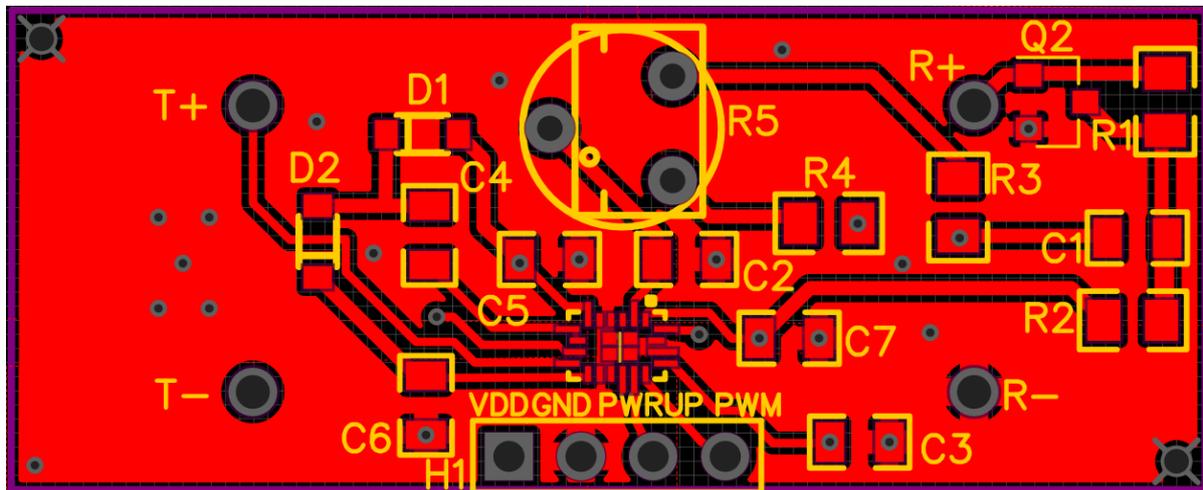


Figure 11 Top Side

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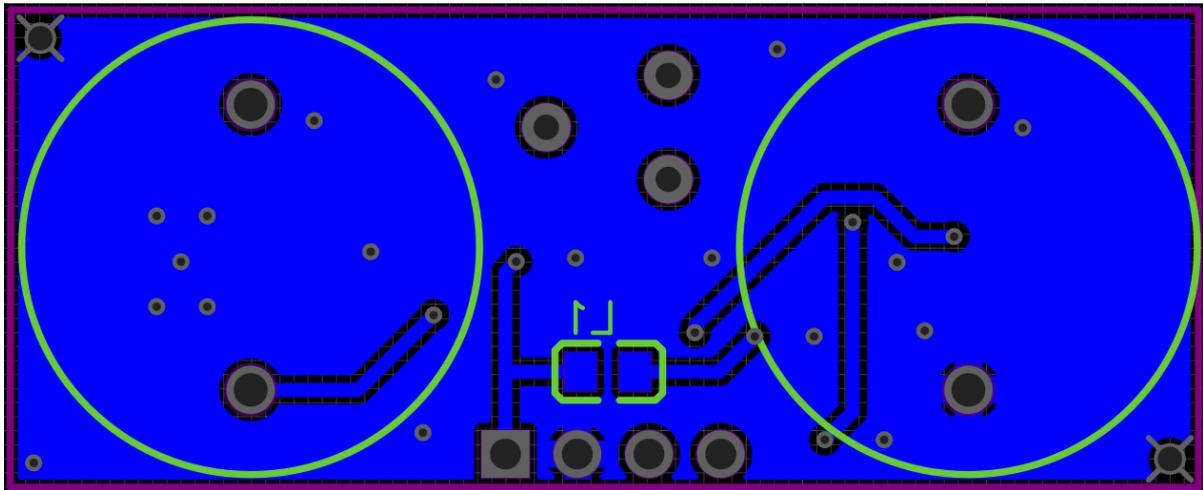


Figure 12 Bottom Side

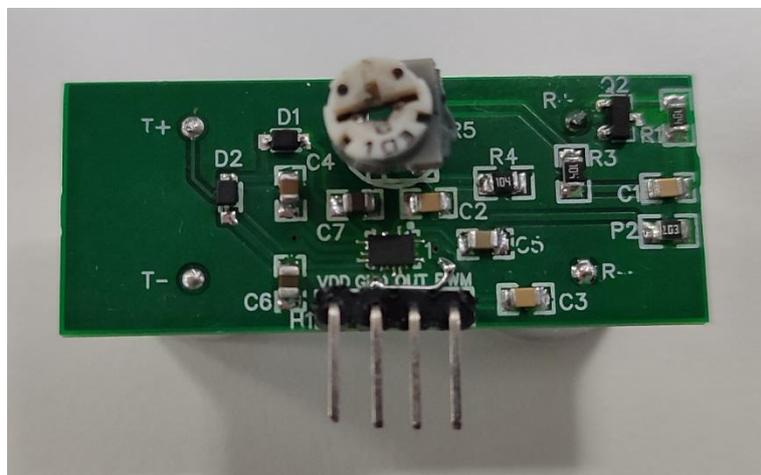


Figure 13 Photo of the Top Side

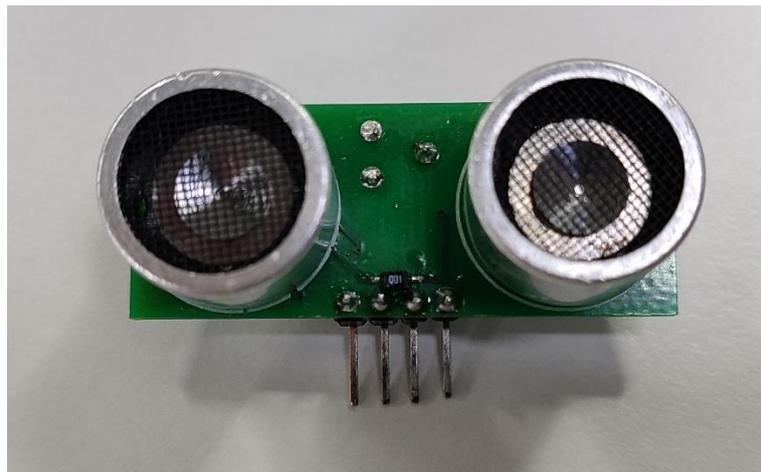


Figure 14 Photo of the Bottom Side

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6 Testing

To test the design, the circuit is connected to 5V (VDD) and the PWR UP is set HIGH.

The example of the results of the measured distance are presented as follows: 100 cm in the [Figure 15](#), 200 cm in the [Figure 16](#), 300 cm in the [Figure 17](#), and 400 cm in [Figure 18](#). The oscilloscope screenshots show the transmitter signal (PIN 9, HV_GPO2_HD), the receiver signal (PIN 14, GPIO1), and the PWM (PIN 15, SCL/GPIO2).

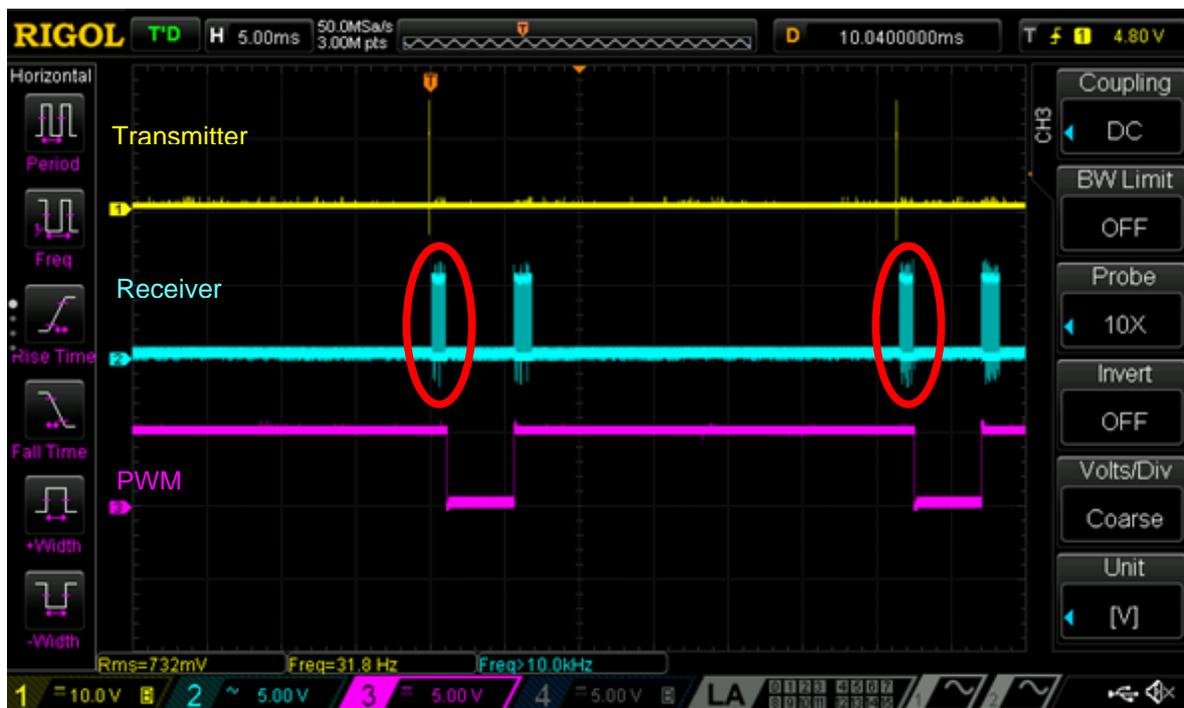


Figure 15 Circuit Test Measuring Distance Within the Range about 100 cm

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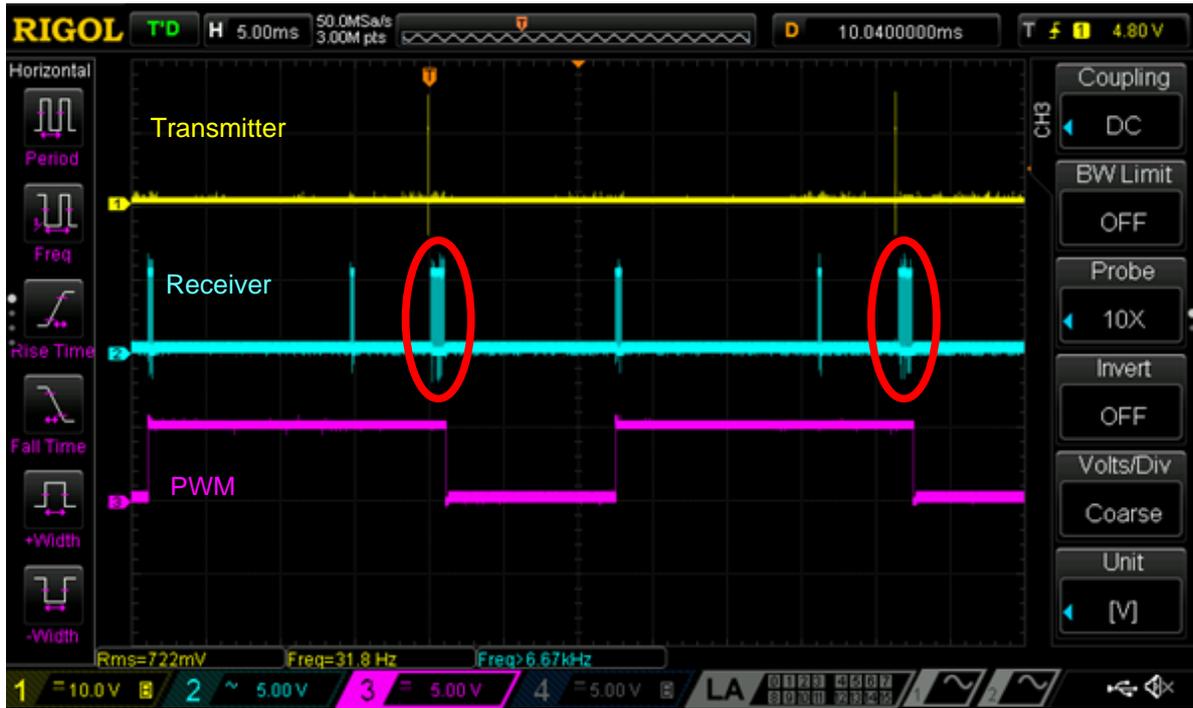


Figure 16 Circuit Test Measuring Distance Within the Range about 200 cm

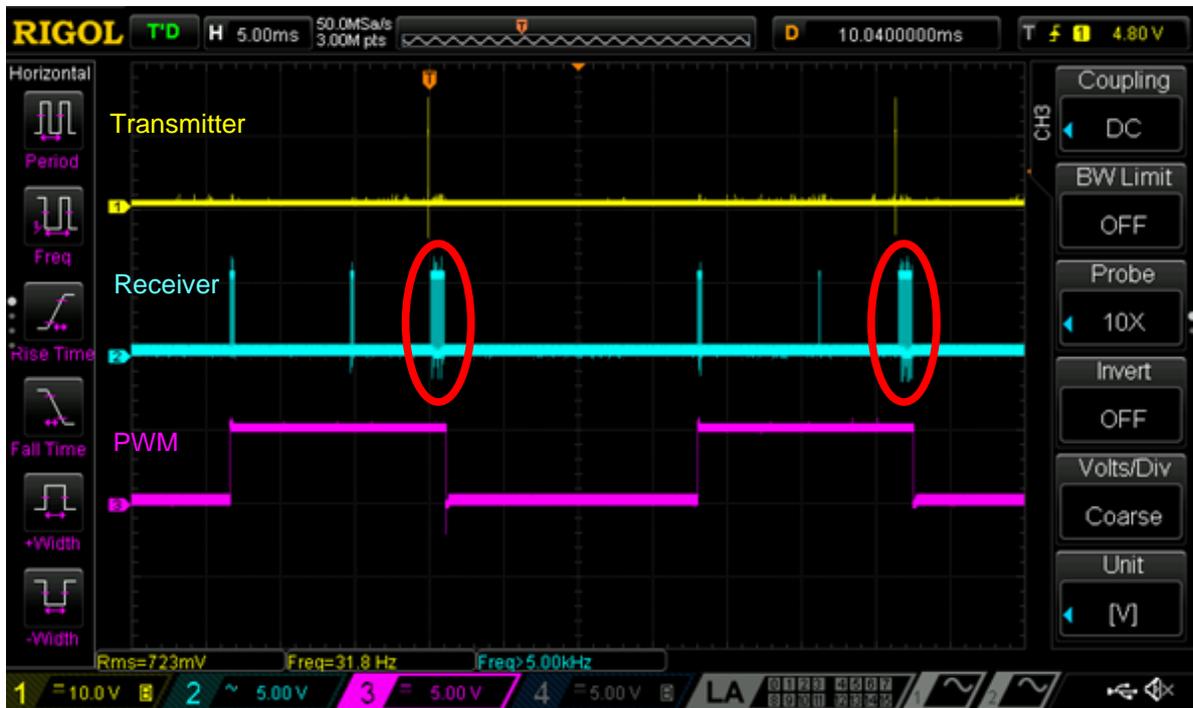


Figure 17 Circuit Test Measuring Distance Within the Range about 300 cm

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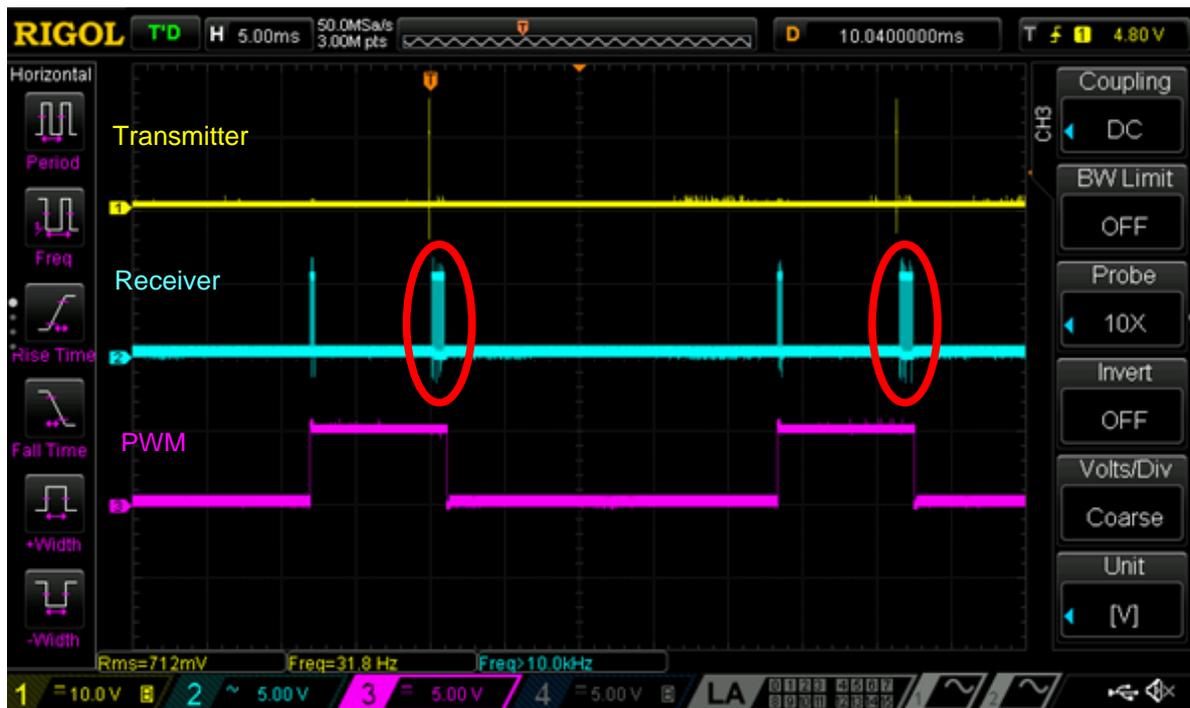


Figure 18 Circuit Test Measuring Distance Within the Range about 400 cm

As can be seen in these figures, the receiver catches the parasitic signal (these glitches are circled in red) from the transmitter from 0 to ~20 cm (4.2.4 PWM).

The results prove that the circuit works as expected, and the GreenPAK module is capable of acting as the control module for the ultrasonic qualitative distance estimation sensor. It measures a distance from 20 cm to about 400 cm.

7 Conclusion

The application note describes how to configure the HV GreenPAK as a control unit to drive the transducers and interpret the receiver's echo pulse output using the PWM to create an ultrasonic qualitative distance estimation sensor. The described ultrasonic sensor measures distance from 20 cm to about 400 cm.

The GreenPAK's internal resources, including the HV, oscillators, logic, and GPIOs are easy to configure to implement the desired functionality for this design.

Revision History

Revision	Date	Description
1.0	31-Jan-2022	Initial Version

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