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# M16C/Tiny Series

## Clock-Synchronous Serial I/O Mode Applied on LED Matrix Drive

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### 1. Abstract

The application note describes an application to display characters on LED matrix. The application employed UART0 (Clock-Synchronous Serial I/O Mode) and 2 I/O ports to control LED driver chips. The MCU used for current application belongs to M16C/29 group.

By running the sample program, a string of Chinese characters will be displayed on 2 continuous LED matrixes in different styles (static display, shift left, shift right, shift up, shift down, static display with color inverted, etc.). The aim of the application is to bring forward a design of LED display which occupies less CPU resources and less peripheral I/O.

Please just take the hardware structure and parameters of the following description as a reference, and confirm or make modifications according to actual conditions in experiment or evaluation.

### 2. Introduction

The application example described in this document is applied to the following:

- MCU: M16C/26A, M16C/28, M16C/29 Group

This program can be used with other microcomputers within the M16C family, which have the same SFR (special function register) as the M16C/26A, M16C/28, and M16C/29 microcomputers. Please check the manual for any additions and modifications to functions. Careful evaluation is recommended before using this application note.

### 3. Specification

#### 3.1 Clock-Synchronous Serial I/O Mode of UARTi

All MCU types mentioned above in introduction contain UARTi (i = 0~2) channels, which are serial I/O channels. Each UART channel is independent and is able to work in several modes. In current application, the clock-synchronous serial I/O mode of UART0 is employed. As the port structure and setting of each UART port varies from each other, the settings of UART1~UART2 port for this application need careful examination (please refer to Hardware Manual).

The clock-synchronous serial I/O mode uses a transfer clock to transmit and receive data. In current application, only simplex communication is required; therefore, the occupied pins are transfer clock pin of UART0 (CLK0) and data transfer pin of UART0 (TxD0).

Please refer to Table 3.1 for the chosen functions of UART0.

Table 3.1 Chosen functions of UART0

Item	Setup		Item	Setup	
Transfer clock source	0	Internal clock (f1/f2/f8/f32)	Transfer format	0	LSB first
		External clock			MSB first
CLK polarity	0	Output transmission data at the falling edge of the transfer clock	Transmission interrupt factor	0	Transmission buffer empty
		Output transmission data at the rising edge of the transfer clock			Transmission complete
		Output transmission data at the rising edge of the transfer clock	Output transfer clock to multiple pins	0	Not selected
					Selected

#### 3.2 LED Matrix

The development of LED technology leads to more and more universal applications of LED matrix. For example, unicolor LED matrix is widely employed to display traffic signals, information board etc, and multi-color LED matrix is used for LED screen. A sample structure of a unicolor 4×4 LED matrix is shown in figure 3.1.

Characters can be coded and displayed in dot matrix. Coded characters with the same resolution and font compose a library. One of the most common libraries for Chinese characters is 16×16 SimSun library, which is the one used in current application.

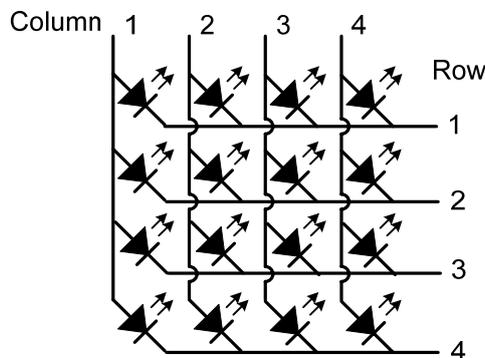


Figure 3.1 A Sample Structure of a Unicolor 4×4 LED Matrix

Normally, a cost-efficient method to display on LED matrix is dynamic display. Therefore the refresh frequency is to be considered. For human eyes, the frequency at which a flickering light is indistinguishable from a steady, non-flickering light is defined as Critical Flicker Frequency (CFF), which is about 48Hz.

In order to get a better effect, the refresh frequency (also called Vertical Scanning Frequency in TV) is set to 76.29Hz (16-division of Line-Scanning frequency); the Line-Scanning frequency (also called Horizontal Scanning Frequency in TV) is set to 1.22 kHz ( $2^{11}$ -division of main clock which is 8-division of 20MHz).

### 3.3 Renesas 16-Bit LED Driver

M66310P/FP is a LED array driver having a 16bit serial-input and parallel output shift-register function with direct coupled reset input and output latch function.

This product guarantees the output electric current of 24mA which is sufficient for cathode common LED drive, capable of flowing 16bits continuously at the same time. Parallel output is open drain output and serial output is available for expansion.

The structure diagram of M6631P/FB is shown in figure 3.2.

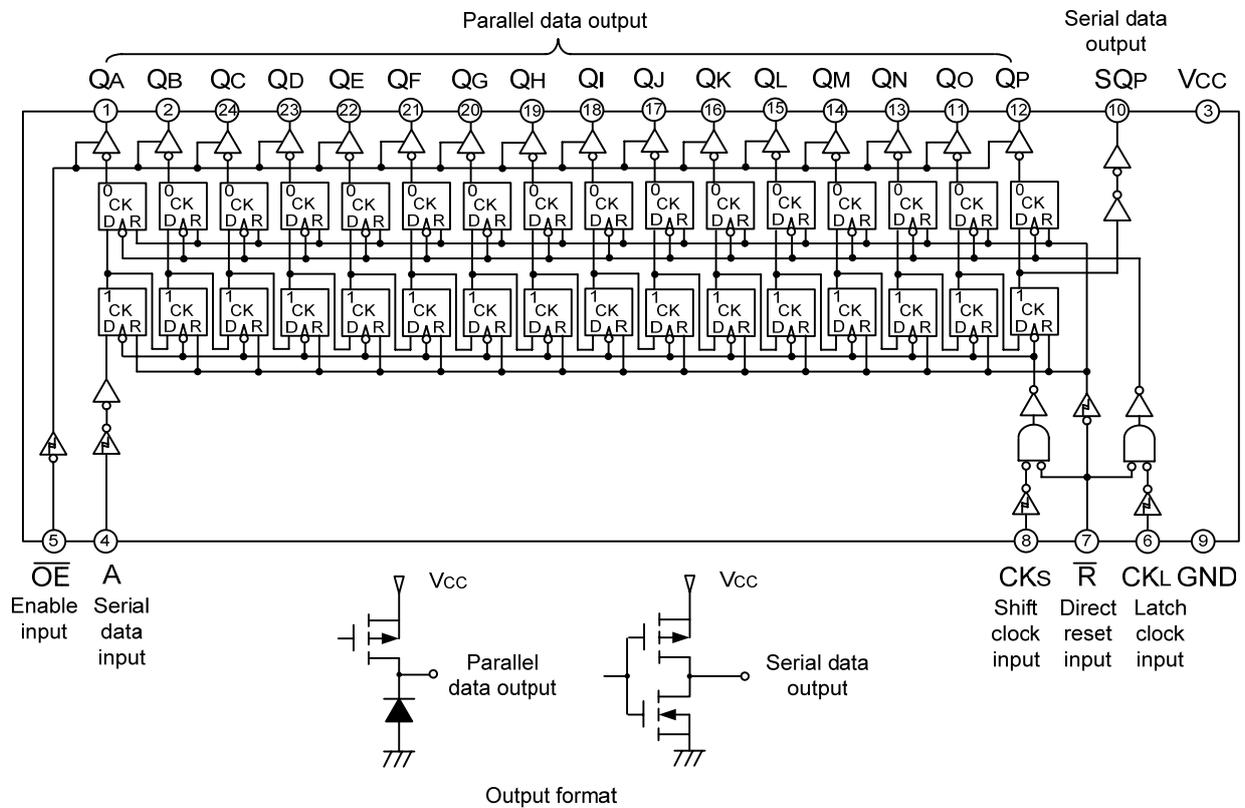


Figure 3.2 M66310P/FP Structure Diagram

4. Design Description

4.1 Hardware structure

In current application, the structure of hardware system is shown in figure 4.1.

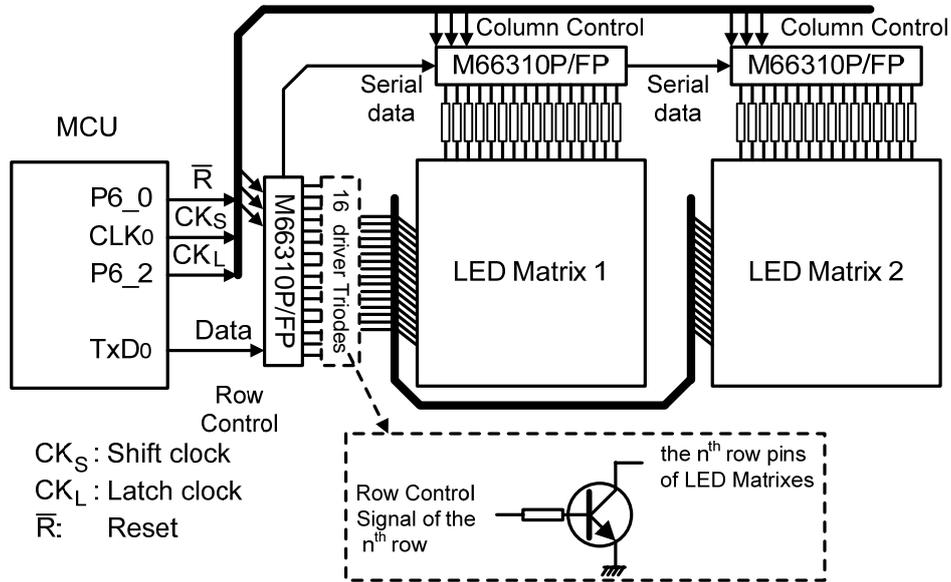


Figure 4.1 Hardware Structure of Application

As shown in figure 4.1, (n+1) chips of M66310P/FP are used (in current application, n = 2), in which n chips are for the column information of corresponding LED matrix and the other 1 chip is for the row control of all LED matrix (additional driver circuit for row control are required according to the actual current consumption). Therefore, at a certain time, respective information is displayed at the same line of respective matrix.

Based on the above hardware structure, the serial data should be transferred in following order:

- a) Column signal of the n<sup>th</sup> LED matrix (information to be displayed on certain line of the n<sup>th</sup> LED matrix)
- b) Column signal of the (n-1)<sup>th</sup> LED matrix (information to be displayed on certain line of the (n-1)<sup>th</sup> LED matrix)
- c) .....
- d) Column signal of the 1<sup>st</sup> LED matrix (information to be displayed on certain line of the 1<sup>st</sup> LED matrix)
- e) Row selection signal of all LED matrixes

On the end of above sequence, if a latch clock is provided after all data are transferred, all the fixed data will be outputted to the parallel output pins of LED driver chips, and then a certain line of all LED matrixes is lighted. By repeating above sequence with selecting row in turn, a dynamic display will be generated. If a proper refresh frequency is set, the information displayed on LED matrix looks static.

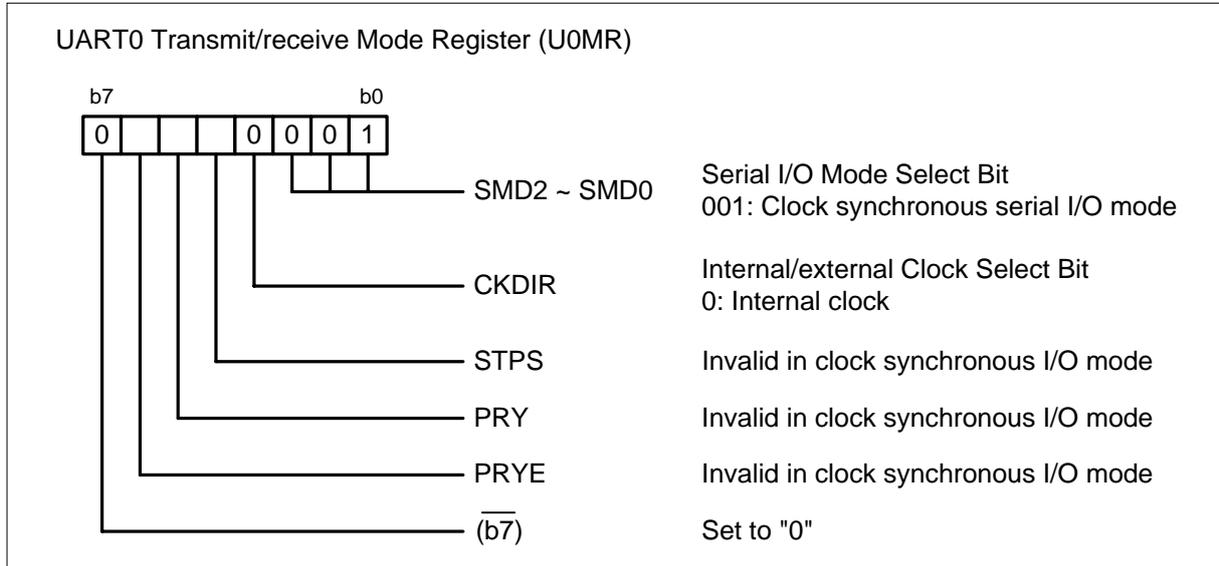
The structure shown in figure 4.1 has a good expansibility. In the condition of current application, which are 1Mbit/s serial communication bit rate and 1.22 KHz Line-Scanning frequency, up to a maximum of 50 LED matrixes in series are feasible. More LED matrixes means more CPU resources occupancy; in the acceptance range of actual conditions, by increasing bit rate or decreasing Line-Scanning frequency, a system will support more LED matrixes.

In addition, the bottle-neck of above hardware structure is the row control driver circuit. With an increasing numbers of LED matrixes, the parameters of corresponding row-driver circuit must be checked carefully.

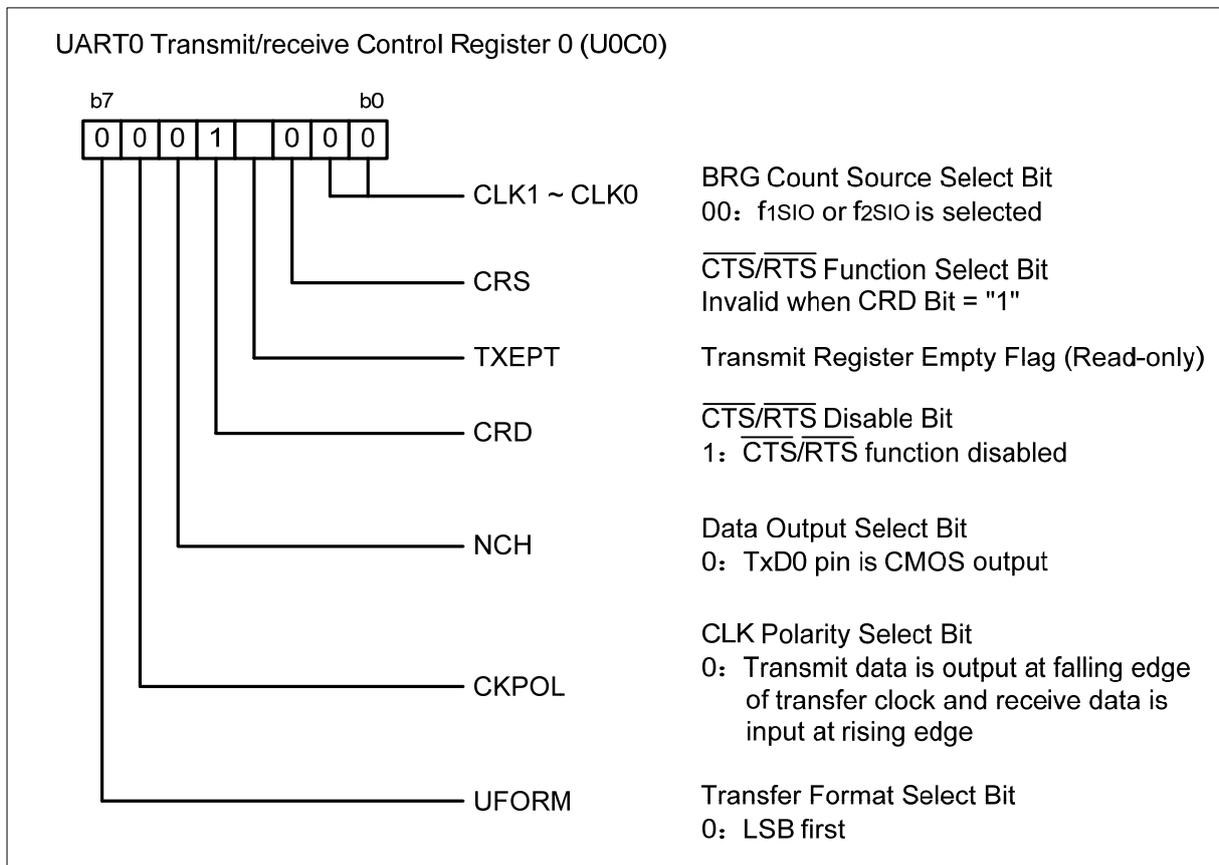
### 4.2 Register Setting

The following procedure in this application note is based on M16C/29 group products, M16C/28 and M16C/26A group's setup procedure please refer to the hardware user's manual.

(1) Set UART0 Transmit/receive Mode Register

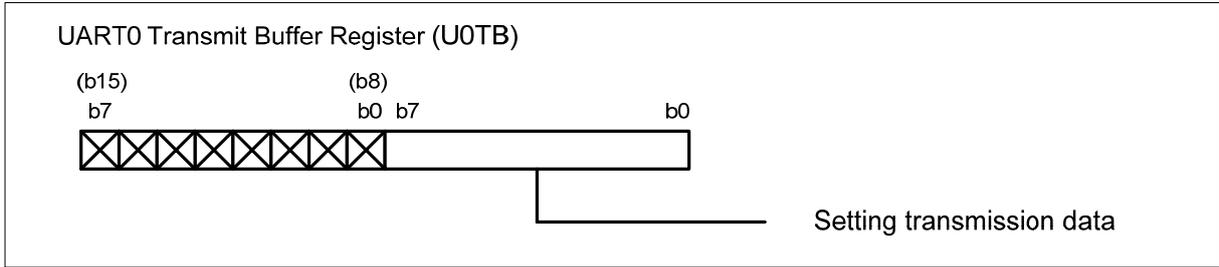


(2) Set UART0 Transmit/receive Control Register 0

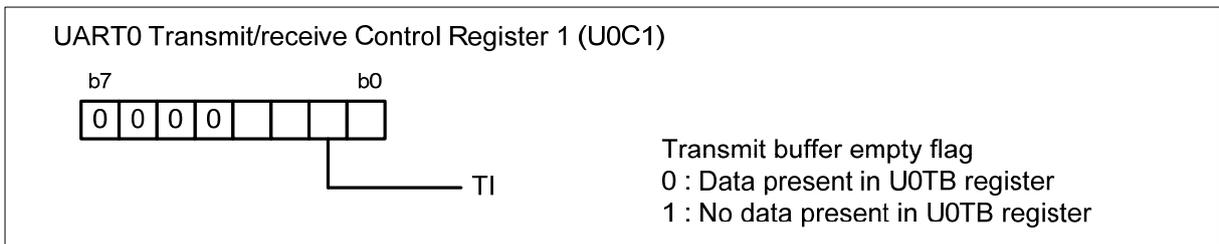




(6) Set UART0 Transmit Buffer Register to write transmit data



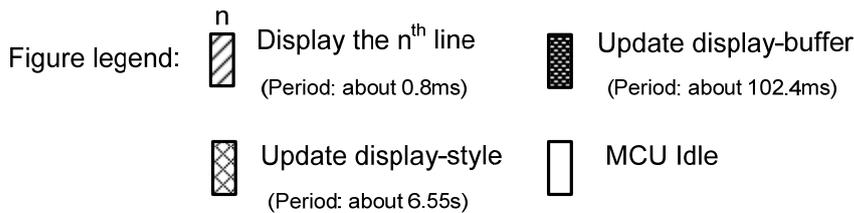
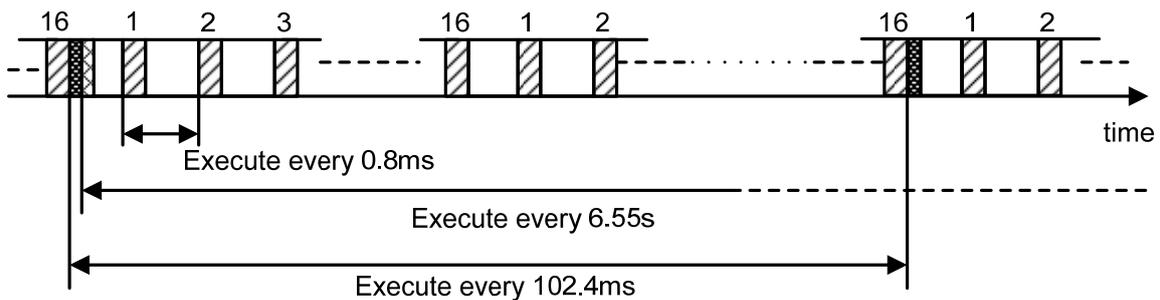
(7) Check UART0 Transmit/receive Control Register 1 to get the status of UART0 Transmit Buffer Register



### 4.3 Time-Sequence and Structure of Program

In current application, the basic function is to display information on LED matrix and switch display styles periodically. In program, a display-buffer is generated according to the hardware structure of LED matrix. Every 0.8ms, MCU transfers data of a line of display-buffer as well as row control signal to LED drivers, and then generate latch clock; every 102.4ms, MCU refreshes display-buffer according to current display style; every 6.55s, MCU changes display style. Please refer to figure 4.2 for a segment of MCU operation sequence.

The display style changes in turn as follows: Static Display → Shifting Left → Shifting Right → Shifting Up → Shifting Down → Static Display with color inverted → Static Display .....



Note: the proportion of time-spending varies in different designs and settings

Figure 4.2 A Segment of MCU Operation Sequence

The sequence diagram shown in figure 4.3 is a segment of signal relationship. The time segment is from the beginning of a Row-Scanning period to the end of transferring the 2<sup>nd</sup> 8bit data.

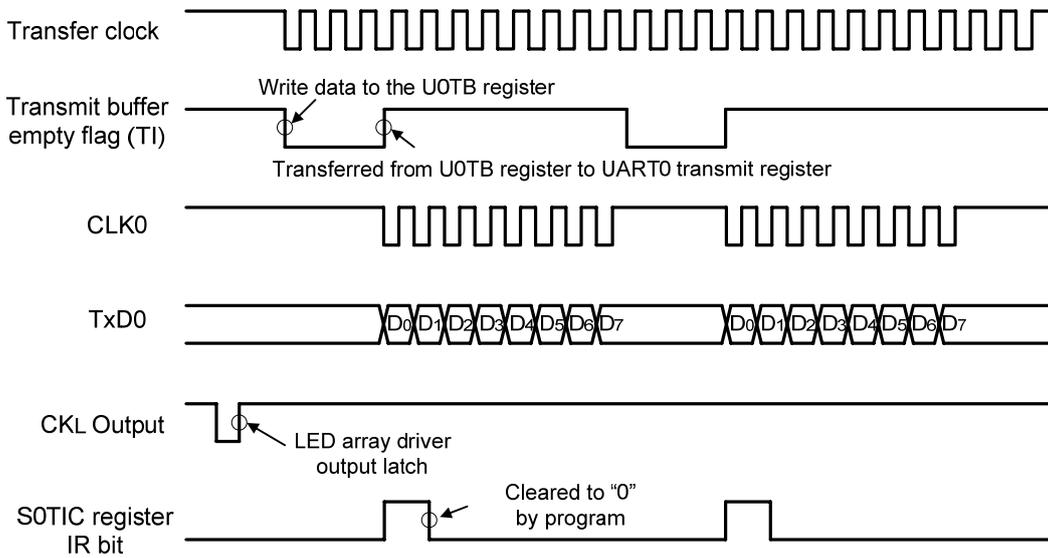


Figure 4.3 A Segment of Transmitting Time-sequence

Figure 4.4 shows the flow chart of main program and display module. Figure 4.5 shows the flow chart of row-scanning sub function and updating display-buffer sub function.

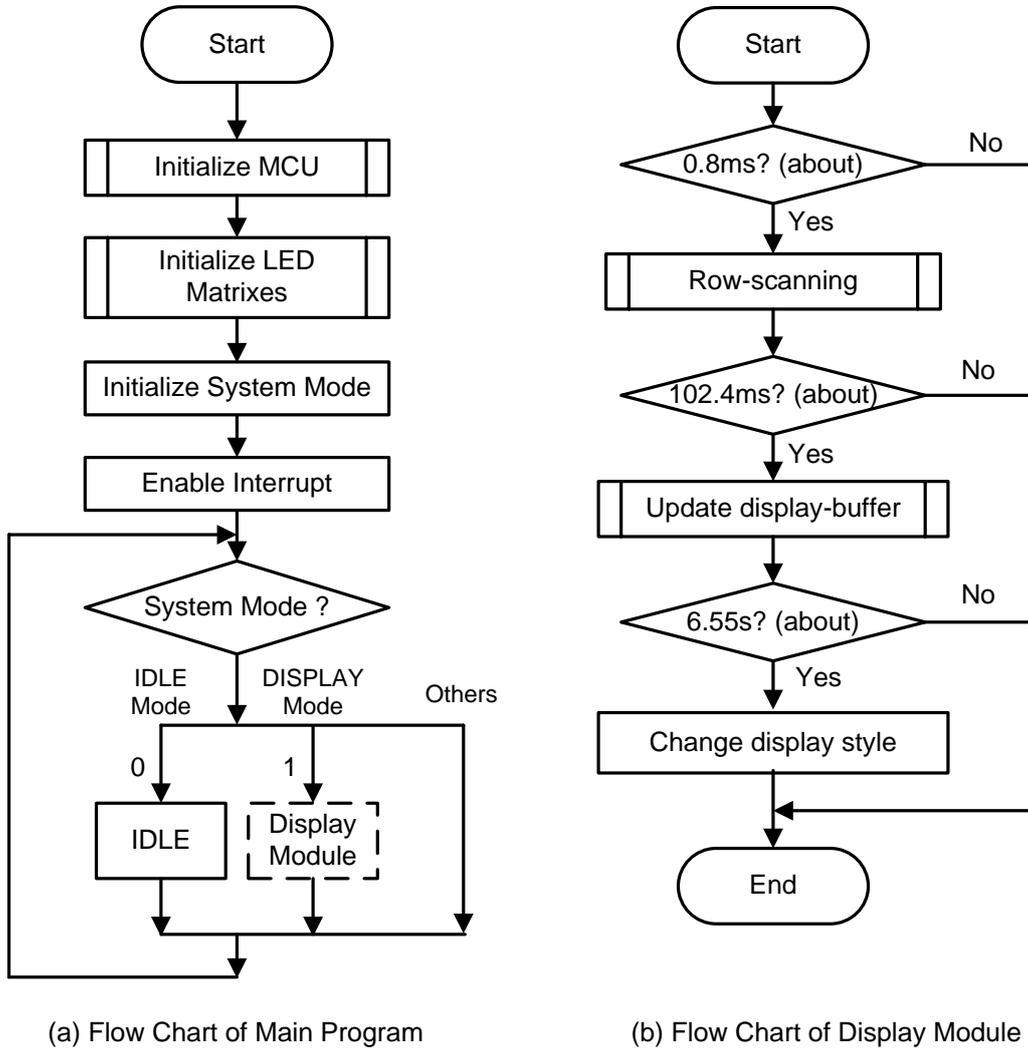
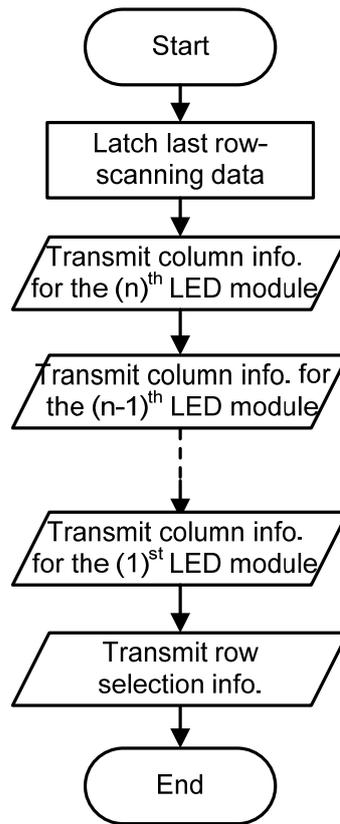
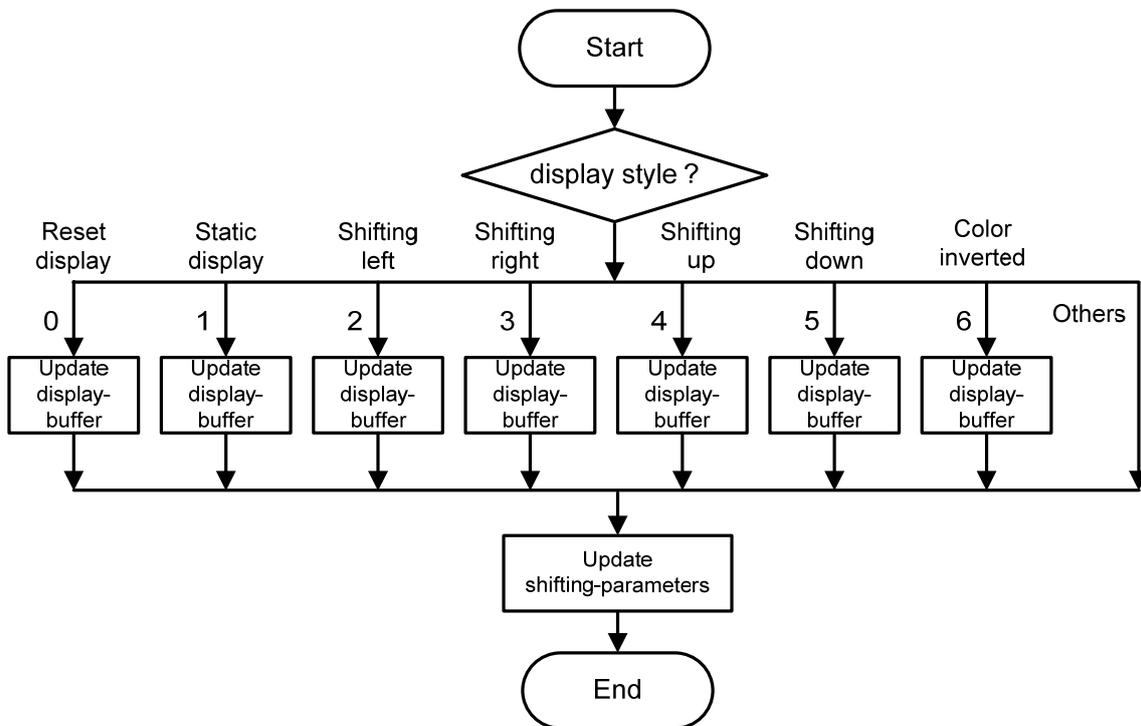


Figure 4.4 Flow Chart of Main Program and Display Module



(a) Flow Chart of Row-Scanning Sub Function



(b) Flow Chart of Updating Display-buffer Sub Function

Figure 4.5 Flow Chart of Row-Scanning Sub Function and Updating Display-buffer Sub Function

## 5. Sample Program

```

/*****/
/*   Project       : M16C/Tiny Application Note           */
/*               : UARTi (Clock-Synchronous Serial I/O Mode)*/
/*               : Application on LED Matrix Driver      */
/*   MCU          : M30290FCTHP                         */
/*   C Compiler   : NC30WA, version 5.40                */
/*   File name    : ClockSS.c                           */
/*   Function     : Display characters in different styles */
/*   Code Version : 1.0                                  */
/*               */
/*   Copyright (C) 2007 Renesas Technology Corp.         */
/*   All right reserved.                                 */
/*****/

/*****/
/*   Header Including                                     */
/*****/
#include "sfr29.h"    /* SFR definition */
#include "fontlib.h" /* Font library */

/*****/
/*   Macro Definition                                   */
/*****/
unsigned char system_mode;          /* System mode */
#define MODE_IDLE      0x00         /* Idle mode */
#define MODE_DISPLAY  0x01         /* Display mode */

#define BLOCKS        2            /* LED Matrix modules (Hardware)*/
#define LINES         16          /* Columns (rows) of LED Matrix */
#define CHARACTERS    6           /* Characters to be displayed */

#define BUFREFRESH_COUNTER 128 /* Time interval to update display-buffer */
/* = n * (time interval of Row-Scanning) */

#define MODECHANGE_COUNTER 64 /* Time interval to update display-style */
/* = n * (BUFREFRESH_COUNTER) */

/*****/
/*   Function Declaration                               */
/*****/
void init_mcu(void);
void ini_ledmatrix(unsigned int near * buf);

```

```

void refresh_buffer(unsigned int near * buf, unsigned char set);
void output_blocks(unsigned int near * buf);
void output_word(unsigned int word_data);
void output_byte(unsigned char byte_data);

/*****
/*   Global Variables Definition                               */
/*****
unsigned int g_RowScan = 0;      /* Row-Scanning counter      */
unsigned int g_BufRef = 0;      /* Buffer-refreshing counter */

/*****
Function:      main
Description:   main function
Calls:        init_mcu(void)
              ini_ledmatrix(unsigned int near *)
              output_blocks(unsigned int near *)
              refresh_buffer(unsigned int near *,unsigned char)

Input:        None
Output:       None
Return:       None
Others:       None
*****/
void main(void){

unsigned int buffer[BLOCKS][LINES] = {}; /* Define Display Buffer Variable */
unsigned char display_style = 1;        /* Define Display style Variable */

init_mcu();                             /* Initialize MCU */

ini_ledmatrix((unsigned int near *)buffer); /* Initialize LED Matrix */

system_mode = MODE_IDLE;                 /* Initialize System Mode */
                                           /* (Set as Idle Mode) */
ta0s = 1;                                /* Start Timer A0 */
asm("fset I");                           /* Enable Interrupt */

while(1){

/* Chose working mode for the system */
switch(system_mode){

/* Idle mode */

```

```

case MODE_IDLE:

    break;

/* Display mode */
case MODE_DISPLAY:

    output_blocks((unsigned int near *)buffer);    /* Row-Scanning */

/* Check whether the condition to update display-buffer is met */
if(g_RowScan >= BUFREFRESH_COUNTER) {

    g_RowScan = 0;
    g_BufRef++;

/* Update display-buffer */
refresh_buffer((unsigned int near *)buffer, display_style);
}

/* Check whether the condition to update display-style is met */
if(g_BufRef >= MODECHANGE_COUNTER) {

    g_BufRef = 0;

/* Reset display (clear the screen) */
refresh_buffer((unsigned int near *)buffer, 0);

/* Update display-style, and make sure it is valid */
display_style++;
if(display_style > 6) {
    display_style = 1;
}
}

system_mode = MODE_IDLE;    /* Set system mode to Idle mode */
break;

default:
    break;
}
}
}

```

```

/*****
Function:      init_mcu
Description:   initialize MCU
Calls:        None
Input:        None
Output:       None
Return:       None
Others:       None
*****/
void init_mcu(void)
{
    /* Setting system clock related registers */
    prcr = 0x01;          /* cm0,cm1,cm2 writing enable */
    cm2 = 0x00;          /* system register2 Initialize */
    cm1 = 0x20;          /* system register1 Xcin-Xcout:High */
    cm0 = 0x08;          /* system register0 Xcin-Xcout:High */
    prcr = 0x00;          /* cm0,cm1,cm2 writing disable */

    prcr = 0x04;          /* pacr writing enable */
    pacr = 0x03;          /* 80pin type */
    prcr = 0x00;          /* pacr writing disable */

    /* Setting Timer A0 related registers */
    ta0mr = 0x40;         /* Timer A0 mode register, count f8 */
    ta0 = 2048 - 1;       /* Timer A0 1.22 KHz count */
    ta0ic = 0x01;        /* Timer A0 interrupt, level 1 */

    /* Setting UART0 related registers */
    u0mr = 0x01; /* Setting UART0 transmit/receive mode register
                  Clock synchronous serial I/O mode Internal clock select */
    u0c0 = 0x10; /* Setting UART0 transmit/receive control register 0
                  ~CTS/~RTS function disabled TxD0 pin is CMOS output
                  Transmission data is output at falling edge of transfer
                  clock and reception data is input at rising edge LSB first */
    ucon = 0x00; /* Setting UART transmit/receive control register 2 setting
                  UART0 transmit interrupt cause is selected to
                  "Transmission buffer empty (TI=1)"
                  transfer clock output from CLK0 only */
    u0brg = 10-1; /* Setting UART1 bit rate generator (1MHz @20MHz f1) */
    u0c1 = 0x01; /* Setting UART transmit/receive control register 1
                  Transmit enabled */
    s0tic = 0; /* Clear UART0 transmit interrupt request bit */
}

```

```

/*****
Function:      ini_ledmatrix
Description:   initialize ledmatrix related HW and SW
Calls:        None
Input:        buf: starting address of display-buffer
Output:       None
Return:       None
Others:       None
*****/

void ini_ledmatrix(unsigned int near * buf)
{
    unsigned char i;
    unsigned char j;

    /* Fill display-buffer in advance (not necessary) */
    for(i= 0; i<BLOCKS; i++) {
        for(j = 0; j<LINES; j++) {
            *(buf + i * LINES + j) = font[i][j];
        }
    }

    p6 = 0x01;      /* Set p6_0 pin to 1 in advance */
                   /* (connected to RESET pins of LED driver chips) */
                   /* Set p6_2 pin to 0 in advance */
                   /* (connected to Latch Clock pins of LED driver chips) */

    pd6_0 = 1;     /* Set direction of p6_0 pin as Output */
    pd6_2 = 1;     /* Set direction of p6_2 pin as Output */

    p6_0 = 0;     /* Generate RESET signal for LED driver chips */
    p6_0 = 1;     /* (Operation on the falling edge) */
}

/*****
Function:      refresh_buffer
Description:   update display-buffer
Calls:        None
Input:        buf: starting address of display-buffer
Set:          display style
              0: Reset display
              1: Static display
              2: Shifting left
              3: Shifting right
*****/

```

```

        4: Shifting up
        5: Shifting down
        6: Static display with color inverted

Output:      None
Return:      None
Others:      None
*****/
void refresh_buffer(unsigned int near * buf, unsigned char set)
{
    static unsigned char character_scale = 0;    /* Parameter for shifting */
    static unsigned char shift_scale = 0;       /* Parameter for shifting */
    unsigned char i;
    unsigned char j;

    /* Chose the display style */
    switch(set) {

        /* Reset display */
        case 0:

            /* Reset parameters for shifting */
            character_scale = 0;
            shift_scale = 0;

            /* Clear display-buffer */
            for(i = 0; i<BLOCKS; i++) {
                for(j = 0; j<LINES; j++) {
                    *(buf + i * LINES + j) = 0x00;
                }
            }
            break;

        /* Static display */
        case 1:

            for(i = 0; i<BLOCKS; i++) {
                for(j = 0; j<LINES; j++) {
                    *(buf + i * LINES + j) = font[i][j];
                }
            }
            break;

        /* Shifting left */

```

case 2:

```

for(i = 0; i<BLOCKS; i++) {
    for(j = 0; j<LINES; j++) {
        *(buf + i * LINES + j) =
            (font[(character_scale+i)%CHARACTERS][j]
             <<shift_scale)
            |(font[(character_scale+i+1)%CHARACTERS][j]
             >>(16-shift_scale));
    }
}
break;

```

/\* Shifting right \*/

case 3:

```

for(i = 0; i<BLOCKS; i++) {
    for(j = 0; j<LINES; j++) {
        *(buf + i * LINES + j) =
            (font[(character_scale+BLOCKS-i-1)%CHARACTERS][j]
             >>shift_scale)
            |(font[(character_scale+BLOCKS-i)%CHARACTERS][j]
             <<(16-shift_scale));
    }
}
break;

```

/\* Shifting up \*/

case 4:

```

for(j = 0; j<(LINES-shift_scale); j++) {
    *(buf + 0 * LINES + j) =
        (font[(character_scale)%CHARACTERS][j+shift_scale]);
}

for(j = 0; j<shift_scale; j++) {
    *(buf + 0 * LINES + j + (LINES - shift_scale)) =
        (font[(character_scale+1)%CHARACTERS][j]);
}
break;

```

/\* Shifting down \*/

case 5:

```

for(j = 0; j<(shift_scale); j++) {
    *(buf + 1 * LINES + j) =
        (font[(character_scale+1)%CHARACTERS]
            [j+(LINES - shift_scale)]);
}

for(j = 0; j<(LINES-shift_scale); j++) {
    *(buf + 1 * LINES + j + shift_scale) =
        (font[(character_scale)%CHARACTERS][j]);
}
break;

/* Static display with color inverted */
case 6:

    for(i = 0; i<BLOCKS; i++) {
        for(j = 0; j<LINES; j++) {
            *(buf + i * LINES + j) = ~font[i][j];
        }
    }
    break;

default:
    break;
}

/* Update parameters for shifting (until the end of current function) */
shift_scale++;

if(shift_scale >= LINES) {

    shift_scale = 0;
    character_scale++;

    if(character_scale >= CHARACTERS) {

        character_scale = 0;
    }
}
}

/*****
Function:      output_blocks

```

```

Description:  output display-buffer to LED matrix driver
              chips serially
Calls:       output_word(unsigned int)
Input:       buf:  starting address of display-buffer
Output:      None
Return:      None
Others:      None
*****/
void output_blocks(unsigned int near * buf)
{
    unsigned char line_index;
    unsigned char i;

    /* Using lower 4bits of Row-Scanning counter
       as row-selection synchronization signal */
    line_index = (g_RowScan - 1) & 0x0F;

    p6_2 = 0;      /* Generate LED matrix driver chip latch signal */
    p6_2 = 1;      /* (operation on rising edge) */

    /* Output column signals for each driver chips in tern */
    for(i = BLOCKS; i>0; i--) {
        output_word(*(buf + (i-1) * LINES + line_index));
    }

    /* Output row selection signal */
    output_word((0x0001<<(LINES-1-line_index)));
}

/*****
Function:     output_word
Description:  output a word data to LED matrix driver chip
Calls:       output_byte(unsigned char)
Input:       word_data:  a word data
Output:      None
Return:      None
Others:      None
*****/
void output_word(unsigned int word_data)
{
    output_byte((unsigned char)(word_data));          /* Output lower 8 bits */
    output_byte((unsigned char)(word_data >> 8));   /* Output higher 8 bits */
}

```

```

/*****
    Function:      output_byte
    Description:   output a byte data to LED matrix driver chip
    Calls:        None
    Input:        byte_data:  a byte data
    Output:       None
    Return:       None
    Others:       None
*****/
void output_byte(unsigned char byte_data)
{
    u0tb = byte_data;  /* Writing transmit data */

    /* Check & wait the status of UART0 transmit interrupt request flag */
    while (ir_s0tic == 0) {
    }
    ir_s0tic = 0;      /* Clear UART0 transmit interrupt request flag */
}

/*****
    Function:      timerA0_int
    Description:   Timer A0 interrupt handling program
    Calls:        None
    Input:        byte_data:  a byte data
    Output:       None
    Return:       None
    Others:       None
*****/
#pragma INTERRUPT timerA0_int
void timerA0_int(void){

    system_mode = MODE_DISPLAY;      /* Set system mode to Display mode */
    g_RowScan++;                      /* Increasing Row-Scanning counter */
}

```

```

/*****
/*   Project       : M16C/Tiny Application Note           */
/*               : UARTi (Clock-Synchronous Serial I/O Mode) */
/*               : Application on LED Matrix Driver       */
/*   MCU          : M30290FCTHP                         */
/*   C Compiler   : NC30WA, version 5.40                */
/*   File name    : fontlib.h                           */
/*   Function     : Characters font                     */
/*   Code Version: 1.0                                  */
/*               */
/*   Copyright (C) 2007 Renesas Technology Corp.        */
/*   All right reserved.                                */
*****/

unsigned int font[][16] = {

    /* 0 Chinese Character "RUI" */
    { 0x0020, 0xf924, 0x2124, 0x2124, 0x21fc, 0x2000, 0xfbfe, 0x2040,
      0x23fe, 0x2252, 0x2a52, 0x3252, 0xc252, 0x0252, 0x024a, 0x0204 },

    /* 1 Chinese Character "SA" */
    { 0x0440, 0x0440, 0xfffe, 0x0440, 0x0440, 0x7820, 0x4bfe, 0x5088,
      0x6050, 0x53fe, 0x4a00, 0x6a00, 0x5200, 0x4400, 0x4400, 0x4800 },

    /* 2 Chinese Character "KE" */
    { 0x0608, 0x7888, 0x0848, 0x0848, 0xfe08, 0x1888, 0x1c48, 0x2a48,
      0x280e, 0x4878, 0x8b88, 0x0808, 0x0808, 0x0808, 0x0808, 0x0808 },

    /* 3 Chinese Character "JI" */
    { 0x1020, 0x1020, 0x1020, 0xfdfc, 0x1020, 0x1420, 0x19fc, 0x3108,
      0xd088, 0x1090, 0x1060, 0x1060, 0x1090, 0x110e, 0x5604, 0x2000 },

    /* 4 blank */
    { 0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0 },

    /* 5 blank */
    { 0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0 },

};

```

## 6. Reference

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<http://www.renesas.com/inquiry>

[csc@renesas.com](mailto:csc@renesas.com)

## Hardware Manual

M16C/29 Group Hardware Manual

M16C/28 Group Hardware Manual

M16C/26A Group (M16C/26A, M16C/26B, M16C/26T) Hardware Manual

M66310P/FP Hardware Manual

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		Page	Summary
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