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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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M16C/62A Group Buzzer Output

1.0 Abstract

The timer mode is used to make the buzzer ring.
Use the following peripheral function:

- The pulse-outputting function in timer mode of timer A.

2.0 Introduction

Specifications (1) Sound a 2-kHz buzz beep by use of timer A0.

(2) Effect pull-up in the relevant port by use of a pull-up resistor. When the buzzer is off, set the port high-impedance, and stabilize the potential resulting from pulling up.

(3) Connect a 16-MHz oscillator to X_{IN} .

Operation

(1) The microcomputer begins performing a count on timer A0. Timer A0 has disabled interrupts.

(2) The microcomputer begins pulse output by setting the pulse output function select bit to "Pulse output effected". $P7_0$ changes into $TA0_{OUT}$ pin and outputs 2-kHz pulses.

(3) The microcomputer stops outputting pulses by setting the pulse output function select bit to "Pulse output not effected". $P7_0$ goes to an input pin, and the output from the pin becomes high-impedance.

Figure 1 shows the operation timing

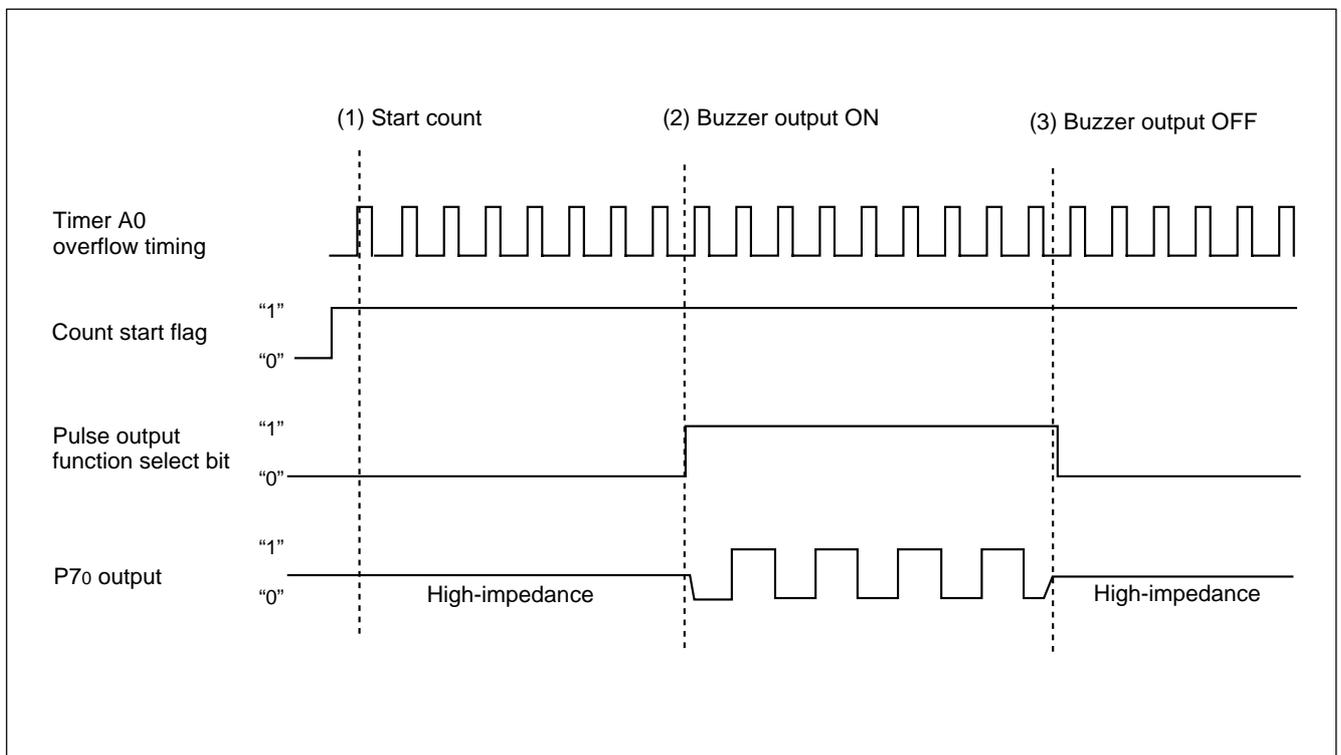
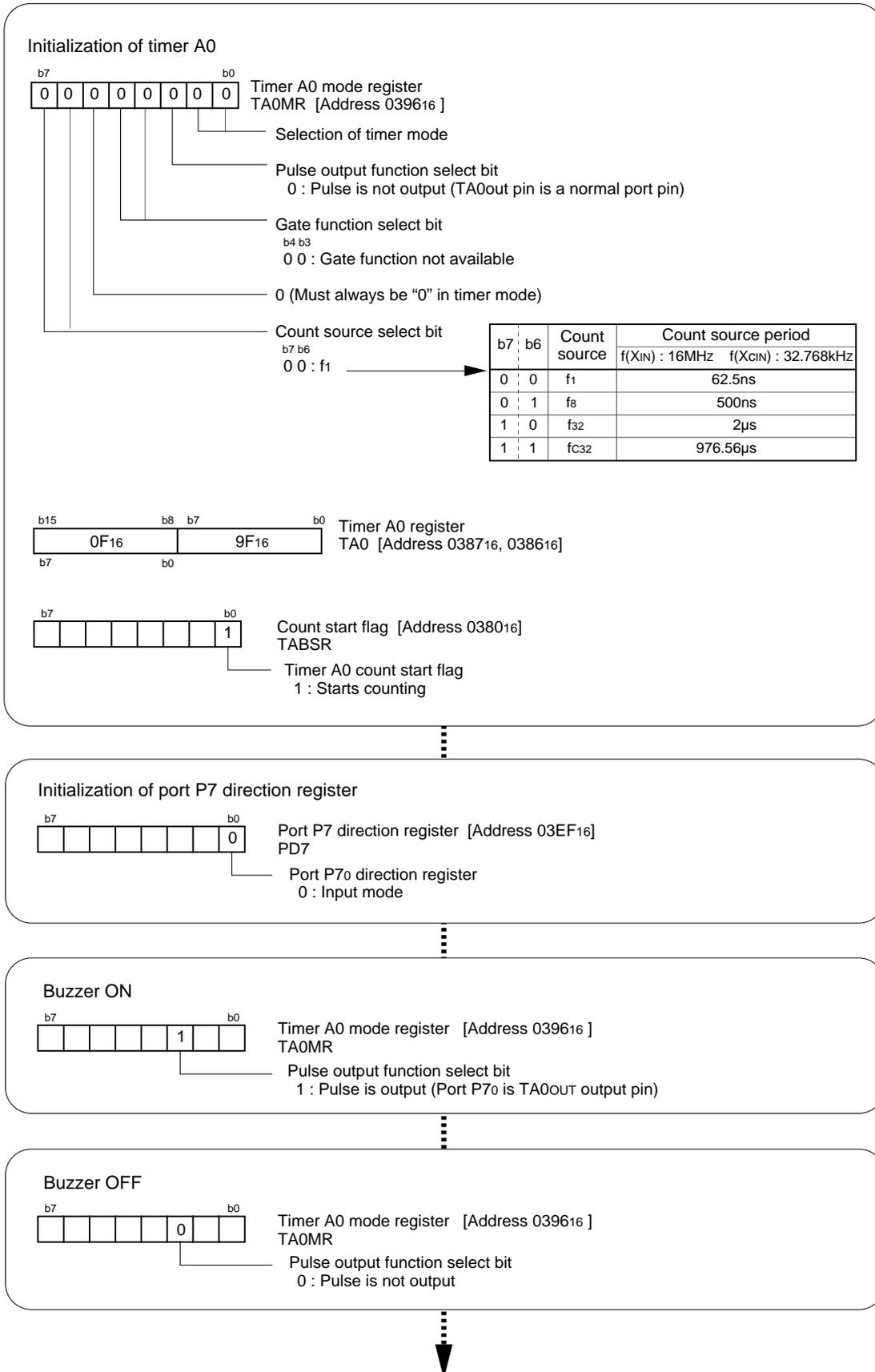


Figure 1. Operation timing of buzzer output

3.0 Set-up procedure




```

;-----
;      BUZZER ON
;-----
BUZZER_ON:
    BSET    mr0_ta0mr      ;Pulse output function select
                        ;Pulse is output (Port P70 is TA0out output pin)
;
MAIN:
    JMP     MAIN
;
;-----
;      BUZZER OFF
;-----
BUZZER_OFF:
    BCLR    mr0_ta0mr      ;Pulse is not output
;
;=====
;      Dummy interrupt processing program
;=====
dummy:
    REIT
;
;*****
;      Setting of fixed vector
;*****
.SECTION    F_VECT, ROMDATA
.ORG       FIXED_VECT_TOP
;
.LWORD     dummy      ;Undefined instruction interrupt vector
.LWORD     dummy      ;Overflow (INT0 instruction) interrupt vector
.LWORD     dummy      ;BRK instruction interrupt vector
.LWORD     dummy      ;Address match interrupt vector
.LWORD     dummy      ;Single-step interrupt vector
.LWORD     dummy      ;Watchdog timer interrupt vector
.LWORD     dummy      ;DBC interrupt vector
.LWORD     dummy      ;NMI interrupt vector
.LWORD     RESET      ;Sets reset vector
;
.END

```

5.0 Reference

Renesas Technology Corporation Semiconductor Home page

<http://www.renesas.com/>

Technical Support

E-mail: support_apl@renesas.com

Data Sheet

M16C/62A group Rev. C.1

(Use the latest version on the Home page: <http://www.renesas.com/>)

User's Manual

M16C/62A group Rev. 1.0

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