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# 3823 Group

# **RRF** Register Application

# 1. Abstract

The following article introduces and shows an example of how to use the RRF Register Application on the 3823 group device.

# 2. Introduction

The explanation of this issue is applied to the following conditions:

Applicable MCU: 3823 Group Frame frequency: 61 Hz

This sample program may include operations of unused bit functions for the convenience of the SFR bit layout. Set the values according to the operational conditions of the user system.



#### 3. Contents

# 3.1 RRF Register Application

Outline: Comparison between how to set display data on the LCD panel and the number of cycles when using the RRF register and instruction.

Specifications:

- •Segment output SEG0 SEG15 and common COM0 COM3 are used.
- •Frame frequency = 61 Hz
- •Duty ratio number = 4, Bias value = 1/3
- •"M3823" is displayed

Number of bytes: 110 bytes when using the RRF instruction, 90 bytes when using the RRF register Number of cycles: 230 cycles when using the RRF instruction, 150 cycles when using the RRF register

Figure 3.1 shows a Segment Allocation Example, Figure 3.2 shows the Circuit Example (When Using External Dividing Resistor), Figure 3.3 shows the LCD Display RAM Map, Figure 3.4 shows a LCD Display RAM Setting Example, Figure 3.5 shows the Relevant Register Settings, and Figure 3.6 and Figure 3.7 show the Control Procedure.

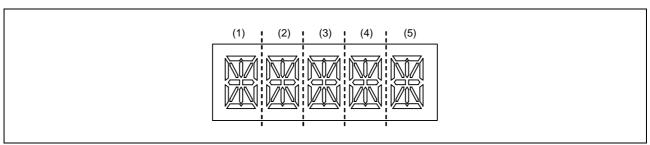


Figure 3.1 Segment Allocation Example

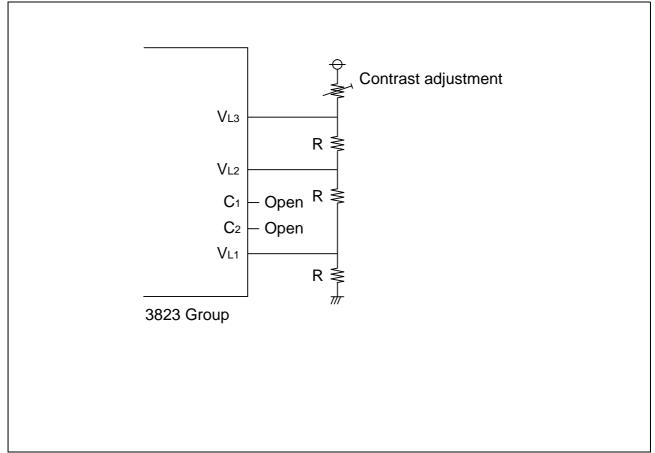


Figure 3.2 Circuit Example (When Using External Dividing Resistor)



	Bit	7	6	5	4	3	2	1	0
Address			COM <sub>2</sub>	COM <sub>1</sub>	COM <sub>0</sub>	СОМз	COM <sub>2</sub>	COM <sub>1</sub>	COM <sub>0</sub>
004016	LRAM0		SE	G <sub>1</sub>	•	SEG <sub>0</sub>			
004116	LRAM1		SE	<b>G</b> 3		SEG <sub>2</sub>			
004216	LRAM2		SE	: <b>G</b> 5		SEG4			
004316	LRAM3		SE	G <sub>7</sub>		SEG <sub>6</sub>			
004416	LRAM4		SE	G <sub>9</sub>		SEG8			
004516	LRAM5		SE	G11		SEG <sub>10</sub>			
004616	LRAM6		SE	G13		SEG <sub>12</sub>			
004716	LRAM7		SE	G15		SEG14			
004816	LRAM8	SEG17				SEG <sub>16</sub>			
004916	LRAM9	SEG19				SEG18			
004A16	LRAM10	SEG21				SEG <sub>20</sub>			
004B <sub>16</sub>	LRAM11	SEG23				SEG22			
004C <sub>16</sub>	LRAM12	SEG25				SEG24			
004D16	LRAM13		SE	G27		SEG <sub>26</sub>			
004E16	LRAM14			G29		SEG28			
004F16	LRAM15		SE	G31		SEG30			

Figure 3.3 LCD Display RAM Map

Address	Bit	7 COM <sub>3</sub>	6 COM <sub>2</sub>	5 COM <sub>1</sub>	COM <sub>0</sub>	3 COM <sub>3</sub>	2 COM <sub>2</sub>	1 COM <sub>1</sub>	O COMo	Disit	
004016	LRAM0	h		f	e	d	COIVIZ	b	a	→(1)	
004016	LRAM1	m	g	- 1	n	k	i	D	i	→(1)	
004116	LRAM2	h	7	f	e	d	C	b	1	→(2)	V <u>-</u>
004216	LRAM3	m	g	<u>'</u>	n	k	i	D	a i	$\rightarrow$ (2)	
004316	LRAM4	h	g	f	e	d	C	b	a	$\rightarrow$ (3)	
004516	LRAM5	m	y I	'	n	k	i	U	i	→(3)	N-445
004616	LRAM6	h	g	f	e	d	C	b	a	→(4)	
004716	LRAM7	m	J J		n	k	i		i	→(4)	
004816	LRAM8	h	g	f	e	d	C	b	a	→(5)	9/7   1 1
004916	LRAM9	m	I		n	k	i		i	→(5)	
004A16	LRAM10	h	g	f	e	d	c	b	a	/(3)	/ <u>/ n ·</u>
004B <sub>16</sub>	LRAM11	m	ĭ		n	k	i		i		
004C16	LRAM12	h	g	f	е	d	c	b	а		
004D16	LRAM13	m	Ĭ		n	k	j		i		
004E16	LRAM14	h	g	f	е	d	C	b	а		
004F <sub>16</sub>	LRAM15	m	Ī		n	k	j		i		

Figure 3.4 LCD Display RAM Setting Example



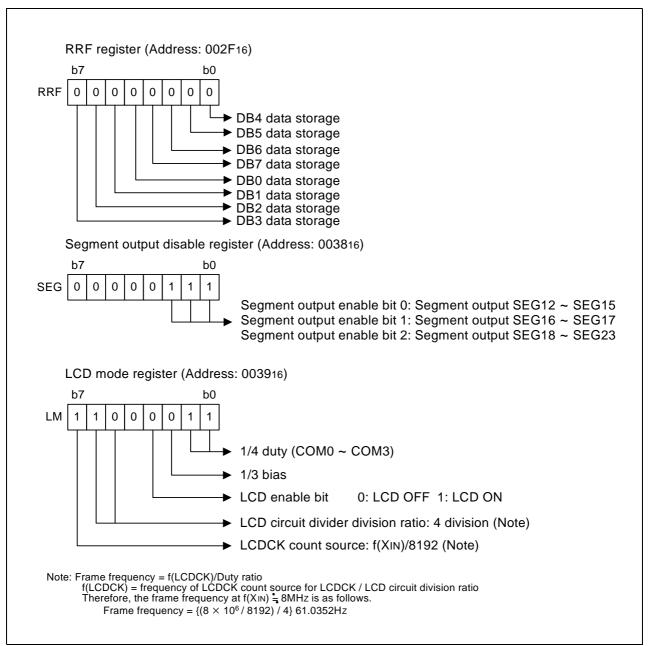


Figure 3.5 Relevant Register Settings



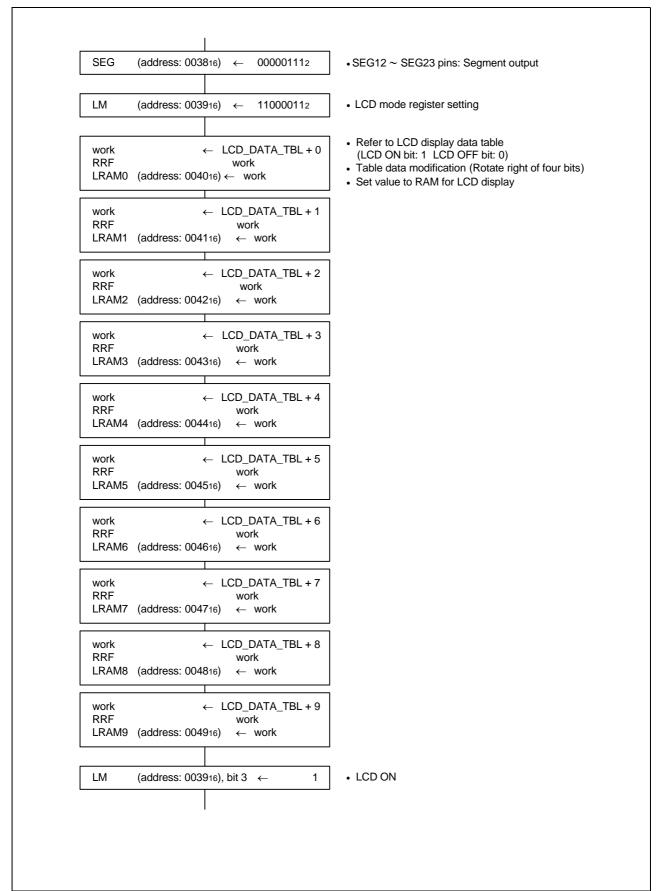


Figure 3.6 Control Procedure When Using RRF Instruction



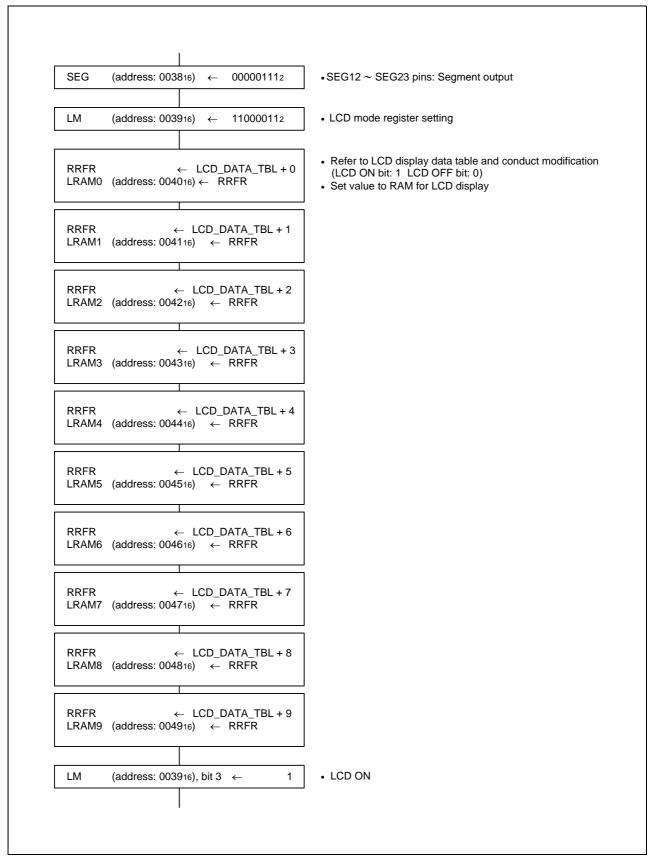


Figure 3.7 Control Procedure When Using RRFR Register



# 4. Sample Programming Code

Download a sample program from the Renesas Technology website. To download, click "Application Notes" in the left side menu on the page of the 3823 Group.

# 5. Reference Document

Datasheet

3823 Group Data sheet

Download the latest version from the Renesas Technology website.

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REVISION HISTORY	3823 Group RRF Register Application
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Rev.	Date	Description					
ixev.	V. Date P		Summary				
1.00	Aug 10, 2006	-	First Edition issued				



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