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Renesas Electronics Corporation

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3803 Group (Specification H)

Pulse Width Modulation (PWM)

1. Abstract

This application note describes how to use the pulse width modulation (PWM) function of the 3803H Group.

2. Introduction

The explanation of this issue is applied to the following conditions:

- Application MCU: 3803H Group (e.g. M38039FFHHP)
- Oscillation Frequency: 8 MHz

3. Contents

3.1 Description of module used

Figure 1 shows a block diagram of PWM module. In this sample task, the following features are used:

- Automatic generation of PWM output by hardware, without software intervention.
- Period and duty cycle of PWM output can be modified.
- If the PWM register or PWM prescaler is updated during PWM output, the pulses will change in the cycle after the one in which the change was made.

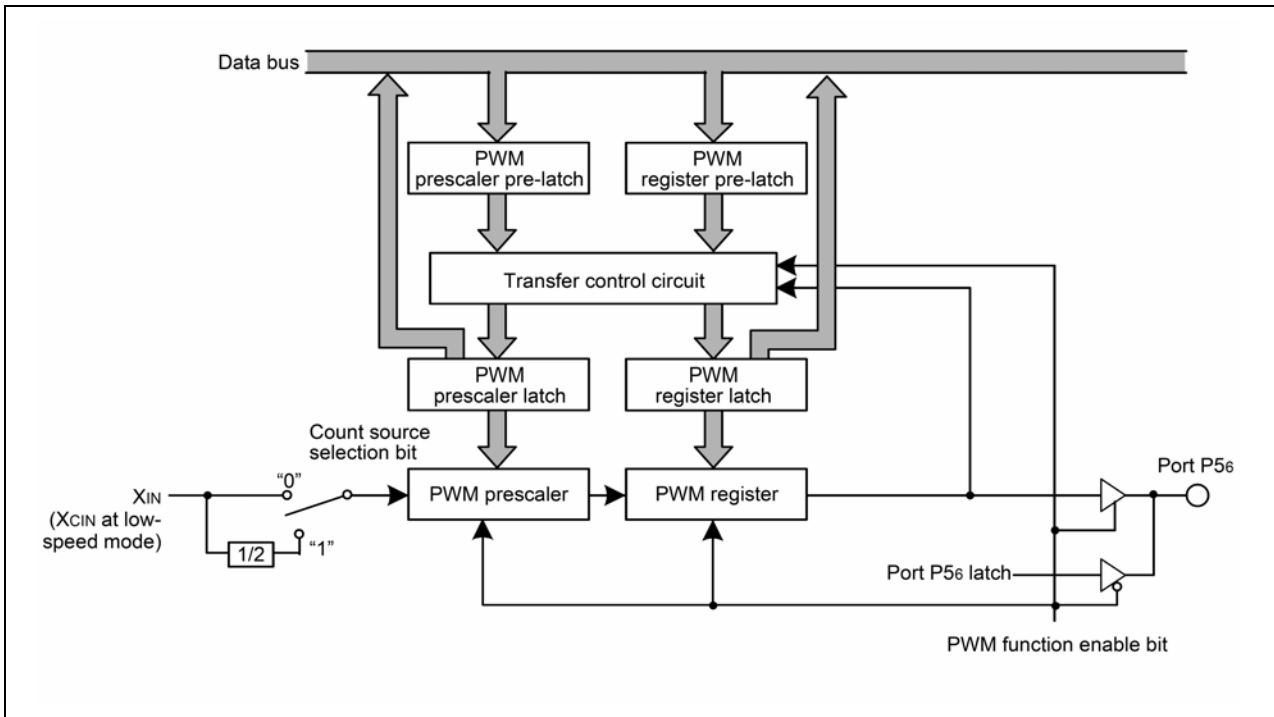


Figure 1 Block Diagram of PWM Module

The registers are described below:

- PWM control register (PWMCON)

PWMCON controls the PWM module. It contains two bits: PWMFE and PWMCS. PWMFE enables or disables the module; PWMCS selects the count source of the PWM between $f(XIN)$ and $f(XIN)/2$.

- PWM prescaler (PREPWM)

PREPWM is a 8-bit auto-reload prescaler. It counts the input from count source selected by the PWMCS bit. The period of the PWM is determined by the value set to the PREPWM.

- PWM register (PWM)

PWM is a 8-bit down-count timer. It counts the output of the PREPWM underflow. Immediately after it reaches 00_{16} , FF_{16} is automatically reloaded. The output is kept at "H" level when the PWM counts between FF_{16} and the value set by the user.

3.2 Formulas for PWM

The following formulas are used to calculate the period and “H” level width of the PWM.

The “n” is the value set in the PWM prescaler (002C₁₆); “m” is the value in the PWM register (002D₁₆).

$$\text{PWM period} = 255 \times (n+1) / f(\text{XIN});$$

$$\text{Output pulse "H" term} = \text{PWM period} \times m / 255$$

3.3 Description of the application example

- An example of the output waveform is shown in Figure 2.
- The period is fixed to 31.875µs and the duty cycle increases from 0 to 1 by 1/256 on each step in every 500µs, then goes back to 0.

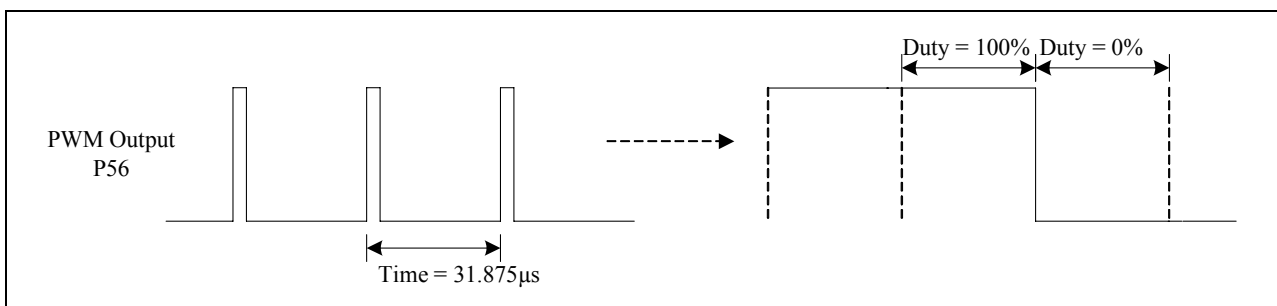


Figure 2 PWM Output Waveform

3.4 Setup of the registers

Figure 3 shows the value of the PWM related registers after the SFR setup at the beginning of the main function.

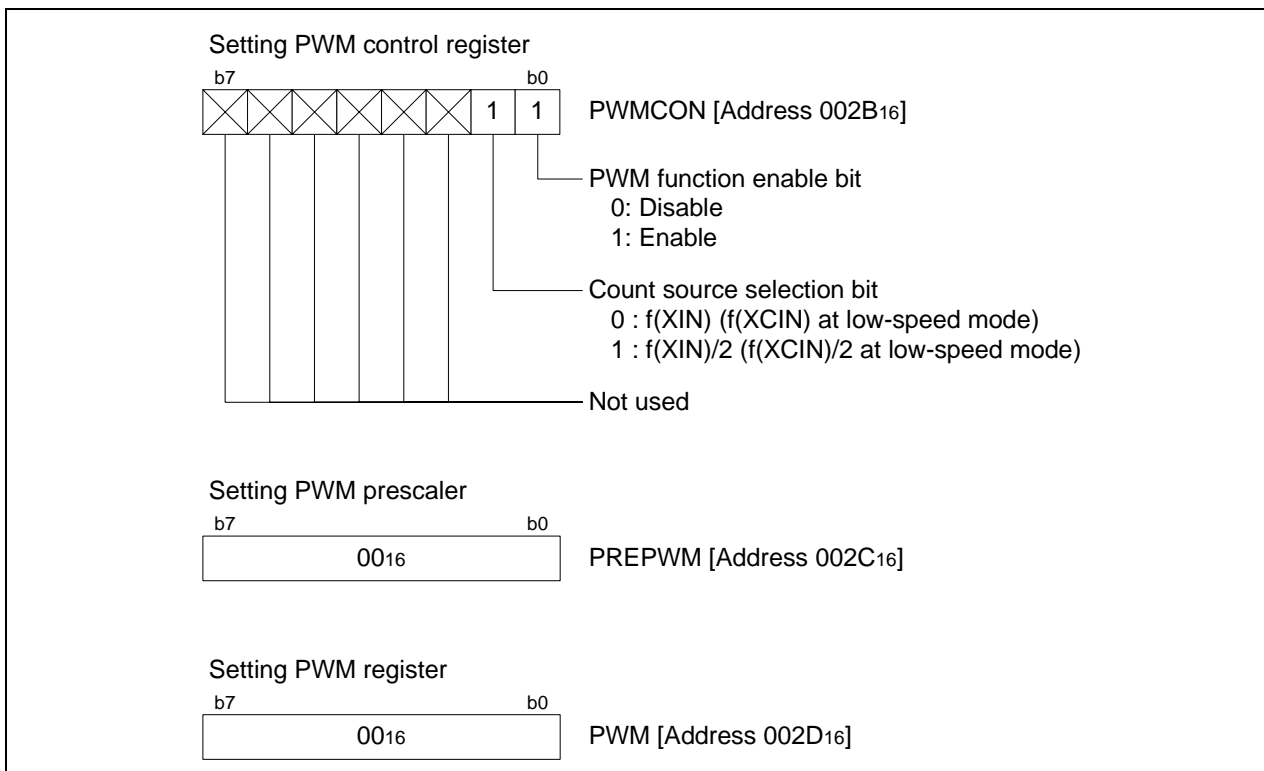


Figure 3 Setting of PWM Registers

4. Flow chart

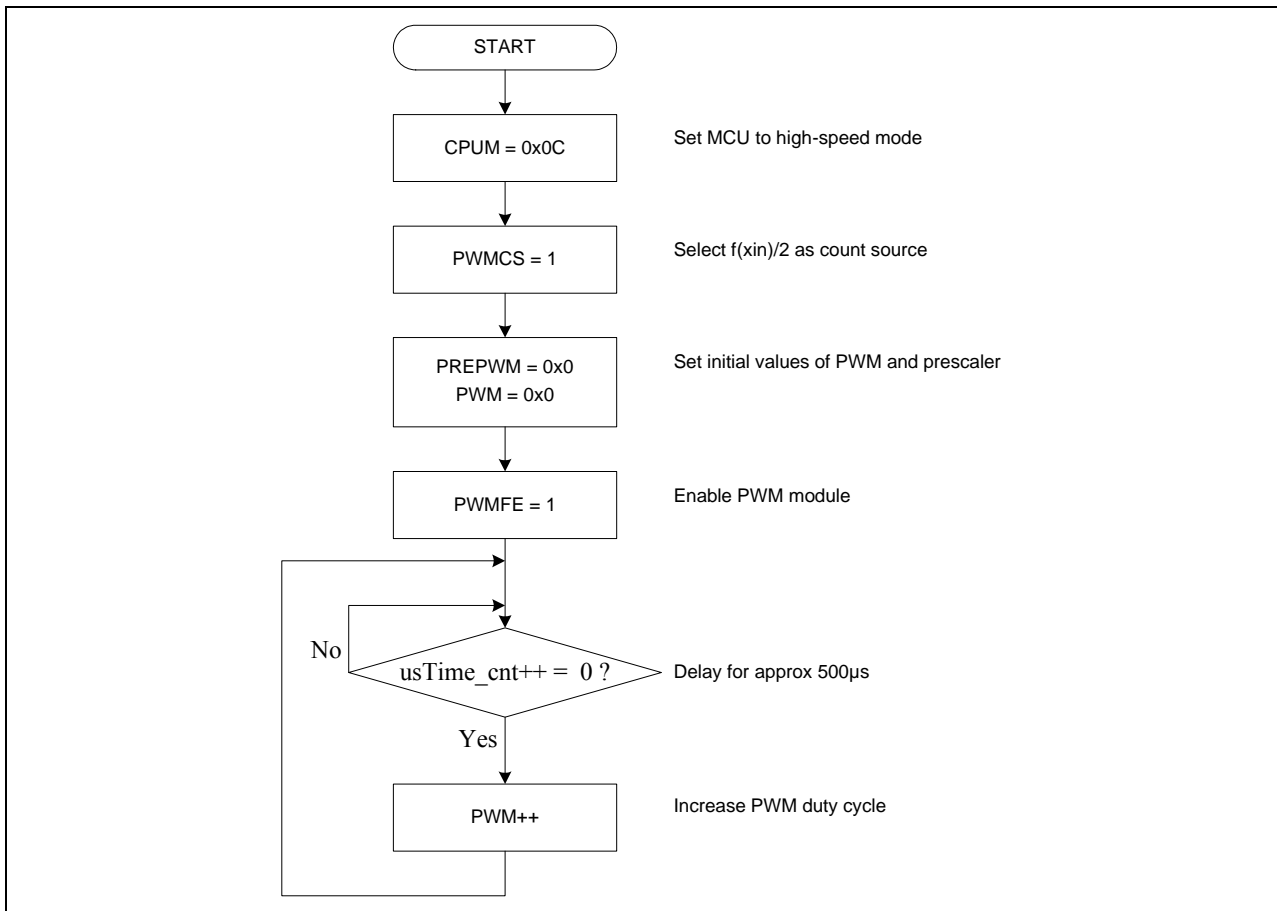


Figure 4 Flow Chat of Main Function

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Revision Record

Rev.	Date	Description	
		Page	Summary
1.00	Aug.21.06	-	First edition issued

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