

RX111

Renesas Starter Kit Sample Code for e2studio

Introduction

Renesas Starter Kits (RSK) is supplied as complete development systems for the selected microcontroller. The kit includes an evaluation board, portable On-Chip Debugger and a set of peripheral sample code.

Target Device

RX111

Development environment

IDE: e2studio Compiler: Renesas RX v1.02.01.000 Hardware: Renesas Starter Kit for RX111

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1. Installation

This section assumes e2studio IDE is already installed on the user's personal computer (PC). Create a new folder and name it as 'RSKRX111_Workspace'. Copy the zipped file 'an_r01an1789eg0100_rx111_rsk.zip', available in the Application Note package downloaded from the website, to this folder. Extract the 'an_r01an1789eg0100_rx111_rsk.zip' file to the RSKRX111_Workspace folder.

2. Creating the Project Workspace

Open e2studio IDE by clicking the Windows Start button, select All Programs > Renesas Electronics e2studio > Renesas e2studio.

e ² Workspace	e Launcher
Select a wor	rkspace
	res your projects in a folder called a workspace. orkspace folder to use for this session.
<u>W</u> orkspace:	C:\Users\QATest\My Documents\e2studio\workspace
<u> U</u> se this a	s the default and do not ask again OK Cancel

Select <OK>.

e² Adm	e ² Administrator Privilege				
?	Administrative privileges are required for correct operation of e2Studio on Windows 7. Please ensure you have appropriate privileges (i.e., Right click renesas-eclipse.exe and choose "Run as Administrator")				
	Do you want to continue?				
	Yes <u>N</u> o				

Select <Yes> to Administrator Privilege dialog.





On the welcome screen select 'Go to the Workbench' icon as shown above.

1. Once the e2studio environment has initialised, right click in the project explorer window and click <Import...>

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2. The Import dialog will now appear. Expand the "General" folder icon, and select "Existing Projects into Workspace", then click 'Next'.

e ² Import	- • •
Select Create new projects from an archive file or directory.	Ľ
Select an import source:	
type filter text	
 ↓ General ↓ Archive File ☆ Existing Projects into Workspace ↓ File System ↓ Preferences ↓ ▷ C/C++ ▷ ▷ CVS ▷ ▷ Run/Debug ▷ ▷ Team 	
(?) < <u>Back</u> <u>Next ></u> <u>Finish</u>	Cancel

3. The Import Dialog will now appear and specify the project to import. Click the "Browse" button and locate the directory: C:\Workspace\RSK\RSKRX111.

And also ensure that the 'Copy projects into workspace' option is ticked, and then click <Finish>



e ² Import	
Import Projects Select a directory to search for existing Eclipse projects.	
Select root directory: C:\WorkSpace\RSK\RSKRX111_e2studio Select archive file:	B <u>r</u> owse
Projects: ADC_Oneshot (C:\WorkSpace\RSK\RSKRX111_e2studio\ADC_Oneshot ADC_Repeat (C:\WorkSpace\RSK\RSKRX111_e2studio\ADC_Repeat) Application (C:\WorkSpace\RSK\RSKRX111_e2studio\Application) Async_Serial (C:\WorkSpace\RSK\RSKRX111_e2studio\Async_Serial) CAC (C:\WorkSpace\RSK\RSKRX111_e2studio\CAC) CAC (C:\WorkSpace\RSK\RSKRX111_e2studio\DAC) DAC (C:\WorkSpace\RSK\RSKRX111_e2studio\DAC) DTC (C:\WorkSpace\RSK\RSKRX111_e2studio\DAC) DTC (C:\WorkSpace\RSK\RSKRX111_e2studio\DTC) DCC (C:\WorkSpace\RSK\RSKRX111_e2studio\DCC) Copy projects into workspace Working sets Add project to working sets Working sets:	Select
< <u>Back</u> <u>Next</u> > <u>Finish</u>	Cancel

The IDE e2studio will load the project.



3. Opening Sample Code and Source Files

Once the project has been opened, the source code and all dependant files can be opened in the editor by expanding the folders in the Project Tree window and double clicking the files listed. All files have been grouped according to their file type.

	- e2studio					
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r_cq_intprq.c	/**************************************					
h r_cq_iodefine.h	<pre>* File Name : Description.txt * Device : R5F51115AxFM</pre>					
h r_cg_macrodriver.h	* Tool-Chain : RX Family C Compiler 1.2.1.0					
Image: book state in the state of the sta	* H/W Platform : RSKRX11					
I r_cg_port_user.c * Note : Application Leading Tool (Applilet) is used to generate code						
Image: book is a construction of the second seco	* for this sample program. The name Applilet is used hereafter.					
⊳ 🔥 r_cg_port.h	 The Applilet details and version number are given in the file * header for each relevant module. 					
Image: style="text-align: center;"> Image: style="text-align: center;"/> Image: s	* Description : Demonstration of the S12AD ADC module, in single shot mode.					
Image: style="text-align: center;"> Image: style="text-align: center;"/> Image: s	* The program configures the ADC to perform oneshot AD					
r_cg_s12adb.c	* conversions and display the results on the LCD.					
⊳ 🚡 r_cg_s12adb.h	* Operation : 1. Compile and download the sample code to the target.					
Ic r_cg_sbrk.c	* start the software.					
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Image: Image: Image: block in the second	* 2. Observe the LCD display - the R5F51115AxFM CPU will					
▷ C r_cg_sci.c	 initially display the ADC reading as 					
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Image: Specific term 0 errors, 14 warnings, 0 others Image: Description 0 errors, 14 warnings, 0 others						
				ADC_Oneshot Release.launch		
				Description.txt		
Macro.html						
📄 makefile.init 📄 RSK Header.txt						

4. Source Code Functionality

Each source code project is specifically written to run on the appropriate RSK. However this source code can be useful as an example of peripheral initialization even without the RSK.

Each sample project will contain a C source file that includes r_cg_main.c' in the name, for example "main_adc12_oneshot.c". This source file will include the C function main() as well as a comment block that describes the function of the sample code.



5. Appendix

Example of comment block with code functionality.

/**************************************
* File Name : Description.txt
* Device : R5F51115AxFM
* Tool-Chain : RX Family C Compiler 1.2.1.0
* H/W Platform : RSKRX111
* Note : Application Leading Tool (Applilet) is used to generate code
* for this sample program. The name Applilet is used hereafter.
* The Applilet details and version number are given in the file
* header for each relevant module.
* Description : Demonstration of the S12AD ADC module, in single shot mode.
* The program configures the ADC to perform oneshot AD
* conversions and display the results on the LCD.
* Operation : 1. Compile and download the sample code to the target.
* Click the 'Resume' icon on the toolbar to
* start the software.
*
* 2. Observe the LCD display - the R5F51115AxFM CPU will
* initially display the ADC reading as
* "AD12 Channel One"
* " Value = 0xxxxx "
*
* 3. Adjust the potentiometer, press SW3 to carry out a
* 12-bit A/D conversion of the ADC channel AN000 and
* observe the change in the value being displayed on the
* LCD.
*
* 4. The user may also examine the ADC conversion result in
* the global variable g_adc_result.
* Repeat Step 3 to carry out further ADC readings.
*



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Revision History

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	1.00	Nov 26, 2013		First edition issued	

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- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.
- 2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
 In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.
 In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at
 - which resetting has been specified.
- 3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access
 these addresses; the correct operation of LSI is not guaranteed if they are accessed.
- 4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal.
 Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.
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