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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SY*-A0039A/E	Rev.	1.00
Title	Errata for User's Manual regarding the USB		Information Category	Technical Notification		
Applicable Product	Renesas Synergy™ S7G2 MCU Group Renesas Synergy™ S5D9 MCU Group Renesas Synergy™ S5D5 MCU Group Renesas Synergy™ S5D3 MCU Group	Lot No. All	Reference Document	S7G2 Microcontroller Group User's Manual Rev.1.40 S5D9 Microcontroller Group User's Manual Rev.1.20 S5D5 Microcontroller Group User's Manual Rev.1.10 S5D3 Microcontroller Group User's Manual Rev.1.00		

The specified Renesas Synergy User's Manual has incorrect statements about the USB.

1. USB 2.0 Full-Speed Module (USBFS) for S7G2, S5D9, S5D5, S5D3

Incorrect

TRNENSEL bit (Transaction-Enabled Time Select*1)

When the USB port is in use for full-speed or low-speed communications, the TRNENSEL bit specifies the timing with which the USBFS issues tokens in a frame (transaction-enabled time).

Set this bit to 1 when a low-speed device is connected. The bit is only valid in host controller mode. Set this bit to 0 in device controller mode.

Correct

When the USB port is in use for full-speed or low-speed communications, the TRNENSEL bit specifies the timing with which the USBFS issues tokens in a frame (transaction-enabled time).

Set this bit to 1 when a low-speed device is connected directly or through a hub. The bit is only valid in host controller mode. Set this bit to 0 in device controller mode.

2. USB 2.0 High-Speed Module (USBHS) for S7G2, S5D9

Incorrect

TRNENSEL bit (Transaction-Enabled Time Select*1)

When the USB port is in use for full- or low-speed communications, the TRNENSEL bit specifies the timing with which the USBFS issues tokens in a frame (transaction-enabled time).

Set this bit to 1 when a low-speed device is connected through a hub. The bit is only valid in host controller mode. Set this bit to 0 when the interface is in use as a device controller.

Correct

When the USB port is in use for full- or low-speed communications, the TRNENSEL bit specifies the timing with which the USBFS issues tokens in a frame (transaction-enabled time).

Set this bit to 1 when a low-speed device is connected directly or through a full-speed hub. The bit is only valid in host controller mode. Set this bit to 0 when the interface is in use as a device controller.