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Peripheral Driver Generator Revised to V.2.01

We have revised Peripheral Driver Generator from V.2.00 to V.2.01. This utility program aids you to create on-chip peripheral I/O drivers.

For an overview of Peripheral Driver Generator, see:

<http://www.renesas.com/pdg>

The above URL is one of our global sites.

1. Description of Revision

The revised product supports the RX62N group of MCUs. The following peripheral I/O modules of these MCUs are supported:

- Clock generator circuit
- Voltage detection circuit (LVD)
- Low power consumption
- Interrupt control unit (ICUa)
- Buses
- DMA controller (DMACA)
- EXDMA controller (EXDMAC)
- Data transfer controller (DTCa)
- I/O ports
- Multi-function timer pulse unit 2 (MTU2)
(Complementary PWM mode and reset synchronous PWM mode excluded.)
- Port output enable 2 (POE2)
- Programmable pulse generator (PPG)
- 8-bit timer (TMR)
- Compare match timer (CMT)
- Real-time clock (RTC)
- Watchdog timer (WDT)
- Independent watchdog timer (IWDT)
- Serial communication interface (SCIa)
- CRC calculator (CRC)
- I2C bus interface (RIIC)
- Serial peripheral interface (RSPI)
- 12-bit A/D converter (S12AD)
- 10-bit A/D converter (ADa)

- D/A converter

2. Host System Requirements

Computer: IBM PC/AT or compatible

OS: Windows(R) XP, Windows Vista(R), or Windows(R) 7

3. How to Obtain and Install the Product

Obtain the installer program of the product in either of the following ways:

(1) Use AutoUpdate Utility. This service will be available on and after March 1, 2011.

(2) Download the installer program of the product from:

http://www.renesas.com/pdg_download

Then execute it. The content of the above Web site will be updated on February 21, according to the revision of the product.

The above URL is one of our global sites.

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