

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A0104A/E	Rev.	1.00
Title	User's Manual Hardware correction : Supported interface for SD channel 1		Information Category	Technical Notification		
Applicable Product	RZ/G2L Group RZ/G2LC Group RZ/V2L Group RZ/G2UL Group RZ/A3UL Group RZ/Five Group	Lot No.	Reference Document	RZ/G2L Group, RZ/G2LC Group, RZ/V2L Group User's Manual: Hardware Rev.1.20		
		All lots		RZ/G2UL Group, RZ/A3UL Group, RZ/Five Group User's Manual: Hardware Rev.1.10		

This technical update describes document corrections of the following User's Manual: Hardware.

RZ/G2L Group, RZ/G2LC Group User's Manual: Hardware Rev.1.20	(R01UH0914EJ0120)
RZ/V2L Group User's Manual: Hardware Rev.1.20	(R01UH0936EJ0120)
RZ/G2UL Group User's Manual: Hardware Rev.1.10	(R01UH0968EJ0110)
RZ/A3UL Group User's Manual: Hardware Rev.1.10	(R01UH0973EJ0110)
RZ/Five Group User's Manual: Hardware Rev.1.10	(R01UH0986EJ0110)

1. Section 40. SD/MMC Host Interface, the following description is fixed.

[From]

### 40.1 Overview

#### 40.1.1 Features

- SD memory/IO card interface (1-bit/4-bit SD bus)
- SD, SDHC, and SDXC SD memory card access supported
- Default, high-speed, UHS-I/SDR50, and SDR104 transfer modes supported
- SD clock (SD\_CLK) frequency =  $SDx\phi/4 \text{ frequency}/2^n$  (n = 0 to 9) (x = 0, 1)
- Error check function: CRC7 (for command/response), CRC16 (for data)
- Interrupt request: 2
- Card detect function
- Write protect supported
- MMC interface (1-/4-/8-bit MMC bus)

NOTE

Channel 1 only supports a 1- or 4-bit MMC bus.

- e-MMC device access supported
- Backward-compatible, high-speed, HS200 transfer modes supported
- High-priority interrupt (HPI) supported

[To]

### 40.1 Overview

#### 40.1.1 Features

- 2 channels
- Channel 0 supports SDHI / e-MMC
- Channel 1 supports SDHI

Add

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- Default, high-speed, UHS-I/SDR50, and SDR104 transfer modes supported
- SD clock (SD\_CLK) frequency =  $SDx\phi/4 \text{ frequency}/2^n$  (n = 0 to 9) (x = 0, 1)
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- Interrupt request: 2
- Card detect function
- Write protect supported
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Delete

NOTE

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- Backward-compatible, high-speed, HS200 transfer modes supported
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