

RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-RL*-A064A/E	Rev.	1.00
Title	RAA604S00 Direction of use		Information Category	Technical Notification		
Applicable Product	RAA604S00	Lot No.	Reference Document	RAA604S00 User's Manual: Hardware Rev.1.10 R01UH0567EJ0110 (Feb 2016)		
		All lots				

This document describes correction of errors in the RAA604S00 User's Manual: Hardware Rev.1.10 (R01UH0567EJ0110).

Corrections

Applicable Item	Applicable Page	Contents
3.4.81 Clock output control register (CLKOUTCON)	Page 92	Function restriction

Document Improvement

The above corrections will be made for the next revision of the User's Manual: Hardware.

Corrections in the User's Manual: Hardware

No.	Corrections and Applicable Items		Pages in this document for corrections
	Item	R01UH0567EJ0110	
1	Function restrictions of clock output (CLKOUT) have been added.	Page 92	Page 3

Incorrect: Strike-through, Correct: Bold

Revision History

RAA604S00 User's Manual: Hardware Rev.1.10 Correction for Incorrect Description Notice

Document Number	Date	Description
TN-RL*-A064A/E	Oct. 17, 2016	First edition issued No.1 in correction (This notice)

1. 3.4.81 Clock output control register (CLKOUTCON) (Page 92)

Incorrect:

3.4.81 Clock output control register (CLKOUTCON)

This register controls the clock output.

The clock can be output from the GPIO0 pin by using the clock output enable bit.

The output clock can be selected between the clock output select bit 0 and the clock output select bit 1.

CLKOUTCON register is set via serial interface in 8-bit units.

Reset signal generation sets this register to 00H.

Correct

3.4.81 Clock output control register (CLKOUTCON)

This register controls the clock output.

The clock can be output from the GPIO0 pin by using the clock output enable bit.

It cannot be used in RF transmission/reception mode.

The output clock can be selected between the clock output select bit 0 and the clock output select bit 1.

CLKOUTCON register is set via serial interface in 8-bit units.

Reset signal generation sets this register to 00H.