

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SH7-A795A/E	Rev.	1.00
Title	Amendments to the SH7137 Group Hardware Manual		Information Category	Technical Notification		
Applicable Product	SH7137 Group	Lot No.	Reference Document	SH7137 Group Hardware Manual (REJ09B0402-0300)		
	SH7136 Group	All lots				
	SH7132 Group					
	SH7131 Group					

As there are some errors in the hardware manuals for the above products, we would like to announce the corrections.

1. The description under (3) Programming Execution in 22.4.3, Programming/Erasing Interface Parameters, of Section 22, Flash Memory, is amended as follows.

[Before amendment]

2. The program data for the user MAT must be prepared in the consecutive area.

The program data must be in the consecutive space, which can be accessed by using the MOV.B instruction of the CPU, and is not the flash memory space.

[After amendment]

2. The program data for the user MAT must be prepared in the consecutive area.

The program data must be in the consecutive space, which can be accessed by using the MOV.L instruction of the CPU, and is not the flash memory space.

2. The description in Figure 22.9, Programming/Erasing Overview Flow, in Section 22, Flash Memory, was amended as follows.

[Before amendment]

4. Programming/erasing is executed only in the on-chip RAM.

However, if the program data is in a consecutive area and can be accessed by the MOV.B instruction of the CPU like SRAM/ROM, the program data can be in an external space.

[After amendment]

4. Programming/erasing is executed only in the on-chip RAM.

However, if the program data is in a consecutive area and can be accessed by the MOV.L instruction of the CPU like SRAM/ROM, the program data can be in an external space.