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RENESAS SEMICONDUCTOR RELIABILITY REPORT

GROUP: RL78/G12

DEVICE : R5F103XXX

APPLICATION: Consumer / Industry

Quality Assurance Div. Renesas Electronics Corporation



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Table. Reliability test results (QFN)

Table. Remarkly test results (4.11)								
Test Items	Reference	Test Conditions	Results Failure/Size	Comment				
High Temperature Operating Life (HTOL)	JESD22-A108	Ta=125 ℃, Vccmax, 1000 hrs	a=125 ℃, Vccmax, 1000 hrs 0/22					
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150 ℃, 1000 hrs	0/22					
Temperature Humidity bias (THB) (*1)	JESD22-A101	Ta=85 ℃, RH=85 %, Vccmax, 1000 hrs	0/22					
Temperature Cycling (TC) (*1)	JESD22-A104	Ta=-65 $^{\circ}$ to 150 $^{\circ}$, 300 cycles	0/22					
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-150 mA	0/3					
Electrostatic discharge (ESD-HBM)	JS-001	1.5 kΩ, 100 pF, +/-2000 V, 1 time	0/3	Class: 2				
Electrostatic discharge (ESD-CDM)	JESD22-C101	+/-500V,1time	0/3	Class: C2				
Solderability (SD)	1 1-\$11)-007 1745 (5 s \$00der coverage \$95 %		0/5					
Resistance to Soldering Heat JESD22-A113, (PC) J-STD-020		MSL3(Moisture Sensitivity Level 3)	0/22					

Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .

^{*1)} With preconditioning per JESD22-A113, MSL 3
•It is tested to confirm that all the samples are satisfied with an individual product specification.



Table. Reliability test results (SOP)

Test Items	Reference	Test Conditions	Results Failure/Size	Comment
High Temperature Operating Life (HTOL)	JESD22-A108	Ta=125 ℃, Vccmax, 1000 hrs	0/22	
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150 ℃, 1000 hrs	0/22	
Temperature Humidity bias (THB) (*1)	JESD22-A101	Ta=85 ℃, RH=85 %, Vccmax, 1000 hrs	0/22	
Temperature Cycling (TC) (*1)	JESD22-A104	Ta=-65 $^{\circ}$ C to 150 $^{\circ}$ C , 300 cycles	0/22	
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-150 mA	0/3	
Electrostatic discharge (ESD-HBM)	JS-001	1.5 kΩ, 100 pF, +/-2000 V, 1 time 0/3		Class: 2
Electrostatic discharge (ESD-CDM)		+/-500V,1time 0,		Class: C2
Solderability (SD)	1 1-511)-007 1745 (5 5 501der coverage >45 %		0/5	
Resistance to Soldering Heat JESD22-A113, (PC) J-STD-020		MSL3(Moisture Sensitivity Level 3)	0/22	

Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .

^{*1)} With preconditioning per JESD22-A113, MSL 3
•It is tested to confirm that all the samples are satisfied with an individual product specification.

The failure rate of the device in an actual use condition can be estimated by the below procedure.

•Equation for the failure rate estimation (λ)

$$\lambda = \lambda b \times \pi T$$
 (FIT)

①Unique failure rate (λb)

$$\lambda b = 3.8 \text{ FIT}$$

Unique failure rate at Ta=55 ℃ using 60 % confidence level.

②Temperature term (π T)

$$\pi T = \exp\{11600 \times Ea \times (1/(273+55)-1/(273+Ta))\}$$

Ea : Activation energy (eV)
Ta : Ambient temperature ($^{\circ}$ C)

π T simplified chart as Ea=0.7 eV												
Ta (℃)	40	50	55	60	65	70	75	80	85	90	100	110
πТ	0.31	0.68	1	1.45	2.08	2.95	4.15	5.77	7.96	10.88	19.82	34.99

·MTTF (Mean Time To Failure)

$$MTTF = 1/\lambda$$



Reference about Renesas package code

Package type	Package code *1	
Lead type plastic package	QFP	PxQP
	SOP	PxSP
Non-lead type plastic package	QFN	PxQN
Grid array type plastic package	BGA	PxBG
	LGA	PxLG

^{*1.} First four digit

Table. Product list

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	Group	Product part number	Package code	No	Group	Product part number	Package code
1	RL78/G12	R5F10366ASM	PTSP0020J*	51			
2	RL78/G12	R5F10366ASP	PLSP0020J*	52			
3	RL78/G12	R5F10366DSP	PLSP0020J*	53			
4	RL78/G12	R5F10367ASM	PTSP0020J*	54			
5	RL78/G12	R5F10367ASP	PLSP0020J*	55			
6	RL78/G12	R5F10367DSP	PLSP0020J*	56			
	RL78/G12	R5F10368ASM	PTSP0020J*	57			
8	RL78/G12	R5F10368ASP	PLSP0020J*	58			
9	RL78/G12	R5F10368DSP	PLSP0020J*	59			
10	RL78/G12	R5F10369ASM	PTSP0020J*	60			
11	RL78/G12	R5F10369ASP	PLSP0020J*	61			
12	RL78/G12	R5F10369DSP	PLSP0020J*	62			
13	RL78/G12	R5F1036AASM	PTSP0020J*	63			
14	RL78/G12	R5F1036AASP	PLSP0020J*	64			
	RL78/G12	R5F1036ADSP	PLSP0020J*	65			
	RL78/G12	R5F10377ANA	PWQN0024K*	66			
	RL78/G12	R5F10377DNA	PWQN0024K*	67			
	RL78/G12	R5F10378ANA	PWQN0024K*	68			
	RL78/G12	R5F10378DNA	PWQN0024K*	69			
	RL78/G12	R5F10379ANA	PWQN0024K*	70			
	, RL78/G12	R5F10379DNA	PWQN0024K*	71			
	RL78/G12	R5F1037AANA	PWQN0024K*	72			
	RL78/G12	R5F1037ADNA	PWQN0024K*	73			
	RL78/G12	R5F103A7ASP	PLSP0030J*	74			
	, RL78/G12	R5F103A7DSP	PLSP0030J*	75			
	RL78/G12	R5F103A8ASP	PLSP0030J*	76			
	RL78/G12	R5F103A8DSP	PLSP0030J*	77			
	RL78/G12	R5F103A9ASP	PLSP0030J*	78			
	RL78/G12	R5F103A9DSP	PLSP0030J*	79			
	RL78/G12	R5F103AAASP	PLSP0030J*	80			
	, RL78/G12	R5F103AADSP	PLSP0030J*	81			
32	,			82			
33				83			
34				84			
35				85			
36				86			
37				87			
38				88			
39				89			1
40				90			1
41				91			
42				92			
43				93			
44				94			
45				95			
46				96			1
47				97			1
48		†		98	1		†
49		<u> </u>		99			1
		ļ	1	100	+		