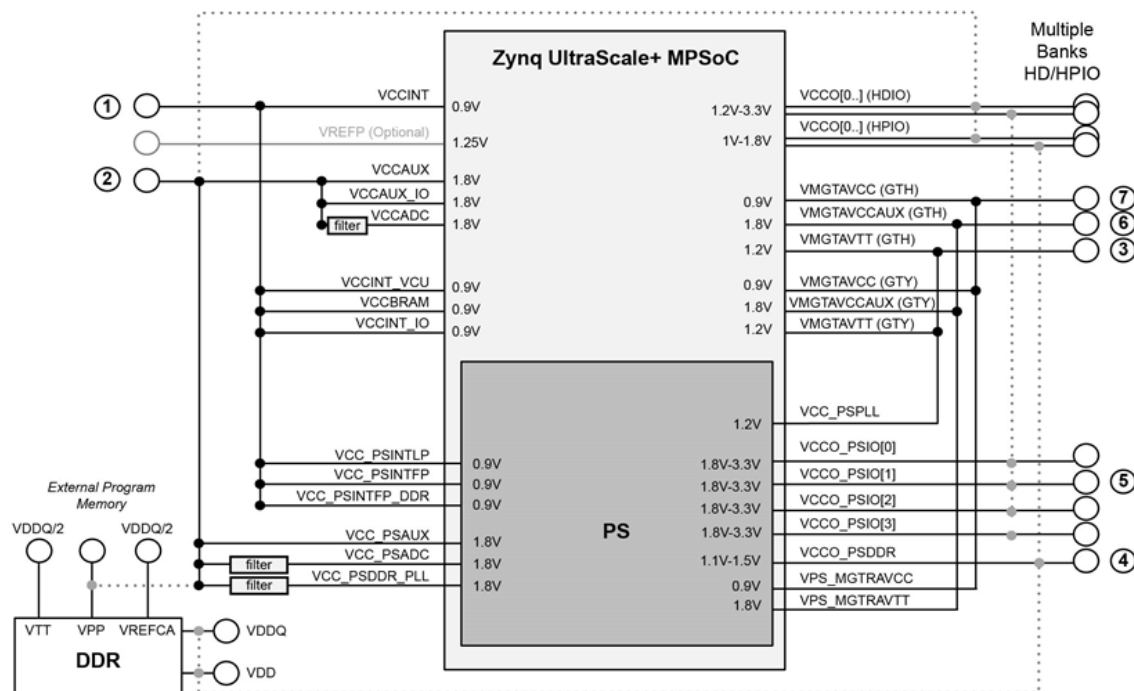


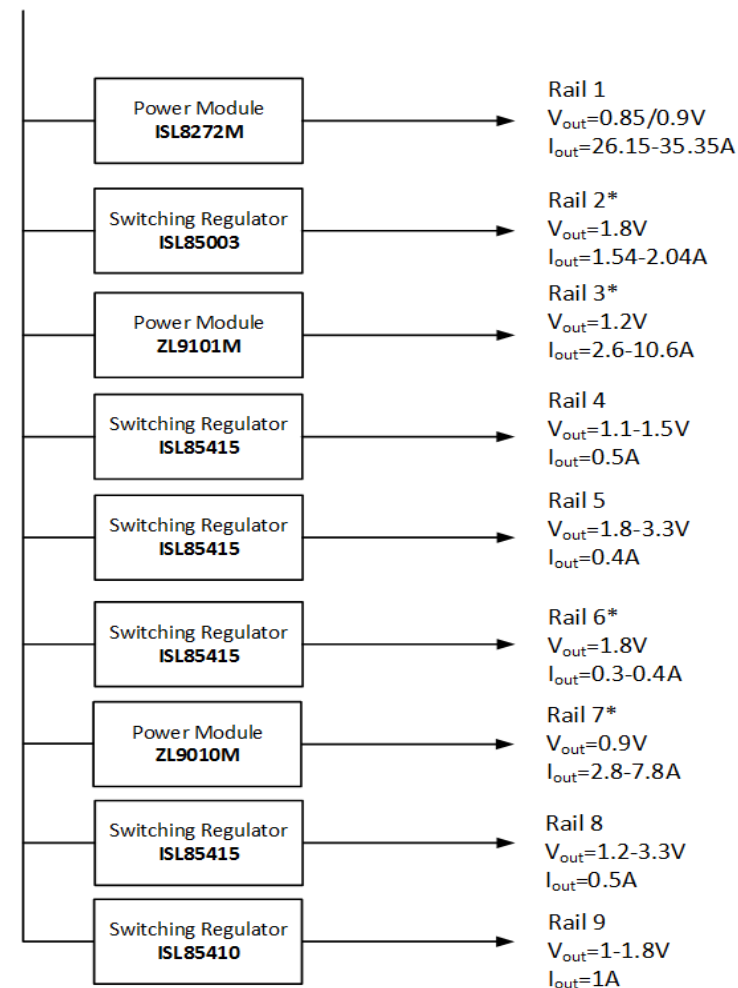
USECASE 3 #4



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Rail	Voltage	Power Supply Rails
Rail 1	0.85/0.9V	VCCINT, VCC_PSINTFP, VCC_PSINTLP, VCC_PSINTFP_DDR, VCCINT_VCU, VCCINT_IO, VCCBRAM
Rail 2*	1.8V*	VCC_PSAUX, VCCAUX, VCCAUX_IO, VCC_PSDDR_PLL, VCCADC, VCC_PSADC
Rail 3*	1.2V*	VMGTAVTT(GTH), VMGTAVTT(GTY), VCC_PSPLL
Rail 4	1.1-1.5V	VCCO_PSDDR
Rail 5	1.8-3.3V	VCCO_PSI0
Rail 6*	1.8V*	VMGTAVCCAUX(GTH), VMGTAVCCAUX(GTY), VPS_MGTRAVTT
Rail 7*	0.9V*	VMGTAVCC(GTH), VMGTAVCC(GTY), VPS_MGTRAVCC
Rail 8	1.2-3.3V	HDIO_VCCO
Rail 9	1-1.8V	HPPIO_VCCO

12 V_{in}

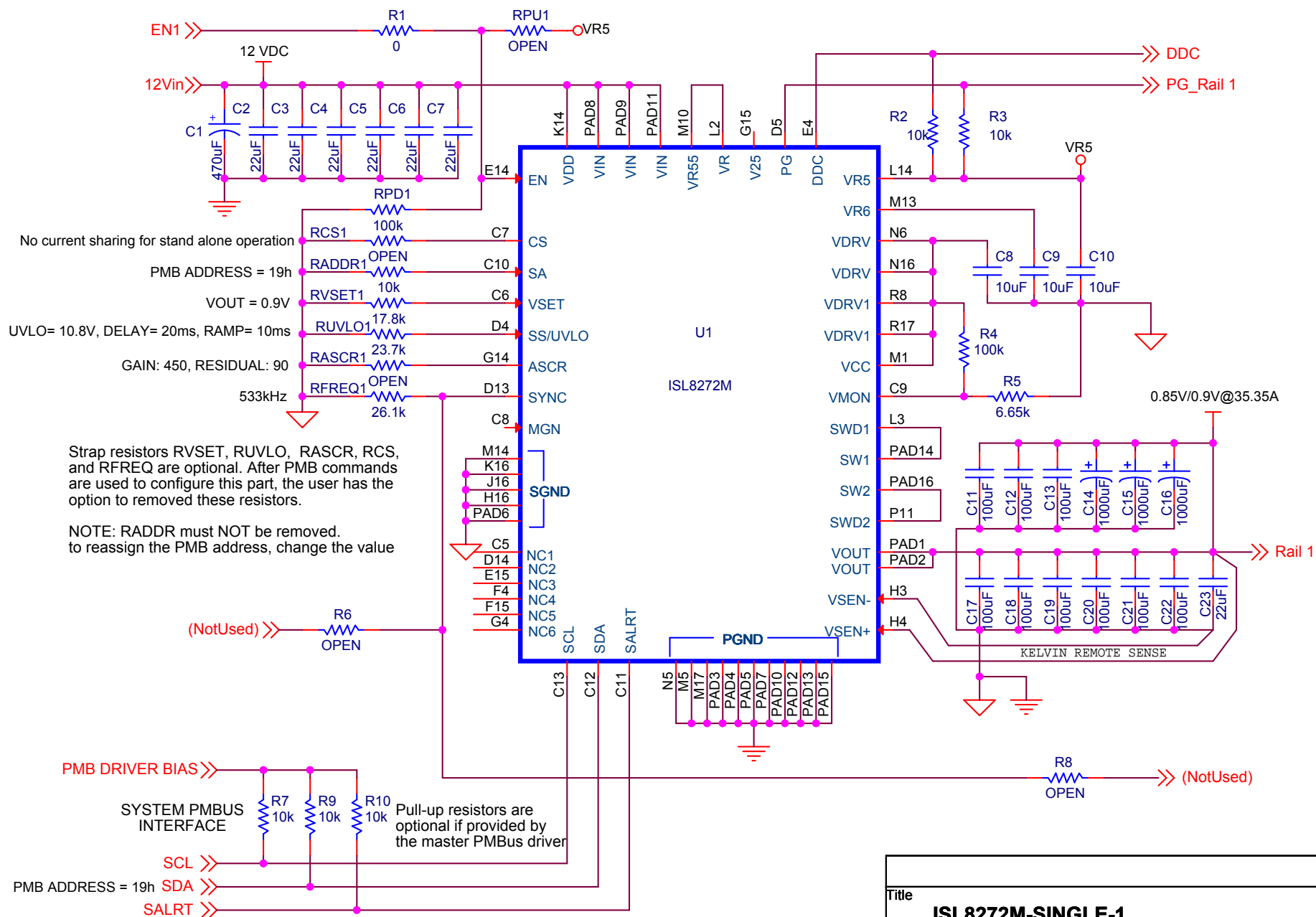


Title ZYNQ US+ MPSoC Power Rails		
Size B	Document Number <Doc>	Rev A
Date: Thursday, March 09, 2017	Sheet 1	of 10

If an external ENABLE is not used, resistor RPU is a pullup for initial evaluation without PMB commands.

Remove RPU1 to make EN low allowing the use of PMB commands.

ISL8272M SINGLE OUTPUT (50A)



Title

ISL8272M-SINGLE-1

Size
A

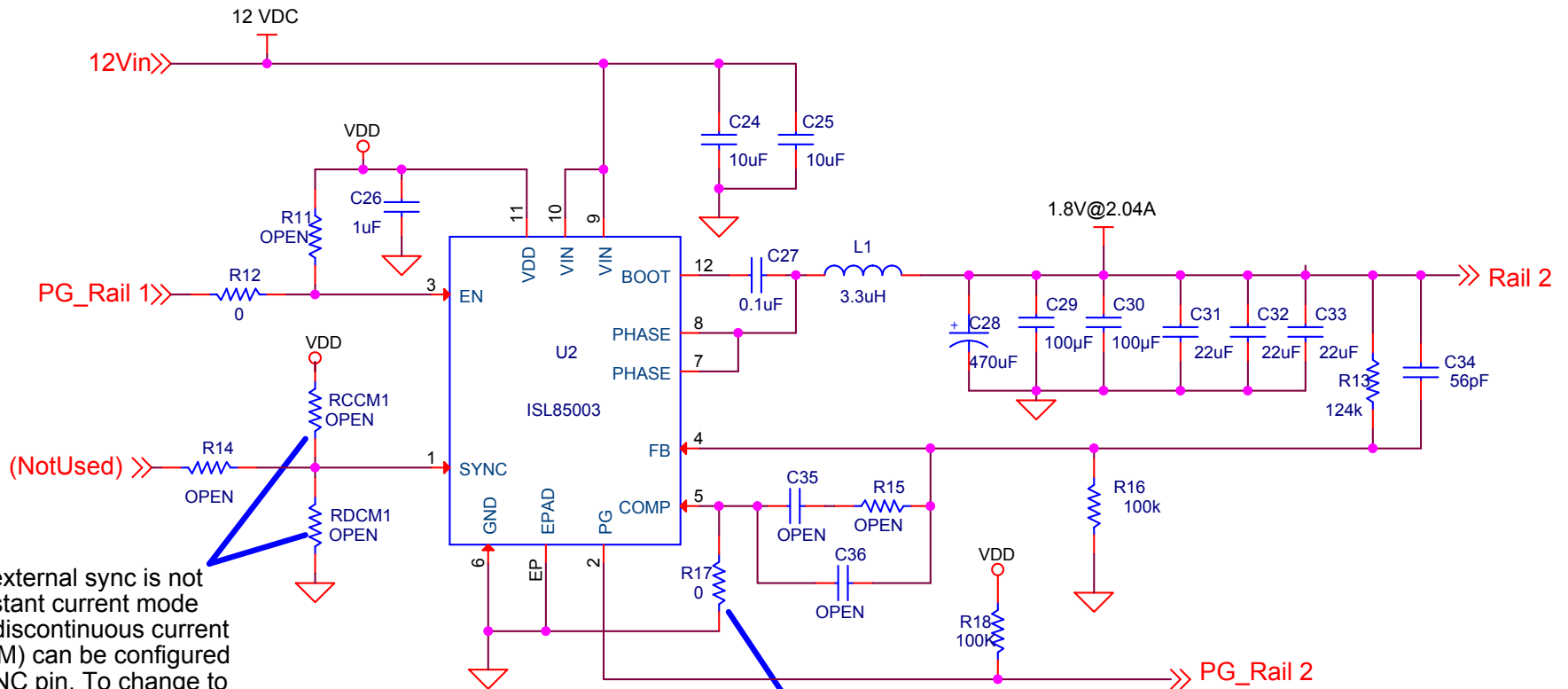
Document Number

Rev

Date:

Sheet 2 of 10

ISL85003 SINGLE OUTPUT (3A)



When an external sync is not used, constant current mode (CCM) or discontinuous current mode (DCM) can be configured on the SYNC pin. To change to DCM mode, remove RCCM, and change RDCM to 0 ohms.

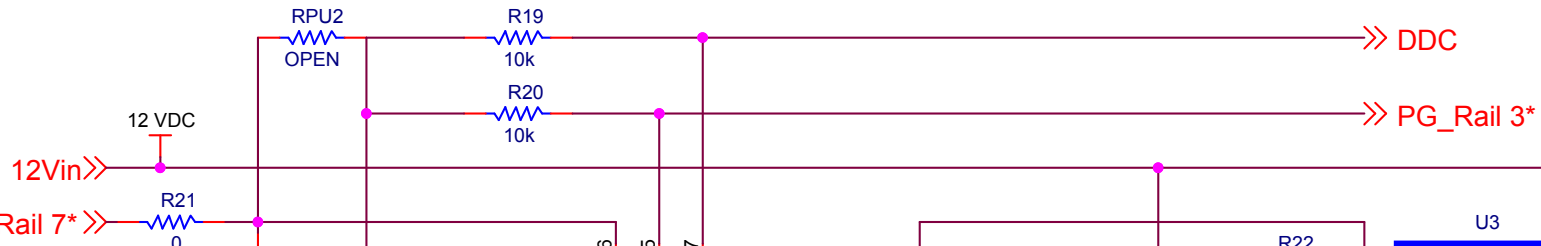
If custom compensation is desired, remove the ground connection on the COMP pin to enable external compensation.

OPEN value components on the COMP pin are place holders if external compensation is used.

Programmable Soft Start is available in the ISL85003A (without synchronization)

Title		
ISL85003-SINGLE-1		
Size	Document Number	Rev
A	<Doc>	<RevCo
Date:	Thursday, March 09, 2017	Sheet 3 of 10

ZL9101M Digital Module (12A)



If an external ENABLE is not used, RENPU is an EN pullup for evaluation without PMB commands.
Remove RENPU for EN = 0 to use PMB commands.

PMB ADDRESS = 1Ah

VOUT = 1.2V

571kHz

Strap resistors RVSET and RFREQ are optional. After PMB commands are used to configure this part, the user has the option to removed these resistors.

NOTE: RADDR must NOT be removed. To reassign the PMB address, change the value.

(NotUsed) >>

R24
OPEN

R25
OPEN

(NotUsed) >>

PMB DRIVER BIAS >>

SYSTEM PMBUS
INTERFACE

SCL >>

SDA >>

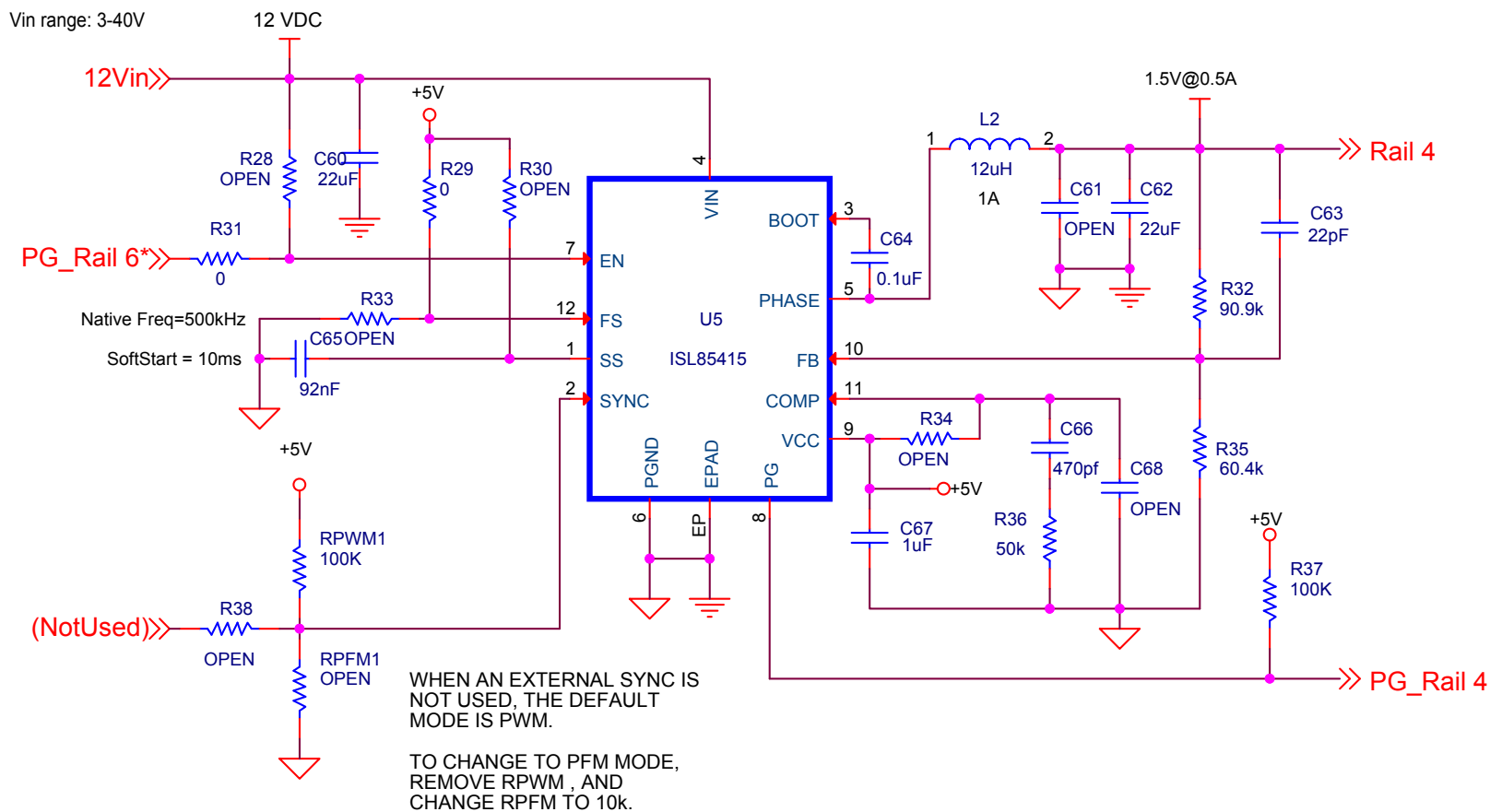
PMB ADDRESS = 1Ah

Hardware SALRT not available in the ZL9101M

Pull-up resistors are optional if provided by the master PMBus driver

Title		
ZL9101M-SINGLE-1		
Size	Document Number	Rev
A		
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ISL85415 SINGLE OUTPUT (500mA)



Title ISL85415-SINGLE-1

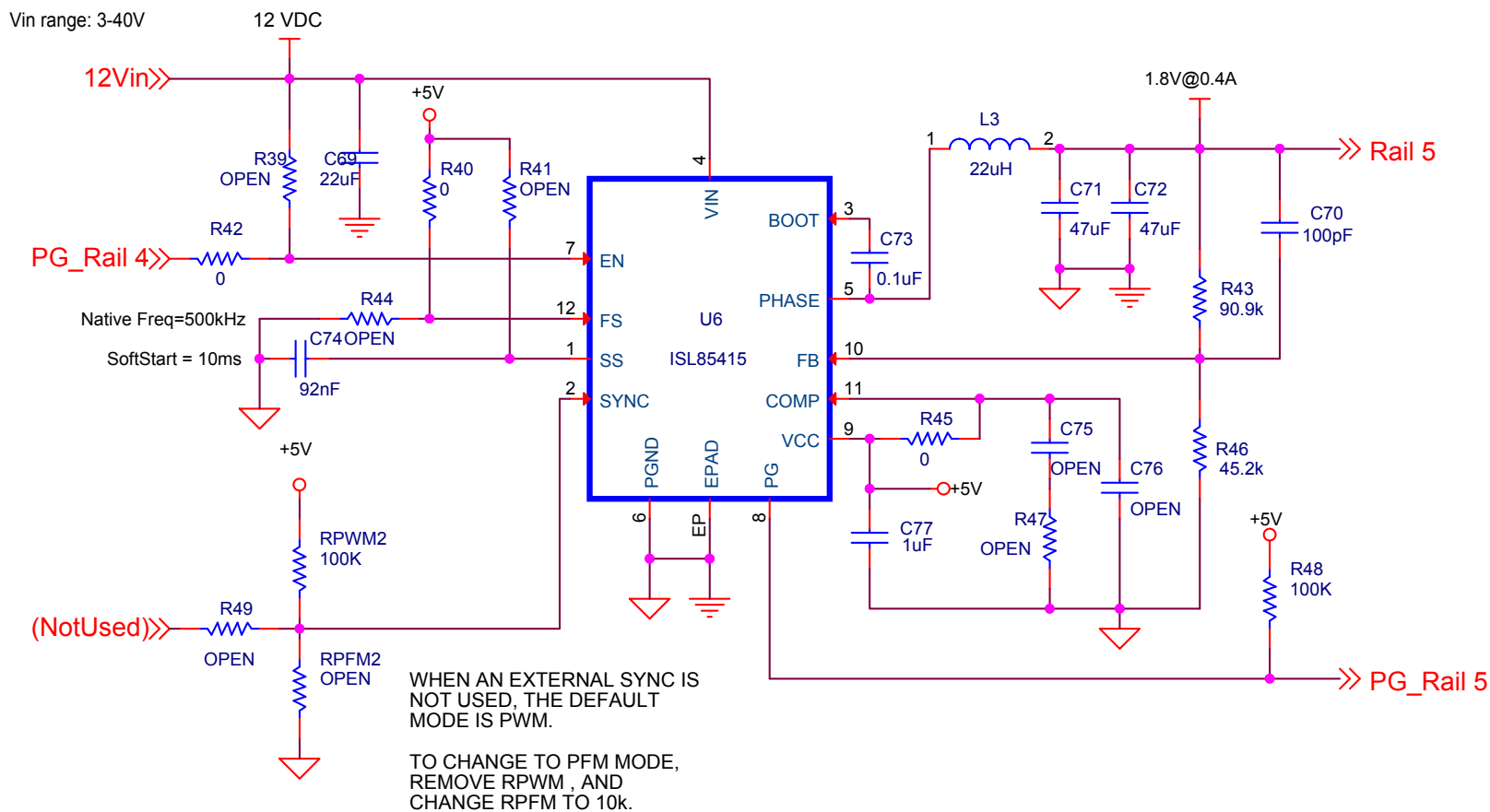
Size A Document Number

Rev

Date: Thursday, March 09, 2017

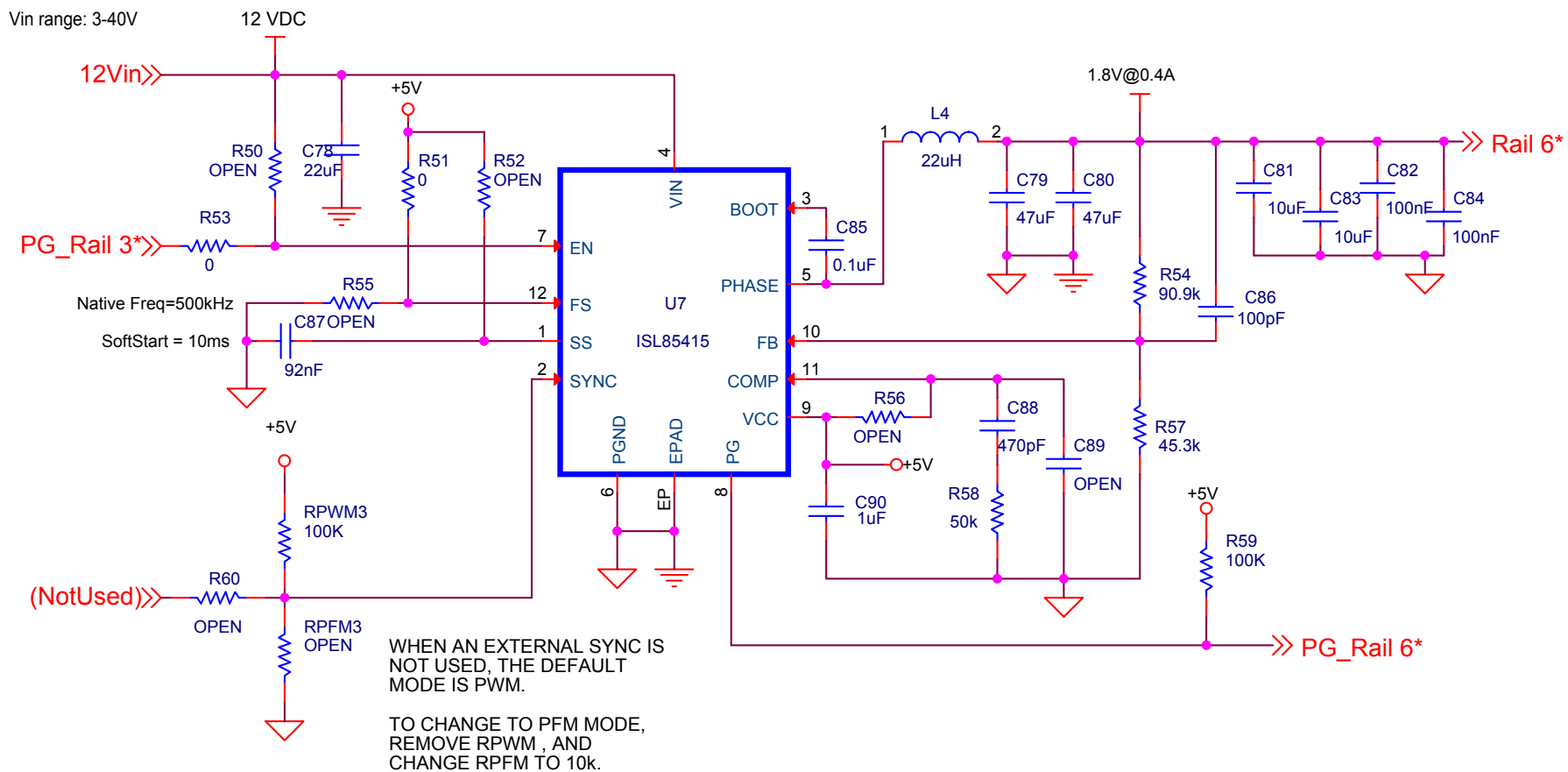
Sheet 5 of 10

ISL85415 SINGLE OUTPUT (500mA)



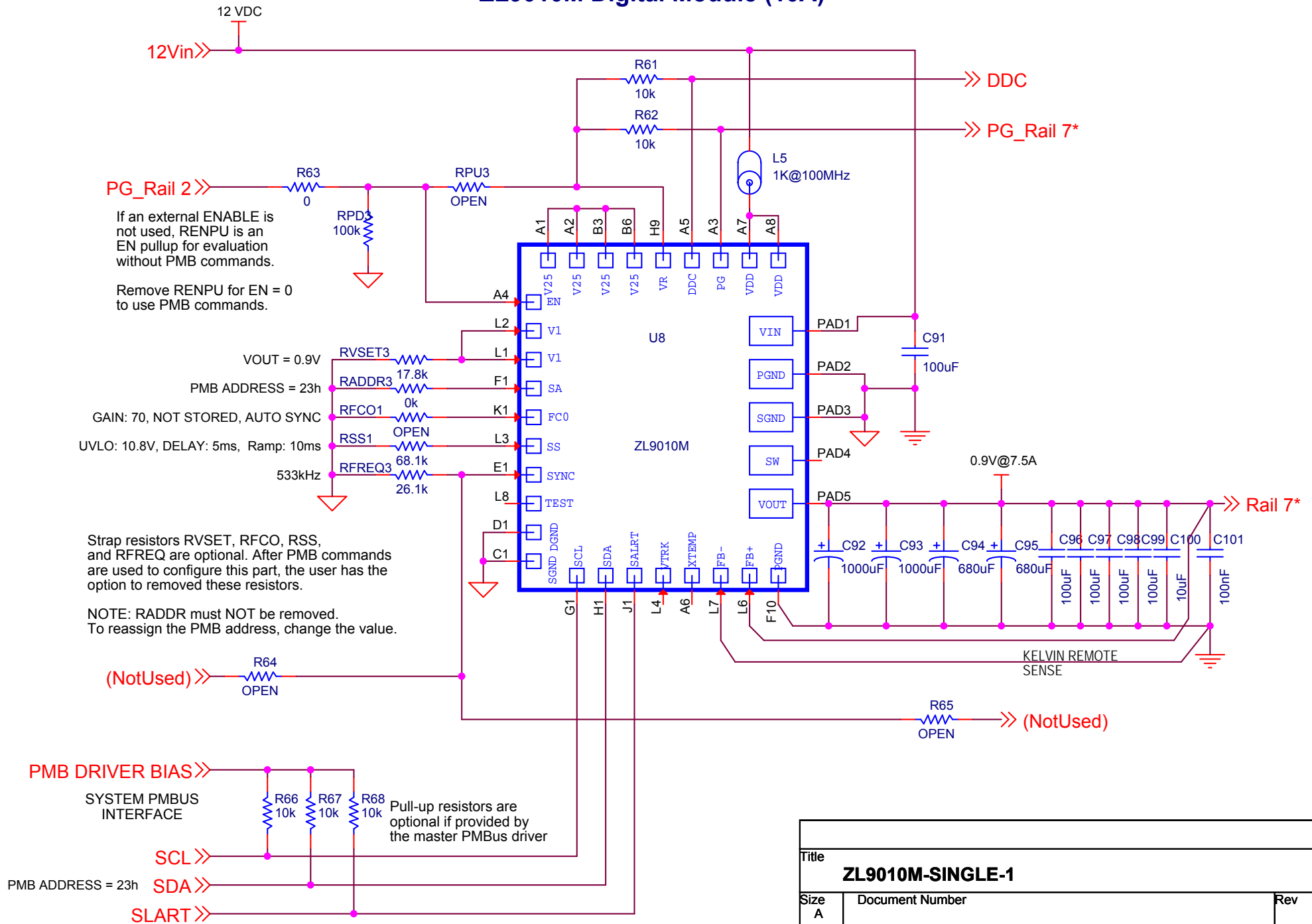
Title		
ISL85415-SINGLE-1		
Size A	Document Number	Rev
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ISL85415 SINGLE OUTPUT (500mA)



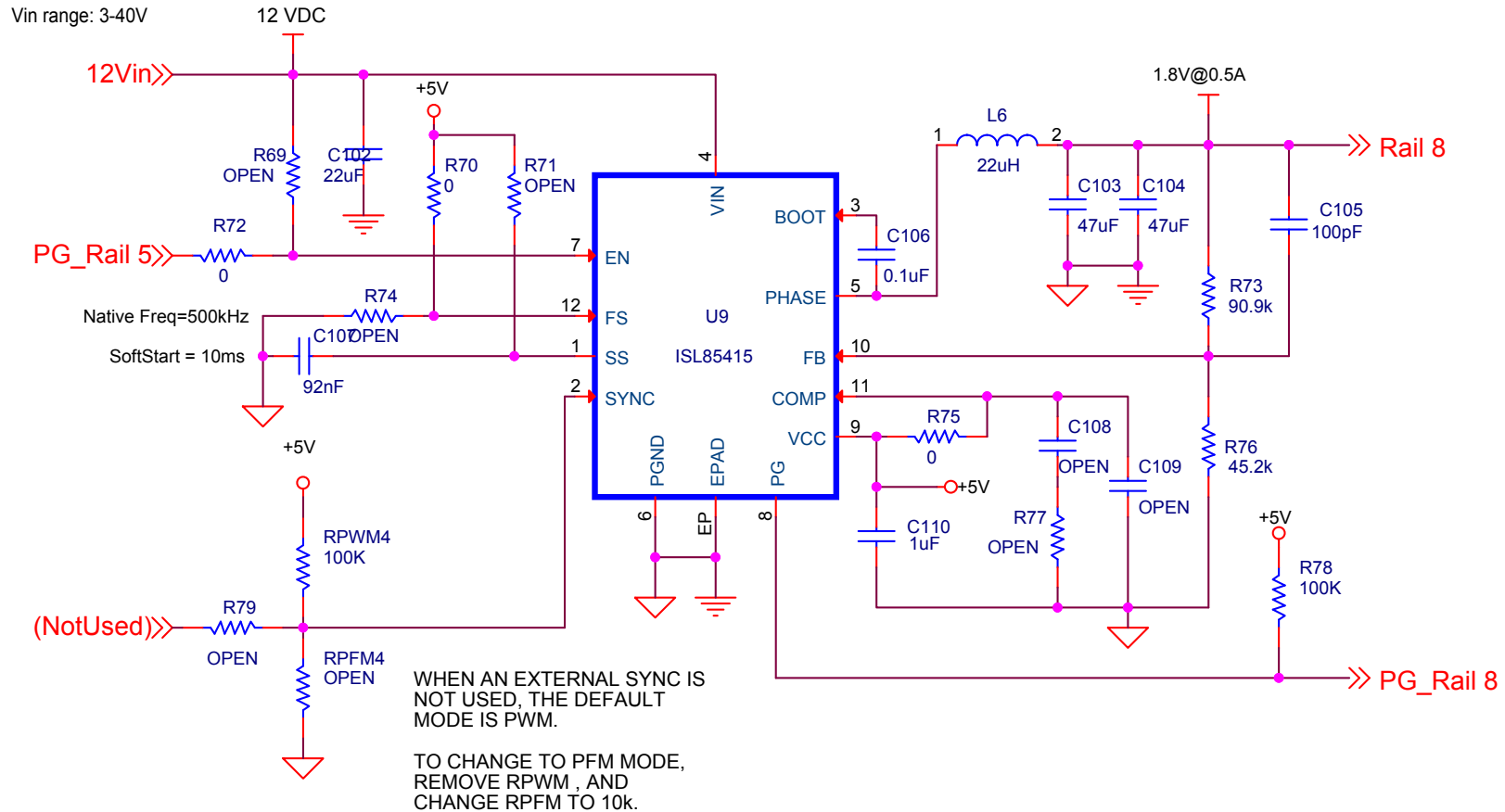
Title		
ISL85415-SINGLE-1		
Size A	Document Number	Rev
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ZL9010M Digital Module (10A)



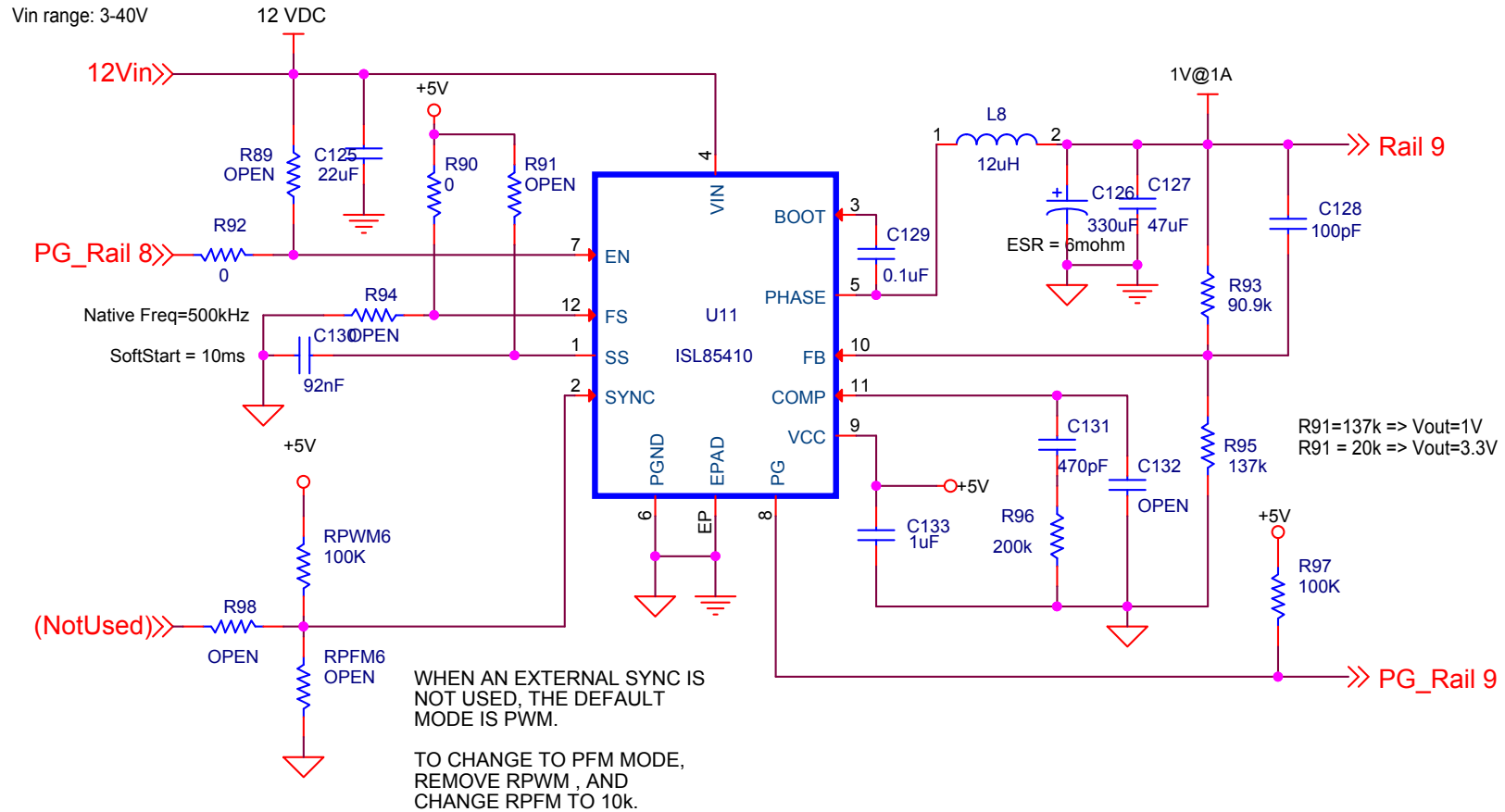
Title		
ZL9010M-SINGLE-1		
Size A	Document Number	Rev
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ISL85415 SINGLE OUTPUT (500mA)



Title		
ISL85415-SINGLE-1		
Size A	Document Number	Rev
Date:	Thursday, March 09, 2017	Sheet 9 of 10

ISL85410 SINGLE OUTPUT (1A)



Title		
ISL85410-SINGLE-1		
Size	Document Number	Rev
A		
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	2	1