



Integrated Device Technology, Inc.  
6024 Silver Creek Valley Road, San Jose, CA 95138

## PRODUCT/PROCESS CHANGE NOTICE (PCN)

PCN #: **N1412-01**                      DATE: **January 7, 2015**

Product Affected:    See attachment I

Date Effective:                      **April 7, 2015**

**MEANS OF DISTINGUISHING CHANGED DEVICES:**

- Product Mark                      The part# will change. Refer to the part# list in Attachment I.
- Back Mark
- Date Code
- Other

Contact:                      TSD Clock Team

Attachment:                       Yes                       No

E-mail:                      [clocks@idt.com](mailto:clocks@idt.com)

Samples:    Contact your local sales representative for sample and datasheet requests.

**DESCRIPTION AND PURPOSE OF CHANGE:**

- Die Technology                      IDT is introducing a new revision of the devices listed in Attachment 1. The new revision better supports configuring devices from EEPROM after power-up reset. IDT will be discontinuing the old revision devices and is asking customers to switch to the newer revision. The last time buy for the old revision devices will be March 31, 2015.
- Wafer Fabrication Process
- Assembly Process
- Equipment
- Material                                      There will be a change in ordering part number and device top mark.
- Testing
- Manufacturing Site                      There is no change in the die technology/process, or AC/DC characteristics.
- Data Sheet
- Other - Die revision                      Please see attachment for details.

**RELIABILITY/QUALIFICATION SUMMARY:**

There is no expected change to the product quality or reliability performance.

**CUSTOMER ACKNOWLEDGMENT OF RECEIPT:**

IDT records indicate that you require written notification of this change. Please use the acknowledgement below or E-Mail to grant approval or request additional information. If IDT does not receive acknowledgement within 30 days of this notice it will be assumed that this change is acceptable.  
IDT reserves the right to ship either version manufactured after the process change effective date until the inventory on the earlier version has been depleted.

Customer: \_\_\_\_\_  **Approval for shipments prior to effective date.**  
 Name/Date: \_\_\_\_\_ E-Mail Address: \_\_\_\_\_  
 Title: \_\_\_\_\_ Phone # /Fax #: \_\_\_\_\_

**CUSTOMER COMMENTS:** \_\_\_\_\_

**IDT ACKNOWLEDGMENT OF RECEIPT:**

RECD. BY: \_\_\_\_\_ DATE: \_\_\_\_\_



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## PRODUCT/PROCESS CHANGE NOTICE (PCN)

### ATTACHMENT I - PCN # : N1412-01

**PCN Type:** Die Revision Change

**Data Sheet Change:** Remove errata, update orderable part number

**Detail Of Change:**

IDT is introducing a new revision of the devices listed in Table 1. The differences between the old revision and the new revision are listed in Table 2. The new revision better supports configuring devices from EEPROM after power-up reset. IDT will be discontinuing the old revision devices and is asking customers to switch to the new revision. The last time buy for the old revision devices will be March 31, 2015.

There will be changes to the ordering part numbers and device top mark as shown in Table 1.

There is no change in die technology/process.

**Table 1: Affected Device Ordering Part Numbers**

Old Revision		New Revision	
Ordering Part Number	Part Number in Top Mark	Ordering Part Number	Part Number in Top Mark
82P33814NLG	82P33814	82P33814ANLG	82P33814A
82P33814NLG8	82P33814	82P33814ANLG8	82P33814A
82P33831BAG	82P33831	82P33831ABAG	82P33831A
82P33831BAG8	82P33831	82P33831ABAG8	82P33831A
82P33714NLG	82P33714	82P33714ANLG	82P33714A
82P33714NLG8	82P33714	82P33714ANLG8	82P33714A
82P33731BAG	82P33731	82P33731ABAG	82P33731A
82P33731BAG8	82P33731	82P33731ABAG8	82P33731A
82P33810BAG	82P33810	82P33810ABAG	82P33810A
82P33810BAG8	82P33810	82P33810ABAG8	82P33810A

**Table 2: Differences Between Revisions**

Revision	Differences
Old Revision	APLL2_PDN [page 7, 0x01, bit 4] defaults to 1; APLL2 is powered down DPLL2_DPLL_PDN [page 7, 0x01, bit 1] defaults to 1; DPLL2 is powered down DEV_ID[1:0] [page 0, 0x00, bit 0 and bit 1) value is 01b
New Revision	APLL2_PDN [page 7, 0x01, bit 4] defaults to 0; APLL2 is powered up DPLL2_DPLL_PDN [page 7, 0x01, bit 1] defaults to 0; DPLL2 is powered up DEV_ID[1:0] [page 0, 0x00, bit 0 and bit 1) value is 10b
With the exception of the changes listed in this table, the two revisions are identical in form, fit and function.	