

# RX65N, RX651 GROUP

The Mainstream of the Next Generation RX





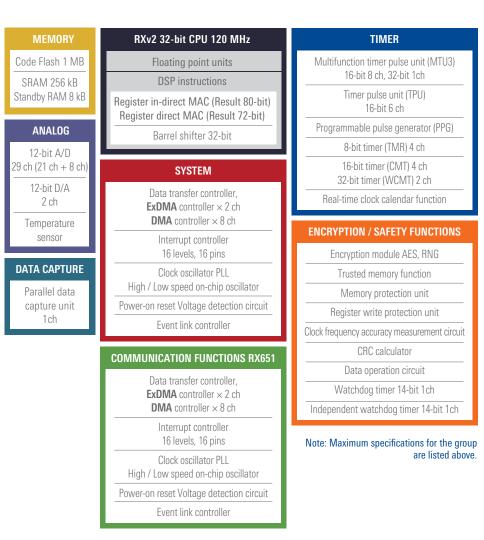
## THE MAINSTREAM OF THE NEXT GENERATION RX

WITH RXV2 CORE, LARGE RAM AND ENHANCED CONNECTIVITY

- 120 MHz/15 mA MCU with higher performance and lower power consumption RXv2 core using new process, achieves operation at 120 MHz/15 mA (Typ), with 1.5 times higher operational performance than and appx. 5 times higher power efficiency than competing MCUs!! RX65N/RX651 support devices that require higher performance with lower power consumption
- Enhanced connectivity in peripheral functions with RX63N / RX631

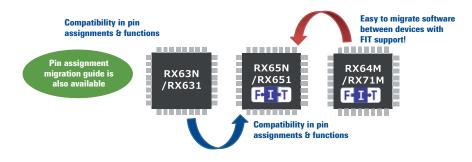
Equipped with SD host/slave interface for growing demand of network. SD connection compliant wireless LAN support is now available. Also additional enhanced functions of RX63x such as serial communication with FIFO, higher-speed Quad SPI communication, and larger RAM of 256 kB etc., to meet customers demands!!

Easy to migrate with compatibility in pin assignments and FIT support drivers Full compatibility with pin assignments between RX63N / RX631, RX64M / RX71M enables easy migration. FIT support drives and middleware with RX63N / RX631, RX64M / RX71M are also available for RX65N / RX651!!



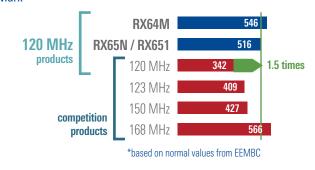
## HIGH COMPATIBILITY BETWEEN RX600 SERIES IN PIN ASSIGNMENTS AND FUNCTIONS

- High compatibility in pin assignments to provide an easy update / optimize path
- Migration APN (application notes) is also available



#### RX65N/RX651 vs. COMPETING MCUs

## More advanced operation capacity than competing MCUs CoreMark \*



## RX65N/RX651 GROUP MEMORY/PACKAGE LINE-UP

R5F565Nxxxxx

R5F5651xxxxx

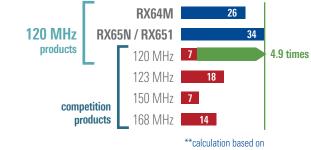
Flash

RAM

E2 Data Flash

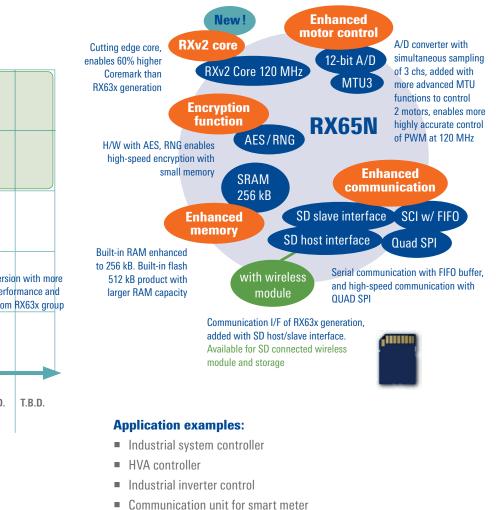
#### With significantly higher power efficiency

CoreMark /mA\*\*



coremark value and type current on datasheet

### **NOTABLE FUNCTIONS**



- System controller for printer
- etc.

									_
	T.B.D.		S		<b>ment</b> ersion with erformanc				
	T.B.D.		f	unctions f	om RX63x	group			
-	1024 kB 256 kB 0 kB	9FDFP 9BDFP 9EDFP 9ADFP	9FDLJ 9BDLJ 9EDLJ 9ADLJ	9FDFB 9BDFB 9EDFB 9ADFB	9FDLK 9BDLK 9EDLK 9ADLK				
-	768 kB 256 kB 0 kB	7FDFP 7BDFP 7EDFP 7ADFP	7FDLJ 7BDLJ 7EDLJ 7ADLJ	7FDFB 7BDFB 7EDFB 7ADFB	7FDLK 7BDLK 7EDLK 7ADLK	impro	lard versic oved perfo ions from	rmance a	ind
-	512 kB 256 kB 0 kB	4FDFP 4BDFP 4EDFP 4ADFP	4FDLJ 4BDLJ 4EDLJ 4ADLJ	4FDFB 4BDFB 4EDFB 4ADFB	4FDLK 4BDLK 4EDLK 4ADLK				
	Pin counts Package Pitch mm Size mm	100 LQFP 0.5 14 x	100 LGA 0.65 7 x 7	144 LQFP 0.5 20 x	145 TFLGA 0.5 7 x 7	T.B.D.	T.B.D.	T.B.D.	

Encryption function (AES, RNG), SDHI No encryption function (AES, RNG), SDHI Encryption function (AES, RNG), no SDHI No encryption function (AES, RNG), no SDHI



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