Integrated Device Technology, Inc.
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Errata Notification

EN #: SMEN-01-05 Errata Revision #: 00

Issue Date: December 6, 2001 Effective Date: December 6, 2001

Product(s) Affected: IDT70V3599, IDT70V3589, IDT70V3319, IDT70V3399, IDT70V7519, IDT70V7599, IDT70V7589, IDT70V7339, IDT70V7319 and IDT70V7399

ERRATA DETAILS: **ATTACHMENT** ☐ Yes ■ No

- ■ Data Sheet Errata
- ☐ Device Errata
- ☐ Device Manual Errata
- ☐ Application Notes Errata
- ☐ Other (Please Specify)

Changed Output Enable Timing Parameter t_{OE} for 166MHz speed from 3.6ns to 4.0ns. (See AC Electrical Characteristics Table for Read and Write Cycle Timing)

Symbol	Parameter	Min.	Max.	Units
t_{OE}	Output Enable to Data Valid	--	4.0	ns

IDT Web Site Location: http://www.idt.com/products/pages/Multi-Ports.html

CUSTOMER NOTIFICATION:

As a result of Errata listed above, IDT has decided to notify you through this Errata Notification.
We recommend you to review this notification in details prior to the use of affected product in your application.
Please use acknowledgement below or E-Mail to request additional information.

If you have any questions, please feel free to contact your local IDT distributor or sales representative.

Customer: _____ E-Mail Address: _____

Name/Date: _____ Phone #: _____

Title: _____ Fax # : _____

CUSTOMER COMMENTS: _____