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The HD74LS75 is ideally suited for use as temporary storage for binary information between processing units and input / output or indicator units. Information present at a data (D) input is transferred to the Q output when the enable (G) is high and the Q output will follow the data input as long as the enable remains high. When the enable goes low, the information (that was present at the data input at the time the transition occurred) is retained at the Q output until the enable is permitted to go high. This device features complementary Q and  $\overline{Q}$  outputs from a 4-bit latch.

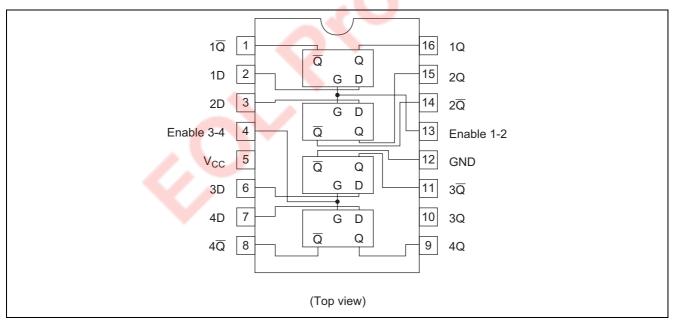
### Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74LS75P	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р		
HD74LS75FPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)	

Note: Please consult the sales office for the above package availability.

# **Pin Arrangement**





### **Function Table**

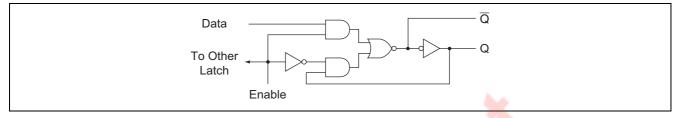
Inp	uts	Outputs			
D	G	Q	Q		
L	н	L	н		
Н	н	Н	L		
Х	L	Q <sub>0</sub>	$\overline{Q}_0$		

H; high level, L; low level, X; irrelevant

Q<sub>0</sub>; level of Q before the indicated steady-state input conditions were established.

 $\overline{Q}_0$ ; complement of  $Q_0$  or level of  $\overline{Q}_0$  before the indicated steady-state input conditions were established.

# **Circuit Schematic (1/4)**



## **Absolute Maximum Ratings**

ltem	Symbol	Ratings	Unit
Supply voltage	V <sub>CC</sub>	7	V
Input voltage	V <sub>IN</sub>	7	V
Power dissipation	PT	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

## **Recommended Operating Conditions**

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	Vcc	4.75	5.00	5.25	V
Output current	Іон	—	—	-400	μA
Oulput current	IOL	—	—	8	mA
Operating temperature	Topr	-20	25	75	°C
Pulse width	tw	20	—	_	ns
Setup time	tsu	15	—	_	ns
Hold time	th	5		_	ns



### **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \ ^{\circ}\text{C})$ 

Item		Symbol	min.	typ.*	max.	Unit	Condition		
Input voltage		V <sub>IH</sub>	2.0	—	—	V			
		V <sub>IL</sub>	_	—	0.8	V			
		V <sub>OH</sub>	2.7	—	—	V	$\label{eq:VCC} \begin{array}{l} V_{CC} = 4.75 \ V, \ V_{IH} = 2 \ V, \ V_{IL} = 0.8 \ V, \\ I_{OH} = -400 \ \mu A \end{array}$		
Output volta	ige	V	_	—	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, \text{ V}_{IH} = 2 \text{ V},$		
		V <sub>OL</sub>	_	—	0.5	V	I <sub>OL</sub> = 8 mA V <sub>IL</sub> = 0.8 V		
Input	D input	- I <sub>IH</sub>	_	—	20	۸			
	G input		_	—	80	μA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 2.7 \text{ V}$		
	D input	- I <sub>IL</sub>	_	—	-0.4	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 0.4 \text{ V}$		
	G input		_	—	-1.6	ШA			
	D input	- Iı	—	—	0.1	mA	$V_{CC} = 5.25 \text{ V}, \text{ V}_{I} = 7 \text{ V}$		
	G input	II II	—	—	0.4	ШA			
Short-circuit output current		I <sub>OS</sub>	-20	_	-100	mA	V <sub>CC</sub> = 5.25 V		
Supply current**		Icc		6.3	12	mA	V <sub>CC</sub> = <mark>5.25</mark> V		
Input clamp voltage		V <sub>IK</sub>	_	—	-1.5	V	$V_{CC} = 4.75 \text{ V}, \text{ I}_{IN} = -18 \text{ mA}$		

Notes: \*  $V_{CC} = 5 V$ , Ta = 25°C

 $^{\star\star}$  I\_{CC} is measured with all outputs open and all inputs grounded.

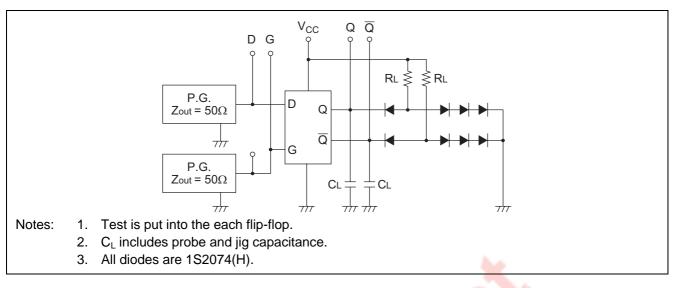
# **Switching Characteristics**

							C	$V_{\rm CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C}$
ltem	Symbol	Inputs	Outputs	min.	typ.	max.	Unit	Condition
	t <sub>PLH</sub>	D	Q		15	27	ns	$C_L = 15 \text{ pF},$ $R_L = 2 \text{ k}\Omega$
	t <sub>PHL</sub>			1	9	17		
Propagation delay time	t <sub>PLH</sub>	D	Q	ł	12	20	ns	
	t <sub>PHL</sub>			—	7	15		
	t <sub>PLH</sub>	G	Q	—	15	27	ns	
	t <sub>PHL</sub>	0	Q	_	14	25		
	t <sub>PLH</sub>	G	Ø	_	16	30	ns	
	t <sub>PHI</sub>	3	Q	_	7	15		

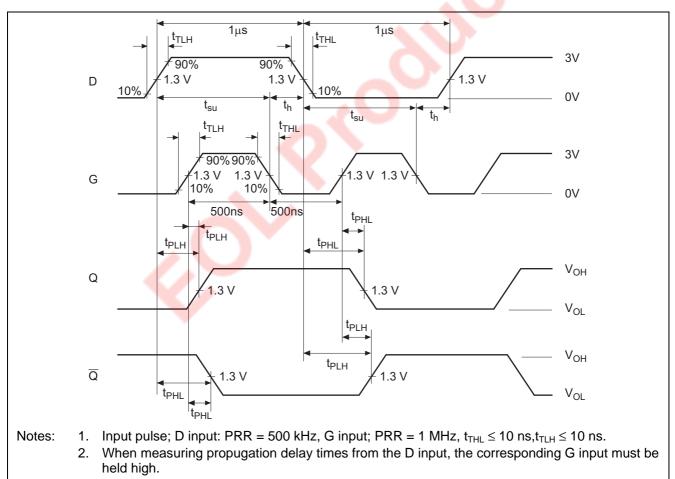


# **Testing Method**

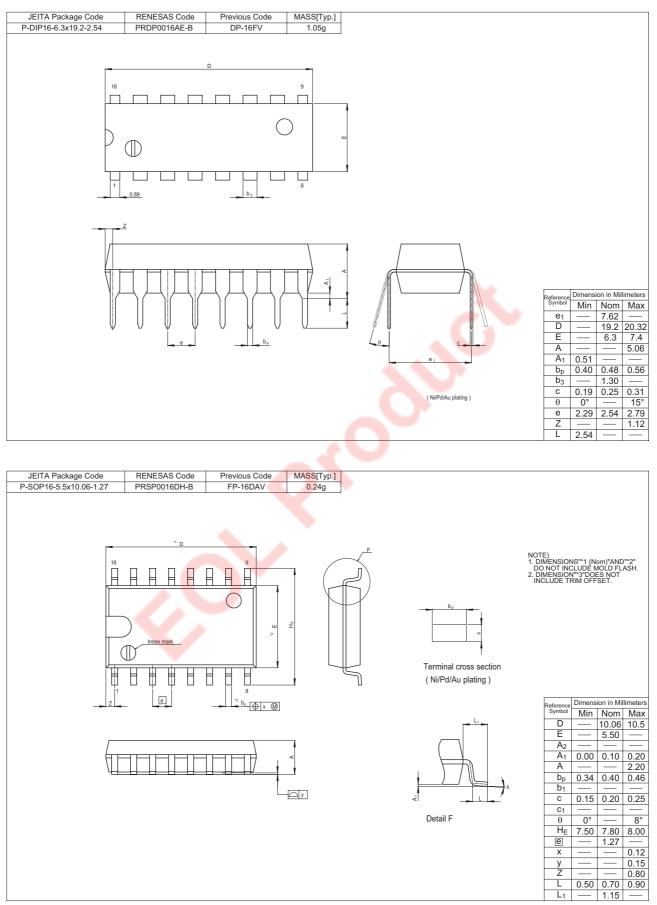
### **Test Circuit**



### Waveform



### **Package Dimensions**





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