

RL78/G15 Group and RL78/G16 Group

Renesas Flash Sample Program Type 01 SC Version (Code Flash)

Introduction

This document explains Renesas Flash Sample Program Type 01 for the RL78/G15 group and RL78/G16 group in the case of using Smart Configurator(SC). It is a process which builds into a user program the functions for "the code flash memory program" included in RFSP Type 01, and it is a method for programming code flash memory using the attached sample program.

In this document, "Renesas Flash Sample Program Type 01" is described to be "RFSP Type 01" or "RFSP".

This document includes the contents in comparison with conventional RFSP Type 01 not using SC. This document distinguishes and expresses it.

Simple version : Conventional RFSP Type 01 not using SC. SC version : RFSP Type 01 using "SC" currently explained by this document.

Target Device

The target device group by which the operation for RFSP Type 01 was confirmed.

RL78/G15 group RL78/G16 group

If this application note is applied to other microcomputers, it is necessary to modify in accordance with the specification of the microcomputer. And, be sure to evaluate enough.



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1. Specification

The sample program included in RFSP Type 01 erases the block 3 (0x0C00) of a code flash area, and programs 64 bytes of data from the top of the block 3.

1.1 Operating Environment

- C Compiler Packages

Table 1-1 The Target C Compiler Packages

Package	Manufacturer	Version
CC-RL(for CS+ or e ² studio)	Renesas Electronics	V1.10 or later
IAR (Embedded Workbench)	IAR Systems	V4.21 or later
LLVM(e ² studio)	(Open source software)	V10.0.0.202309 or later

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- Emulator

Table 1-2 shows the emulator on which the operation of RFSP Type 01 was confirmed.

Table 1-2 Emulator on which RFSP Type 01 Operation was Confirmed

Emulator	Manufacturer
E2 emulator Lite	Renesas Electronics

- Target MCU
 - RL78/G15
 - RL78/G16



1.2 Structure of Sample Program Folders

Figure 1.1 shows the structure of sample program folders.

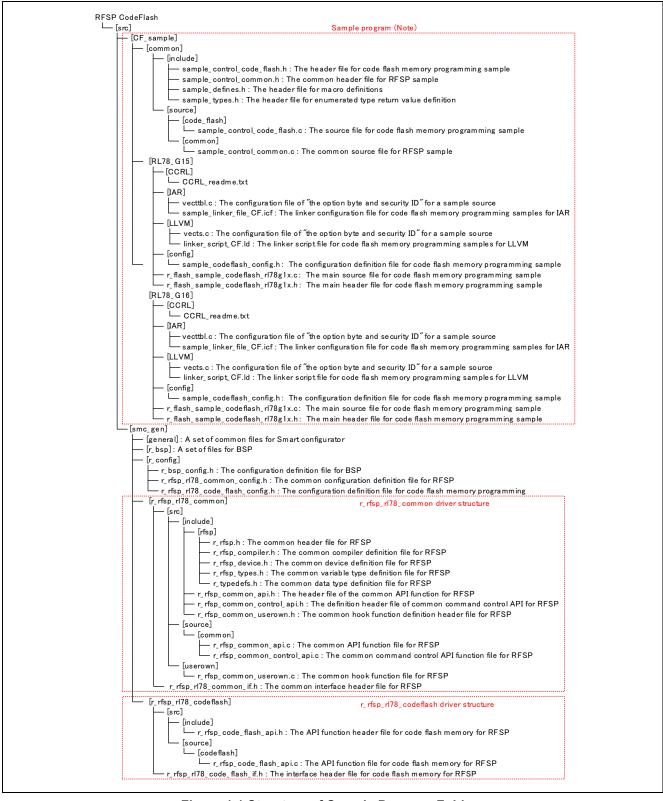


Figure 1.1 Structure of Sample Program Folders

Note: The sample program of a zip file format downloads by Smart configurator. Extract the compressed file (CF_sample.zip) outputted to the "demo" folder, and move the [CF_sample] folder under the [src] folder. Refer to "2.3 Project Registration of Sample Program" for the details of project registration.



1.3 File Structure of RFSP Program

1.3.1 File Structure of RFSP Common Program (r_rfsp_rl78_common)

The difference of SC version RFSP common program and Simple version common program is shown. Refer to the "Renesas Flash Sample Program Type01 Application Note (R20AN0652)" for the detail specification for the RFSP common program.

Table 1-3 Difference of the Files of the SC Version RFSP and the Simple Version RFSP (Common API : r_rfsp_rl78_common\src\source\common)

File name	Simple version	SC version
r_rfsp_common_api.c	No chang	je –
r_rfsp_common_control_api.c	No change	

Table 1-4 Difference of the File of the SC Version RFSP and the Simple Version RFSP (Common API : r_rfsp_rl78_common\src\userown)

File name	Simple version	SC version
r_rfsp_common_userown.c	No chang	je

Table 1-5 Difference of the Files of the SC Version RFSP and the Simple Version RFSP (Common Header : r_rfsp_rl78_common\src\include)

File name	Simple version	SC version
r_rfsp_common_api.h	No chang	ge
r_rfsp_common_control_api.h	No chang	ge
r_rfsp_common_userown.h	No change	

Table 1-6 Difference of the Files of the SC Version RFSP and the Simple Version RFSP (Common Header : r_rfsp_rl78_common\src\include\rfsp)

File name	Simple version	SC version
r_rfsp.h	No chang	ge
r_rfsp_compiler.h	No chang	je
r_rfsp_device.h	No chang	je
r_rfsp_types.h	No chang	je
r_typedefs.h	No chang	je

Table 1-7 Difference of the File of the SC Version RFSP and the Simple Version RFSP (Common Interface Header : r_rfsp_rl78_common)

File name	Simple version	SC version
r_rfsp_rl78_common_if.h	-	Newly created.
		Include the header file for common API.



1.3.2 File Structure for RFSP Code Flash Program (r_rfsp_rl78_codeflash)

The difference of SC version RFSP code flash program and Simple version code flash program is shown. Refer to the "Renesas Flash Sample Program Type01 Application Note (R20AN0652)" for the detail specification for the RFSP code flash program.

Table 1-8 Difference of the File of the SC Version RFSP and the Simple Version RFSP (Code Flash API : r_rfsp_rl78_codeflash\src\source\codeflash)

File name	Simple version	SC version
r_rfsp_code_flash_api.c	No chang	je

Table 1-9 Difference of the File of the SC Version RFSP and the Simple Version RFSP (Code Flash API Header : r_rfsp_rl78_codeflash\src\include)

File name	Simple version	SC version
r_rfsp_code_flash_api.h	No chang	je

Table 1-10 Difference of the File of the SC Version RFSP and the Simple Version RFSP (Code Flash Interface Header : r_rfsp_rl78_codeflash)

File name	Simple version	SC version
r_rfsp_rl78_code_flash_if.h	-	Newly created.
		Include the header file for code flash API.



1.4 Code Flash Programming Processing Using a Sample Program

Figure 1.2 shows the flow chart of the sample program. The sample_codeflash_main function executes processing which reprograms code flash.

Sample_CodeFlashControl function processing does not have change from Simple version. Refer to the item of "Sample_CodeFlashControl function" on Renesas Flash Sample Program Type01 Application Note (R20AN0652).

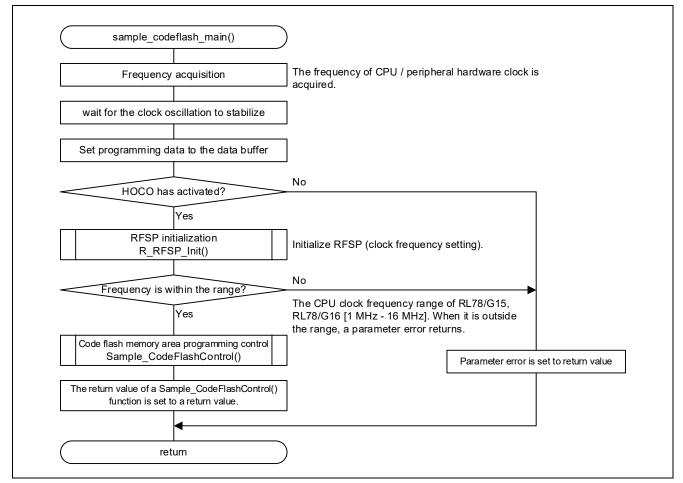


Figure 1.2 The Flow Chart of the Sample Program

Note: "Frequency acquisition" of CPU and peripheral hardware clock settings is using the function included in the "RL78 Family Board Support Package".



2. Creating a Sample Project for Code Flash Reprogramming

2.1 Example of Creating a Sample Project

2.1.1 In Case of CS+

Refer to Renesas Flash Sample Program Type01 Application Note (R20AN0652)"Example of Creating a Sample Project" to create a project.

2.1.2 In Case of e² studio (CC-RL)

Refer to Renesas Flash Sample Program Type01 Application Note (R20AN0652) "Example of Creating a Sample Project" to create a project.

In this application note, because Smart Configurator is used, press a "Next" button after selecting a target device and a debugging tool. And perform the following processes.

3 –	
New Renesas CC-RL Executable Project Select Coding Assistant settings	Ď
Use Smart Configurator Use Peripheral Code Generator	
UART Timer Automatic generation of peripheral function settings Application under development Software D/A Port Middleware Middleware Software DMA Clock RTOS Microcontroller	
? < <u>Back</u> <u>Next</u> > <u>Finish</u>	Cancel

Select "Use Smart Configurator" and press a "Finish" button.

2.1.3 In Case of IAR EW for Renesas RL78

Refer to Renesas Flash Sample Program Type01 Application Note (R20AN0652) "Example of Creating a Sample Project" to create a project.



2.1.4 In Case of e² studio (LLVM)

Refer to Renesas Flash Sample Program Type01 Application Note (R20AN0652)"Example of Creating a Sample Project" to create a project.

In this application note, because Smart Configurator is used, press a "Next" button after selecting a target device and a debugging tool. And perform the following processes.

Select "Use Smart Configurator" and press a "Next" button.

• -		×
LLVM for Renesas RL78 Select Smart Configurator settings		\$
Use Smart Configurator The e2 studio peripheral smart configurator automatically generates programs (device drivers) for peripheral functions (clocks, timers, serial interfaces, A/D converters, DMA controllers, etc.) based entered via a graphical user interface (GUI). Functions are provided as application programming (APIs) and are not limited to initialization of peripheral functions.	l on settir	-
Application Code Software Components Middleware & Drivers Device Drivers MCU Hardware		
? < Back Next > Einish	Cance	1

Select "Disable multiplication code generation (-disable-mda)" and press a "Finish" button.

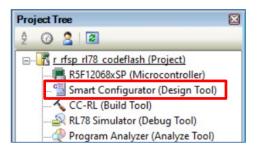
			20 <u>—</u> 33		×
LVM for R	Renesas RL78			_	4
Select Addi	itional CPU Option	IS			2
Select Add	itional CPU Option	ns			
✓ Disable	multiplication cod	de generation (-disable	e-mda)		
	7//				
Make th	e double data typ	e 64-bits wide (-m64b	it-doubles)		
_					•i)
_		e 64-bits wide (-m64b etadata on function st		k-size-sec	tion)
_				k-size-sec	tion)
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_				k-size-sec	tion)
_				k-size-sec	tion)



2.2 Example of Source Code Registration

2.2.1 In Case of CS+

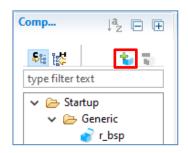
(1) Double-click "Smart Configurator" (design Tool) of "Project Tree", and start Smart Configurator.



(2) Select a "Components" tab.

<u>(</u>	Smart Conf	igurator		-		×	<
File	Window	Help					_
<u> 1</u>					i 🗄	\$	ŝ
靀 r_	rfsp_rl78_c	odeflash.scfg $ imes$					Ð
Ov	erview	information	Generate Code	Genera	💼 ate Repo		•
• (General In	formation			?	^	
		Overview Get an <u>overview</u> of the features provided by Smart Configurator.	Application Code Software Components	Smart			
		Videos Introduction to Smart Configurator Browse related videos	RTOS Device Drivers	Smart Configurator			
		What's New Check out <u>what's new</u> in the latest release.	MCU Hardware	ior			
-	Current Co	nfiguration				~	
Over	view Boar	d Clocks System Components Pins Interrupt					
🗉 c	onsole	≓ ⊑ - 📬 - 🗖	Sconfiguration Problems	7	000		
No co	onsoles to a	display at this time.	0 items				
			Description		Туре	•	
			<			>	

(3) Press the "addition" button of "Components" and open the "addition" dialog of "Components".





- (4) Select the following components and press a "Finish" button.
 - Flash Sample Program[Renesas Flash Sample Program Type01 Code Flash]
 - (r_rfsp _rl78_t01_codeflash)
 - Flash Sample Program[Renesas Flash Sample Program Type01 Flash Common] (r_rfsp _rl78_t01_common)

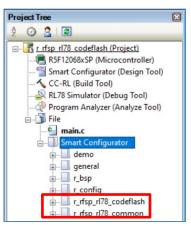
诸 New Co	mponent		_		×
Software	Component Selection			-	
Select cor	nponent from those available in list				-
Category	All				~
Function	All				~
Filter					
Compon	ents	Short Name	Туре	Version	^
H Flash S	ample Program[Renesas Flash Sample Program Type01 Code I	Flash] r_rfsp_rl78_t01_codeflash	Generic SW	1.20	
🖶 Flash S	ample Program[Renesas Flash Sample Program Type01 Data F	lash] r_rfsp_rl78_t01_dataflash	Generic SW	1.20	
🖶 Flash S	ample Program[Renesas Flash Sample Program Type01 Flash (Common] r rfsp rl78 t01 common	Generic SW	1.20	\sim
Show c	nly latest version				
Descriptio	n				
The analo	g to digital (A/D) converter is function for converting analog i	inputs to digital signals.			^
					~
Download	RL78 Software Integration System modules				
Configure	general settings				
(?)					_
		< Back Next >	Finish	Cancel	

(5) Press a "Generate Code" button and close "Smart Configurator" after the completion of generation for the code.

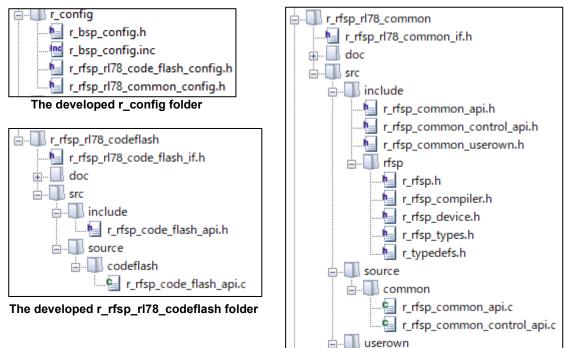
Software component configuration						
Components 🔌 🖄 🛱 🗄 🕀	Configure	•				
Image: Startup Image: S	Property There are no configurable items.	Value				



(6) The "r_rfsp_rl78_common" folder and the "r_rfsp_rl78_codeflash" folder is added to the project tree.



Each folder is developed as follows.



r_rfsp_common_userown.c



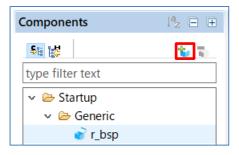
- 2.2.2 In Case of e² studio (CC-RL)
 - (1) Open the project file of "Smart Configurator" after starting e² studio.

🎦 Project Explorer 🛛 🗙	🖻 🕏	7 8 🗆 t
✓		
> 🔊 Includes		
> 🚰 src		
r_rfsp_rl78_codeflash	scfg	
📄 r_rfsp_rl78_codeflash	HardwareDe	bug.launch

(2) Select a "Components" tab.

🔅 r_rfsp_rl78	_codeflash.scfg $ imes$	
Overview	information	Gene
✓ General I	nformation	
	Overview Get an <u>overview</u> of the features provided by Smart Configurator.	Applic
	Videos Introduction to Smart Configurator Browse related videos	Softwar
\bigcirc	What's New Check out <u>what's new</u> in the latest release.	
	Product Documenta ⁻ User manual and release note Application Notes Tool news	MCU
▼ Current C	Configuration	
Selected bo	ard/device: R7F100GLGxFB (ROM	size: 128KB, RAM
Generated I	ocation (PROJECT_LOC\): src\sn	nc_gen
Overview Bo	ard Clocks System Component	s Pins Interrupt

(3) Press the "addition" button of "Components" and open the "addition" dialog of "Components".





- (4) Select the following components and press a "Finish" button.
 - Flash Sample Program[Renesas Flash Sample Program Type01 Code Flash]
 - (r_rfsp _rl78_t01_codeflash)
 - Flash Sample Program[Renesas Flash Sample Program Type01 Flash Common]

(r_rfsp _rl78_t01_common)

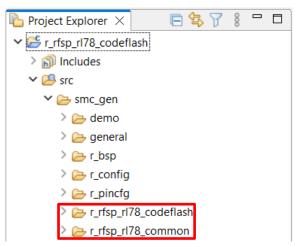
📴 New Co	mponent				×
Software	Component Selection				
Select con	ponent from those available in list			T	
Category	All				\sim
Function	All				\sim
Filter					
	A		1		
Compone	ents	Short Name	Туре	Version	^
	Sample Program[Renesas Flash Sample Program Type01 Code Flash]	r_rfsp_rl78_t01_codeflash		1.20	
	Sample Program[Renesas Flash Sample Program Type01 Data Flash]	r_rfsp_rl78_t01_dataflash		1.20	
🖶 Flash S	Sample Program[Renesas Flash Sample Program Type01 Flash Common]	r_rfsp_rl78_t01_common	Generic SW	1.20	~
Show o	nly latest version				
Descriptio	n				
The analo	g to digital (A/D) converter is function for converting analog inputs to a	digital signals.			^
					~
Download	RL78 Software Integration System modules				
Configure	general settings				
0	< .	Back <u>N</u> ext >	<u>F</u> inish	Cance	I

(5) Press a "Generate Code" button and generate the code.

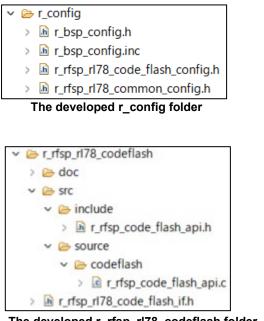
Software component configura	Generate Code	😑 Generate Report	
Components 🚵 🛃 🛓	Configure		í
Image: Startup ✓ Turkston ✓ Turkston	Property There are no configurable items.		
Overview Board Clocks System Compone	nts Pins Interrupt		

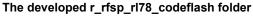


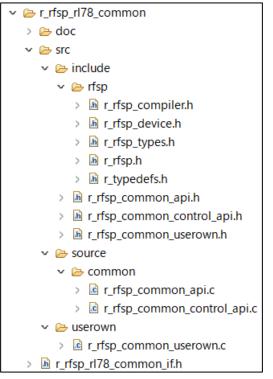
(6) The "r_rfsp_rl78_common" folder and the "r_rfsp_rl78_codeflash" folder is added to the project tree.



Each folder is developed as follows.







The developed r_rfsp_rl78_common folder



2.2.3 In Case of IAR EW for Renesas RL78

(1) Select "File" [New...] after starting Smart Configurator for RL78.

File	Window	Help
Ċ	New	
Þ	Open	
	Save	Ctrl+S
	Restart	
	Exit	

(2) Select the item of "Platform" and "Toolchain".

Select the same device as the device selected in "the project of IAR EW for Renesas RL78" by "Platform".

Select "IAR RL78 Toolchain" as "Toolchain".

Input arbitrary names into "File name".

Press the "Browse..." button and set the location of the project folder for IAR EW for Renesas RL78. And press "Finish" button.

The ".setting" folder and the "<file name>.scfg" file are created to the set location.

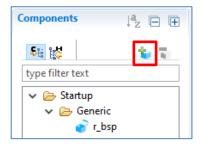
Konst Configuration File			×
Smart Configuration Settings			
Select platform and toolchain settings for the ne	w configuration file		
Category: RL78			\sim
Platform:	Toolchain:		
type filter text	🔨 Renesas CCRL78 Toolchain		
✓ Device	😢 IAR RL78 Toolchain		
> RL78/F24	e ² LLVM for Renesas RL78		
✓ RL78/G15			
> RL78/G15 - 8pin			
 RL78/G15 - 10pin RL78/G15 - 16pin 			
 ✓ RL78/G15 - 20pin 			
R5F12067xSP			
R5F12068xSP			
> RL78/G23			
ROM size: 8 KB, RAM size: 1 KB, Pin count: 20			
File name: r_rfsp_rl78_codeflash			
Location: C:\WorkSpace\r_rfsp_rl78_codeflash		Brows	se
	<u>F</u> inish	Cancel	



(3) Select a "Components" tab.

💰 Smart Configurator	_		×
File Window Help			
		: 🗈 🛛	<u>s</u>
<pre># r_rfsp_rl78_codeflash.scfg ×</pre>			8
Overview information	Generate Code Generat	🗈 e Report	ā
		?	
Get an <u>overview</u> of the features provided by Smart Configurator.	Application Code		
Videos Introduction to Smart Configurator Browse related videos What's New Check out what's new in the latest release.	Software Components Middleware & Drivers Device Drivers MCU Hardware	3	
Current Configuration		~	
Overview Board Clocks System Components Pins Interrupt			
🚍 Console 📑 🖳 🔻 🗂 🗖	🔝 Configuration Problems 🛛 🍸 🖇]
No consoles to display at this time.	0 items		
	Description	Туре	
	<	>	

(4) Press the "addition" button of "Components" and open the "addition" dialog of "Components".





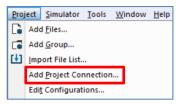
- (5) Select the following components and press a "Finish" button.
 - Flash Sample Program[Renesas Flash Sample Program Type01 Code Flash]
 - (r_rfsp _rl78_t01_codeflash)
 - Flash Sample Program[Renesas Flash Sample Program Type01 Flash Common] (r_rfsp _rl78_t01_common)

🚺 New Co	🕻 New Component – 🗆 🗙					
Software	Software Component Selection					
Select cor	nponent from those available in list					
Category	All					\sim
Function	All					\sim
Filter						
Compone	ents		Short Name	Туре	Version	^
🖶 Flash S	ample Program[Renesas Flash Sample Program Type0	1 Code Flash]	r_rfsp_rl78_t01_codeflash	Generic SW	1.20	
	ample Program[Renesas Flash Sample Program Type0		r_rfsp_rl78_t01_dataflash	Generic SW	1.20	
H Flash S	ample Program[Renesas Flash Sample Program Type0	1 Flash Common]	r_rfsp_rl78_t01_common	Generic SW	1.20	
Show o	nly latest version					
Descriptio	n					
The analo	og to digital (A/D) converter is function for converting	analog inputs to d	igital signals.			\sim
						~
Download	RL78 Software Integration System modules					
Configure	Configure general settings					
۲		< Back	Next >	Finish	Cancel	
		< <u>D</u> ack	INCAL 2	Linish	Cancel	

(6) Press a "Generate Code" button and close "Smart Configurator" after the completion of generation for the code.

Software component configuration			
Components $\ge \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ $	Configure	٢	
Image: Startup Image: S	Property There are no configurable items.	Value	

(7) Start IAR EW for Renesas RL78. And select "Project" menu [Add Project Connection], and open the additional dialog of Project Connection.





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(8)Select "IAR Project Connection", and press an "OK" button.

Add Project Connection		×
Connect using: IAB Project Connection		~
	OK	Cancel

(9) Select the ipcf file created by Smart Configurator and press an "Open" button.

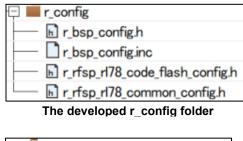
Select IAR Project	Connection File				×
← → ~ ↑ <mark> </mark>	≪ IAREW → r_r	fsp_rl78_codeflash →	5 V		178_codeflash
Organize 🔻 Ne	ew folder			==	- 🔳 🕐
 Desktop Documents Downloads Music Pictures Videos Windows (C:) 	^	Name settings Debug settings src trash buildinfo.ipcf		Date modified 10/20/2022 4:10 PM 10/20/2022 3:58 PM 10/20/2022 4:27 PM 10/20/2022 4:11 PM 10/20/2022 4:10 PM 10/20/2022 4:10 PM	Type File folder File folder File folder File folder File folder IPCF File
💣 Network	~ <				>
	File <u>n</u> ame:			 IAR Project Connecting Open 	on File (*.ipr ~ Cancel



(10) "r_rfsp_rl78_common" and "r_rfsp_rl78_codeflash" are added to Workspace.

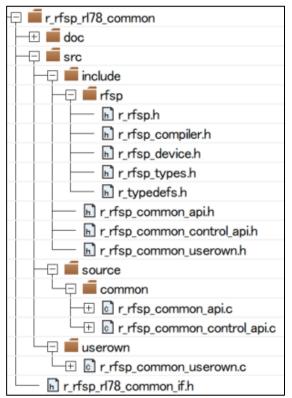
Workspace				
Debug				
Files				
🖻 🜒 r_rfsp_rl78_codeflash - Debug *				
📙 🕂 🖬 Renesas_SC				
📗 🖵 🖵 📹 smc_gen				
🛛 🚽 🛨 🛋 general				
-⊞ 🛋 r_bsp				
⊢⊞ ≡ r_config				
– ⊞ 🛋 r_rfsp_rl78_codeflash				
- I III IIII IIII IIII IIIII IIIIIIIIII				

Each folder is developed as follows.





The developed r_rfsp_rl78_codeflash folder

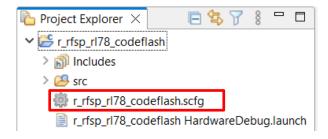


The developed r_rfsp_rl78_common folder



2.2.4 In Case of e² studio (LLVM)

(1) Open the project file of "Smart Configurator" after starting e² studio.



(2) Select a "Components" tab.

r_rfsp_rl78_codeflash.scfg ×			
Overview	information		
→ General I	nformation		
	Overview Get an overview of the features provided by Sm		
	Videos Introduction to Smart Configurator Browse related videos		
\bigcirc	What's New Check out <u>what's new</u> in the latest release.		
	Product Documentation User manual and release notes Application Notes Tool news		
✓ Current Configuration			
Selected board/device: R5F12068xSP (ROM size: 8 KB, RAM si: Generated location (PROJECT_LOC\): src\smc_gen			
Dverview Boa	ard Clocks System Components Pins Interrupt		

(3) Press the "addition" button of "Components" and open the "addition" dialog of "Components".

Components	è 🕹 🖾 📮 🕀
₩	1
type filter text	
🗸 🗁 Startup	
🗸 🧁 Generic	
💣 r_bsp	,



- (4) Select the following components and press a "Finish" button.
 - Flash Sample Program[Renesas Flash Sample Program Type01 Code Flash]
 - (r_rfsp _rl78_t01_codeflash)
 - Flash Sample Program[Renesas Flash Sample Program Type01 Flash Common]

(r_rfsp _rl78_t01_common)

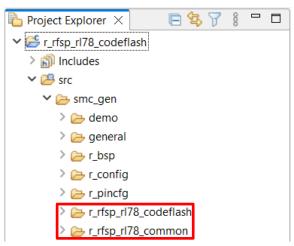
💽 New Co	New Component – 🗆 X						
Software	Software Component Selection						
Select con	ponent from those available in list				-		
Category	All				\sim		
Function	All				~		
Filter							
	^		-				
Compone		Short Name	Туре	Version			
	Sample Program[Renesas Flash Sample Program Type01 Code Flash] Sample Program[Renesas Flash Sample Program Type01 Data Flash]	r_rfsp_rl78_t01_codeflash r_rfsp_rl78_t01_dataflash		1.20			
	Sample Program[Renesas Flash Sample Program Type01 Data Flash]		Generic SW	1.20			
	nly latest version						
Descriptio							
The analo	g to digital (A/D) converter is function for converting analog inputs to c	ligital signals.			^		
					\sim		
Download	RL78 Software Integration System modules						
Configure	Configure general settings						
?	< 1	ack <u>N</u> ext >	<u>F</u> inish	Cance	el		

(5) Press a "Generate Code" button and generate the code.

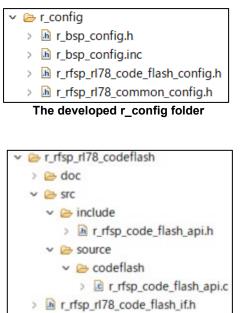
Software compo	oftware component configuration			
Components	ù⊿⊭₂∈€	Configure	١	
	178_t01_codeflash 178_t01_common	Property There are no configurable items.	Value	
Overview Board Cloc	ks System Componen	ts Pins Interrupt	~	



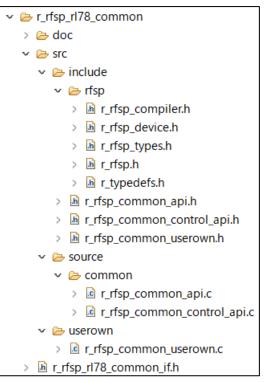
(6) The "r_rfsp_rl78_common" folder and the "r_rfsp_rl78_codeflash" folder is added to the project tree.



Each folder is developed as follows.



The developed r_rfsp_rl78_codeflash folder



The developed r_rfsp_rl78_common folder



2.3 Project Registration of Sample Program

(1) Extract "CF_sample.zip."

The folder of the device name generated after extracting leaves only the folder of a target device. Delete the folders of other device names. For example, when using the RL78/G16, leave only the "RL78 G16" folder, and delete the untargeted "RL78 G15" and other folders together.

The common file duplicates in the case which uses it at the same time with the sample program of a data flash area. Extract to overwrite both as the same folder name.

(2) Register the folder of the sample program into the project of CS+, e² studio, or IAR.

* Files included in the folder other than the compiler package used, do not need to be registered.

- (3) Setting the section items
- (3-1) Setting of the section items on CS+

Setting of the section Items on CS+ inputs in the "Link Options" tab. (Common in each area)

- Setting the [Section] items

Set "No" to [Layout sections automatically], and the section items display on [Section start address]. And setting the following section items.

".text,.data,.sdata,.RLIB,.SLIB,.textf,.constf/0D8,.const/01D00,.dataR,.bss/0FFB00,.sdataR,.sbss/ 0FFE20"

Note: The products of RL78/G16 group are available in two ROM sizes (16 KB or 32 KB). Replace the const section address from "01D00" to "03D00" or "07D00" according to the ROM size of the product to be used.

>	Device	
>	Output Code	
>	List	
>	Variables/functions information	
~	Section	
	Layout sections automatically	No
	Section start address	.text,.data,.sdata,.RLIB,.SLIB,.textf,.constf/0D8,.const/01
>	Section that outputs external defined symbols to the file	Section that outputs external defined symbols to the file[0]
>	ROM to RAM mapped section	ROM to RAM mapped section[2]
>	Verify	
>	Message	

After set section items, return [Layout sections automatically] to "Yes".

>	Device	
>	Output Code	
>	List	
>	Variables/functions information	
\sim	Section	
	Layout sections automatically	Yes(-AUTO_SECTION_LAYOUT)
	Section start address	.text,.data,.sdata,.RLIB,.SLIB,.textf,.constf/0D8,.const/01D0
>	Section that outputs external defined symbols to the file	Section that outputs external defined symbols to the file[0]
>	ROM to RAM mapped section	ROM to RAM mapped section[2]
>	Verify	
>	Message	

Note: Replace the folder name for the RL78/G15 sample ("RL78_G15") with the folder name for the target product. Replace the folder name in the case of using RL78/G16 to "RL78_G16".



(3-2) Setting of the section items on e^2 studio (CC-RL)

Setting of the section Items on e² studio (CC-RL) inputs in the "Properties" window.

Select the [Property] in a project, and open a Properties window. Select "C/C++ Build" [Setting] - "Linker" [Section]. And set section items on the displayed screen. Remove a check mark to [Layout sections automatically(-auto_section_layout)]. And rewrite the contents of the text box of the [Section (-start)] to following section items.

".text,.data,.sdata,.RLIB,.SLIB,.textf,.constf/0D8,.const/01D00,.dataR,.bss/0FFB00,.sdataR,.sbss/ 0FFE20"

Note: The products of RL78/G16 group are available in two ROM sizes (16 KB or 32 KB). Replace the const section address from "01D00" to "03D00" or "07D00" according to the ROM size of the product to be used.

Properties for r_rfsp_rl78	codeflash – 🗆 X
type filter text > Resource Builders < C/C++ Build Build Variables Environment Logging Settings Stack Analysis Tool Chain Editor > C/C++ General Project Natures	Settings > • • • • • • • • • • • • • • • • • •
Project References	V Bevice V Apply and Close Cancel

Be sure to put a check mark to [Layout sections automatically (-auto_section_layout)], then press the "OK" button.

Specify execution start address (-entry)	
Execution start address (-entry= <symbol>)</symbol>	_start
Layout sections automatically (-auto_sect	ion_layout)
Sections (-start)	.const,.text,.data,.sdata,.RLIB,.SLIB,.textf,



(3-3) Setting of the section items on IAR EW for Renesas RL78

On IAR Embedded Workbench, Linker configuration file (*. icf) describes link setting executed by building. Select "Options" by the click right mouse button of project with tree. Select [Linker] by "Category" in the displayed window, And put a check mark to "Override default" of the [Config] tab. Select Linker configuration file (*. icf) in the "Open" window of " ... " button. Select the "sample_linker_file_CF.icf" file prepared for RFSP Type 01.

The "sample_linker_file_CF.icf" file is stored in the folder shown below.

\CF_sample\RL78_G15\IAR\

Options for node "r_rfsp_rl	78_codeflash"					×
Category:					Factory S	ettings
General Options Static Analysis C/C++ Compiler						
Assembler	#define	Diagnostics	Checksum	Encodings	Extra Op	tions
Output Converter	Config	Library Input	Optimizations	Advanced	Output L	ist
Custom Build Build Actions		onfiguration file- ride default				
Linker Debugger			mple¥RL78_G15¥IAR	¥sample_linke	er_file_CF.icf	
Open ← → · ↑ CF_	sample > RL78_G15	iar 🗸	د Search IAF		×	
Organize 🔻 New folder				🖽 🕶 🛄 🔮	9	
This PC	Name	^	Date modified	Туре		
3D Objects Desktop Documents Documents	sample_linker_f	ile_CF.icf	6/3/2022 1:37 PM	ICF File	•	
	mple_linker_file_CF.icf	:	 Icf Files (*.icf) Open 	Cancel		

Note: The ROM size in the icf file for RL78/G16 is set to 32 KB. In the case of a 16 KB device, modify the address range of "ROM_near", "ROM_far", and "ROM_huge" to "from 0x000D8 to 0x03CFF". In addition, modify the placement address of the "OCD ROM area" to "from 0x03E00 size 0x0200".

define r define r define r define r define r define r define r	region R region S region R region R region R region V region C	ROM_far ROM_huge SADDR RAM_near RAM_far RAM_huge /ECTOR CALLT	= mem: [from = mem: [from	0x000D8 td 0x000D8 td 0xFF220 td 0xFF700 td 0xFF700 td 0xFF700 td 0xFF700 td 0x00000 td	0 0x07CFF; 0 0x07CFF; 0 0xFFEDF; 0 0xFFE1F; 0 0xFFE1F; 0 0xFFE1F; 0 0xFFE1F; 0 0x0007F; 0 0x000BF;		From 0x000	D8 to 0x03CF	F
if (_ {↩	finedsym RESERVE_	EPROM bol(RES OCD_ROM =	= mem:[from ERVE_OCD_ROM) ≔ 1)↔))↔	o 0x093FF];	с	► from 0x0	3E00 size 0x0)200



Set the items of [General Options] - [Target] tab in the "Options" screen. Select the target device for [Device] and "Far" for [Code model]. To specify the allocation address of the [Near constant location], set the check mark to [Default Address Override (O)], select "Mirror ROM0", and change [Start address:] to 0xF1D00.

Note: The products of RL78/G16 group are available in two ROM sizes (16 KB or 32 KB). Replace [Start address:] from "0xF1D00" to "0xF3D00" or "0xF7D00" according to the ROM size of the product to be used.

Category: Static Analysis C/C++ Compiler Assembler Output Converter Custom Build Build Actions Linker Debugger COM Port E1 E2 E3 mulator Tk Near constant location Output Refut addresses Simulator Tk Mirror ROM 0 OxF1 D00	General Options Static Analysis C/C++ Compiler Assembler Output Converter Custom Build Build Actions Linker Debugger COM Port E1 E2 E20 E2 Lite / E2 On-board E2-CUBE E2-CUBE	Options for node "r_rfsp_rl78	_codeflash"				Х
		General Options Static Analysis C/C++ Compiler Assembler Output Converter Custom Build Build Actions Linker Debugger COM Port E1 E2 E20 E2 Lite / E2 On-board EZ-CUBE EZ-CUBE Simulator	Target Outpu Device RL78 - R5F1 2068 Code model Far Use far runtin Data model Near Near constant loca	ut Library C The library calls	Configuration Floating-poin Size of type 'd 32 bits 64 bits Calling conver Default tart address: End 0xF1 D00 0x	Library Options 1 t ouble': n tion	



(3-4) Setting of the section items on e^2 studio (LLVM)

On e² studio (LLVM), Linker script file(*. ld) describes link setting executed by building. Select the [Property] in a project, and open a Properties window. Input the linker script file path in the window displayed by selection of "C/C++" build [Setting] - "Linker" [Source].

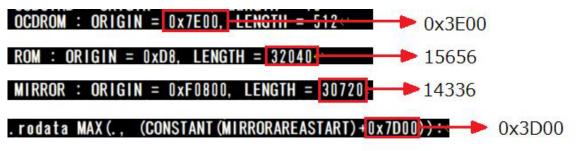
That linker script path to add is shown below.

\${workspace_loc:/\${ProjName}/src/CF_sample/RL78_G15/LLVM/linker_script_CF.ld}

Properties for r_rfsp_rl78_cod	deflash		—	×
type filter text	Settings			₩ 8
 > Resource Builders > C/C++ Build Build Variables Environment Logging Settings Tool Chain Editor > C/C++ General Project Natures Project References Renesas QE Run/Debug Settings 	 CPU Optimization Debug Warnings Settings Settings Compiler Source Includes Source Includes Source Includes Source Arcnives 	Entry point: -WI,-e_PowerON_Reset Linker script "\${workspace_loc:/\${ProjName}/src/CF_sample/RL78	 	~
(?)			Apply and Close Cancel	

* It is an example of the include path in the case which extracted CF_sample.zip directly under the src folder.

Note: The ROM size in the linker script file for RL78/G16 is set to 32 KB. In the case of a 16 KB device, change the value of "ORIGIN" in "OCDROM" to"0x3E00", the value of "LENGTH" in "ROM" to "15656", and the value of "LENGTH" in "MIRROR" to "14336". In addition, modify the start address of the "rodata" to "0x3D00".





(4) Include Path Settings

The figure shows the case of RL78/G15. In this case, too, the folder name ("RL78_G15") for the RL78/G15 sample should be read as the folder name of the target device. In the case of RL78/G16, the folder name should be change to "RL78_G16".

(4-1) Setting of the include path on CS+

Setting of the include path on CS+ inputs path in "Common Options" tab.

- Add the Include directory path in the "Path Edit" window displayed by selection of [Frequently Used Options(for Compile)] - [Additional include paths].

In the phase where code generation was performed by (5) of "2.2.1 In Case of CS+", the include path of files other than the sample program is registered. For a reason, it needs to register the include path of the sample program.

Those include path to add is shown below.

src\CF_sample\common\include]
src\CF_sample\RL78_G15	
src\CF_sample\RL78_G15\config	
Path Edit	\times
Path(One path per one line):	
src¥smc_gen¥r_bsp¥mcui¥all src¥smc_gen¥r_bsp¥board¥generic_r178_g15 src¥smc_gen¥r_bsp src¥smc_gen¥r_bsp	^
src¥CF_sample¥common¥include src¥CF_sample¥RL78_G15 src¥CF_sample¥RL78_G15¥config	
<	>
Browse	
Permit non-existent path	
Include subfolders automatically	

* It is an example of the include path in the case which extracted CF_sample.zip directly under the src folder.



(4-2) Setting of the include path on e² studio (CC-RL)

Setting of the include path on e² studio (CC-RL) inputs path in "Properties" window.

- Input the Include directory path in the window displayed by selection of "C/C++" build [Setting] "Compiler" [Source].
- Those include path to add is shown below.

\${workspace_lc	pc:/\${ProjName}/src/CF_	sample/common/include} sample/RL78_G15} sample/RL78_G15/config}			
Properties for r_rfsp_rl	78_codeflash		- 		K
type filter text	Settings			← → ⇒ ▼	000
 > Resource Builders > C/C++ Build Build Variables Environment Logging Settings Stack Analysis Tool Chain Editor > C/C++ General 	 S Common CPU Device Miscellaneous S Compiler Compiler Compiler Conpiler Object Optimization 	"\${workspace_loc:/\${ProjName}/src/ "\${workspace_loc:/\${ProjName}/src/	smc_gen/r_rfsp_r178_codeflash}" smc_gen/r_rfsp_r178_codeflash/src/include}" smc_gen/r_rfsp_r178_common/src/include}" smc_gen/r_rfsp_r178_common/src/include/rfsp}" smc_gen/c_rfsp_r178_common/src/include/rfsp}" cf_sample/common/include}" CF_sample/common/include}"		^

* It is an example of the include path in the case which extracted CF_sample.zip directly under the src folder.



(4-3) Setting of the include path on IAR EW for Renesas RL78

Setting of the include path on IAR Embedded Workbench selects "C/C++ Compiler" of "Category", and inputs path in "Preprocessor" tab.

- Input the Include directory path in the "Edit include Directories" window displayed by selection of [Additional include directories: (one per line)].

Those include path to add is shown below.

<pre>\$PROJ_DIR\$\src\CF_sample\common\include</pre>						
\$PROJ_DIR\$\src\CF_sample\RL78_G15						
\$PROJ_DIR\$\src\CF_samp	ole\RL78_G15\c	onfig				
Options for node "r_rfsp_rl78	_codeflash"				×	
Category:					Factory Settings	
General Options	🗆 Multi-file Compi	ilation				
Static Analysis	🗆 Discard Ur	nused Publics				
C/C++ Compiler	Diagnostics	MISRA-C:2004	MISRA-C:1998	Encodings	Extra Options	
Assembler	Language 1			utput List	Preprocessor	
Output Converter Custom Build	Language i	Language 2	optimizations 0	atput List		
Build Actions	□ <u>I</u> gnore stan	dard include dire	ectories			
Linker	Additional in	duda diractarian	(one per line)			
Debugger		clude directories:		/		
COM Port			rfsp_rl78_common¥ rfsp_rl78_common¥		p ^	
E1			common¥include	S. C. THERMONT I S		
E2	\$PROJ_DIR\$	¥src¥CF_sample¥	RL78_G15			
E20	\$PROJ_DIR\$	¥src¥CF_sample¥	RL78_G15¥config		~	

* It is an example of the include path in the case which extracted CF_sample.zip directly under the src folder.



(4-4) Setting of the include path on e^2 studio (LLVM)

Setting of the include path on e² studio (LLVM) inputs path in "Properties" window.

- Input the Include directory path in the window displayed by selection of "C/C++" build [Setting] - "Compiler" [Includes].

Those include path to add is shown below.

Image: Settings Image: Settings Image: Settings Image: Settings Image: Settings V C/C++ Build Settings Image: Settings	\${workspace_loc:/\${	ProjName}/src/CF_sample/common/include} ProjName}/src/CF_sample/RL78_G15} ProjName}/src/CF_sample/RL78_G15/config}	
Builders Include file directories (-1) Builders Optimization C/C++ Build Optimization Build Variables Warnings Environment Warnings Logging Settings Settings Settings Tool Chain Editor Settings > C/C++ General Source Project Natures Source Project References Source Buildues Source Source			
	Builders C/C++ Build Build Variables Environment Logging Settings Tool Chain Editor C/C++ General Project Natures Project References Renesas QE	Optimization Debug Warnings Warnings Warnings Settings Settings Source Morkspace_loc:/\$(ProjN S(workspace_loc:/\$(ProjN S(workspace_loc:)\$(ProjN S(workspace_loc:)\$(ProjN	ame)/src/smc_gen/r_pincf6]" ame)/src/smc_gen/general}" ame)/src/smc_gen/r_config]" ame)/src/smc_gen/r_fsp_r178_common/src/include}" ame)/src/smc_gen/r_fsp_r178_codeflash)" ame)/src/smc_gen/r_fsp_r178_codeflash)" ame)/src/smc_gen/r_fsp_r178_codeflash/src/include/fsp}" ame)/src/smc_gen/r_fsp_r178_codeflash/src/include}" ame)/src/Smc_gen/r_fsp_r178_common/src/include}" ame)/src/CF_sample/common/include)" ame)/src/CF_sample/RL78_G15]"



- (5) Device Item Settings
- (5-1) Setting of the device Items on CS+

Refer to the chapter of "Device Item Settings" of Renesas Flash Sample Program Type01 Application Note (R20AN0652).

(5-2) Setting of the device Items on e² studio (CC-RL)

Refer to the chapter of "Device Item Settings" of Renesas Flash Sample Program Type01 Application Note (R20AN0652).

(5-3) Setting of the device Items on IAR EW for Renesas RL78

Build including vecttbl.c prepared as a sample program. A user option byte's value is set to 0xEEFFF9, and an on-chip debugging option byte's value is set to 0x85.

When the code is generated by Smart Configurator, vecttbl.c is generated to a "smc_gen\r_bsp\mcu\rl78_g15\" folder. And because vecttbl.c duplicates, it is necessary to repeal this file.

Right-click a mouse by "Renesas_SC\smc_gen\r_bsp\mcu\rl78_g15\vecttbl.c" in the [project] on a tree. And select an "option" and set a "check" to [Exclude from build] in the displayed screen.

Workspace	×	
Debug	\sim	
Files Image: SC Image: Sc Image: Sc Image: Sc		e "vecttbl.c" Coald C-STAT Statio Analysis Select O-STAT Ohecks. Import Settings. Export Settings. Export Settings. Export Settings. Export Settings. Export Settings. There is no O-STAT license available for this product version. OK Cancel

(5-4) Setting of the device Items on e² studio (LLVM)

Build including vects.c prepared as a sample program. A user option byte's value is set to 0xEEFFF9, and an on-chip debugging option byte's value is set to 0x85.

When the code is generated by Smart Configurator, r_cg_vect_table.c is generated to a "src\general\" folder. And because r_cg_vect_table.c and vects.c duplicate the settings, it is necessary to repeal this file.

Right-click a mouse by "src\general\r_cg_vect_table.c" in the [project] on a Project Explorer. And select [Resource Configuration] - [Exclude from build...] and use it to exclude r_cg_vect_table.c from build target.

(6) Execute the sample program from a main function.

Describe the sample_codeflash_main function included in r_flash_sample_codeflash_rl78g1x.c like the "main function" for the project. And build, download and execute it.

* The header file which described the prototype declaration for sample_codeflash_main function. Include prepared "r_flash_sample_codeflash_rl78g1x.h."

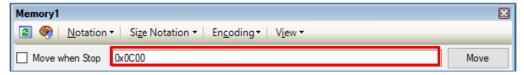


2.4 The Check of Operation for Sample Program

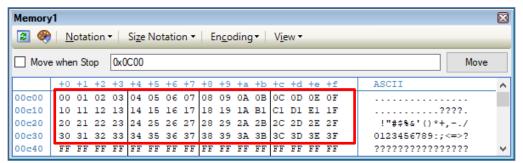
The block 3 (0x0C00) of a code flash area is erased. And 16 Words(64 bytes) of data is programmed from the top of the block 3. Confirm this operation by the following methods.

2.4.1 In Case of CS+

- (1) Select [Debug]menu [Download] and start debugging.
- (2) After selecting [View]menu [Memory], select "Memory1", "Memory2", "Memory3", or "Memory4", and display a memory window.
- (3) Set "0x0C00" to the address of a memory window, and display the block 3 of a code flash area.



(4) Select [Debug]menu - [Go] and the program is executed. Check that the value of "0x0C00-0x0C3F" displayed on the memory window has changed after stopping the program.





2.4.2 In Case of e² studio (CC-RL)

- (1) Select [Run]menu [Debug] and start debugging.
- (2) Select [Window]menu [Show View] [Memory] and display Memory view.
- (3) Press " 🚑 " button and display the Monitor Memory window.

Memory ×	🖓 🗤 🖓 🖓	📍 🛃 🎫 🖳 🥵	•	000	
Monitors 🔮 🗙 🔌					

(4) Input "0x0C00" into the address to monitor, and press the OK button. The block 3 of a code flash area is displayed on the memory view.

Monitor M	emory		×
Enter address	s or expressi	on to monito	or:
			~
0	OK	Canc	el

(5) After the program is executed, check that the value of "0x0C00-0x0C3F" displayed on the memory window is changing.

8					-		×
ii Memory $ imes$	🤹 👓 😵 📑 📑 🛃 😼 📲 🖛 🖇 🗂						
Monitors	💠 💥 💥 0x0C00 : 0xC00 <hex integer=""> 🗙 💠 New Renderings</hex>						
0x0C00		Address	0 - 3	4 - 7	8 - B	C - F	^
		000000000000000000000000000000000000000	03020100	07060504	0B0A0908	0F0E0D0C	
		0000000000000C10	13121110	17161514	1B1A1918	1F1E1D1C	
		000000000000C20	23222120	27262524	2B2A2928	2F2E2D2C	
		000000000000C30	33323130	37363534	3B3A3938	3F3E3D3C	
		000000000000C40	FFFFFFF	FFFFFFF	FFFFFFF	FFFFFFF	



2.4.3 In Case of IAR EW for Renesas RL78

- (1) Select [Project] menu [Download and Debug] and start debugging.
- (2) After select [View]menu [Memory], select either from "Memory 1" to "Memory 4", and the memory window is displayed.
- (3) Set "0x0C00" to the address of the memory window. And the block 3 of code flash area is displayed.

Memory 1	I		×
Go to	0x0C00	Memory	

(4) After the program is executed, check that the value of "0x0C00-0x0C3F" displayed on the memory window is changing.

Memory 1 X				
Go to 0x0C00	V Memory V 🔽 📩 就			
0x00000c00 0x00000c10 0x00000c20 0x00000c30 0x00000c30	00 01 02 03 04 05 06 07 08 09 0a 0b 0c 0d 0e 0f 10 11 12 13 14 15 16 17 18 19 1a 1b 1c 1d 1e 1f 20 21 22 23 24 25 26 27 28 29 2a 2b 2c 2d 2e 2f 30 31 32 33 34 35 36 37 38 39 3a 3b 3c 3d 3e 3f Iff ff ff	!"#\$%&'()*+,/ 0123456789:;<=>?		
<		>		



2.4.4 In Case of e² studio (LLVM)

- (1) Select [Run]menu [Debug] and start debugging.
- (2) Select [Window]menu [Show View] [Memory] and display Memory view.
- (3) Press " 🛶 " button and display the Monitor Memory window.

8			×
Memory ×	🏶 101 fan 🇞 📑 📑 📑	▼ 80	
Mc 👍 Ҝ 🥳			

(4) Input "0x0C00" into the address to monitor, and press the OK button. The block 3 of a code flash area is displayed on the memory view.

Monitor Memory			
Enter a	address or express	sion to monitor:	
		~	
?	ОК	Cancel	

(5) After the program is executed, check that the value of "0x0C00-0x0C3F" displayed on the memory window is changing.

•						×
🚺 Memory 🗙		🤹 101 <u>0</u> 1	iono 🤣 📑 록	⇒ti 🛄 🔁	🖥 🕶 🖇 🗖	
Mo 💠 🗶 🔆	0x0C00 : 0xC00 <hex inte<="" th=""><th>eger> 🗙 🖕</th><th>New Rendering</th><th>JS</th><th></th><th></th></hex>	eger> 🗙 🖕	New Rendering	JS		
Ox0C00	Address	0 - 3	4 - 7	8 - B	C - F	^
	000000000000000000000000000000000000000	03020100	07060504	0B0A0908	0F0E0D0C	
	000000000000000000000000000000000000000	13121110	17161514	1B1A1918	1F1E1D1C	
	000000000000000000000000000000000000000	23222120	27262524	2B2A2928	2F2E2D2C	
	0000000000000C30	33323130	37363534	3B3A3938	3F3E3D3C	
	000000000000000000000000000000000000000	FFFFFFF	FFFFFFF	FFFFFFF	FFFFFFF	~



RL78/G15 Group and RL78/G16 Group

3. Precautions for Specifications

(1) Allocation of the user program for flash memory reprogramming operation

Allocate the user program for programming the code/data flash area to the code flash area. Selfprogramming by fetching from the RAM is prohibited. Additionally, reprogramming the boot area and the block for storing the user program for executing self-programming is prohibited.

- (2) Prohibit the interrupts in self-programming mode Prohibit an interrupt before setting the self-programming mode. To prohibit an interrupt, clear (0) the IE flag by the DI instruction in the same way as in the normal operation mode.
- (3) Setting the CPU operating frequency for the flash memory sequencer

When using the flash memory sequencer to reprogram the code/data flash memory, set the value corresponding to the CPU operating frequency in the FSET4-0 bits of the FSSET register before proceeding. Note that if reprogramming is attempted while the value corresponding to the CPU operating frequency is not correct, operation is undefined and written data are not guaranteed. Even if the values in the flash memory are as expected immediately after reprogramming, retaining the values for any specified period is not guaranteed.

(4) Operation setting of high-speed on-chip oscillator

The high-speed on-chip oscillator should be kept operating before executing self-programming. If it is stopped, it should be made to operate again (HIOSTOP = 0), and the flash self-programming code should be re-executed after 30 us have elapsed.

- (5) Restriction of execution of other operations during self-programming Do not execute other settings or instructions which are not related to the self-programming procedure during the selfprogramming execution flow
- (6) User program operation during flash memory reprogramming operation

The CPU is stopped during reprogramming through self-programming. The code flash or data flash memory cannot be accessed while it is being reprogrammed.



4. Reference Document

Please get the latest version of each document from the Renesas Electronics Corp. website (https://www.renesas.com).

No	Document Title	Document Number		
1	RL78/G15 User's Manual Hardware R01UH0959			
2	RL78/G16 User's Manual Hardware	R01UH0980		
3	RL78 Family Board Support Package Module R01AN5522			
4	Renesas Flash Sample Program Type01 Application Note R20AN0652			
5	E1/E20/E2 Emulator, E2 Emulator Lite Additional Document for User's Manual (Notes on Connection of RL78)	R20UT1994		



5. Revision History

Rev.	Duta	Description		
	Date	Page	Summary	
1.00	Oct.31.22		Newly created.	
1.10	Apr.28.23		RL78/G16 was added.	
1.20	Nov.30.23		The LLVM compiler was supported.	



General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power is supplied until the power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{L} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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