[Upgrade to revision] Smart Configurator for RZ Family

Outline

We will be revising the Smart Configurator for the RZ family from V1.00.00 to V1.01.00.

1. Product and Version to be updated

Smart Configurator for the RZ family from V1.00.00

2. Description of Revision

Refer to the release notes at the URL below for details of the major revision points.

Smart Configurator for the RZ family V1.01.00 Release Note (This document will be released on March 21.)

https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut4011

2.1 Addition of Supported Devices

Support for the devices below has been newly added.

RZ Family

RZ/A1H Group:	R7S721000VCBG, R7S721000VCFP, R7S721000VLFP
	R7S721001VCBG, and R7S721001VLBG
RZ/A1M Group:	R7S721010VCBG, R7S721010VCFP, R7S721010VLFP
	R7S721011VCBG, and R7S721011VLBG
RZ/A1LC Group:	R7S721034VCBG

3. Updating Your Product

Download the installer from the URL below and install the product. (This program will be available from March 21.)

https://www.renesas.com/smart-configurator#downloads



4. Features

The Smart Configurator is a tool for checking pin settings and the state of settings of microprocessors when designing hardware specifications.

You can set pins of the peripheral functions from the GUI of the Smart Configurator, and check for contention between multiplexed functions in pin configuration figures and pin configuration tables. Output of the setting information makes it easy for you to transfer correct hardware specifications to software designers.

Refer to the URL below for details of the product.

https://www.renesas.com/smart-configurator

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Hardware Resource $\boxplus = \downarrow_z^a$	Pin Functio	on	· · · · · · · · · · · · · · · · · · ·				1
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🗉 🦏 Multi-function timer pulse	V	TCLKB	P6_1/D17/LCD0_DATA9/SSL00/Te				
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MTU3	V	TIOCOD	P2_3/CKE/CAN1TX/TIOC0D		R7S72	21020VCBG	
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8 E05000010: Pin L14 cannot be			Pin				
8 E05000010: Pin L15 cannot be used multiple times. JP0_0 used by Pin Allocator, TDI used by Pin Alloc					Pin		

The pins set to be used by each peripheral function can be displayed in the [Pins Configuration] window. In the [MCU Package] window, the used pins, unused pins, and pins for which multiplexed functions are in contention, are indicated by different colors, making the allocation easy to check.



Revision History

		Description		
Rev.	Date	Page	Summary	
1.00	Mar. 16, 2017	-	First edition issued	
1.01	Mar. 16, 2017	all	Release date of the document and product corrected	

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