

RL78/L12

R01AN1590EG0100

Rev.1.00

Utilising the Serial Array Unit (SAU) in Sync Mode for e2studio

Nov 01, 2013

Introduction

The purpose of this Application Note is to show the user how to add the associated RL78/L12 sample code to a new or existing e2studio workspace; as well as give an explanation of what the sample code does.

The sample code provided with this Application Note demonstrates usage of the Serial Array Unit (SAU) in synchronous mode. The program runs on the RL78/L12 RSK and performs a loop back test between two SAU channels.

Target Device

RL78/L12

Development environment

IDE: e2studio

Compiler: GNURL78 v12.02 -ELF

Hardware: Renesas Starter Kit for RL78/L12

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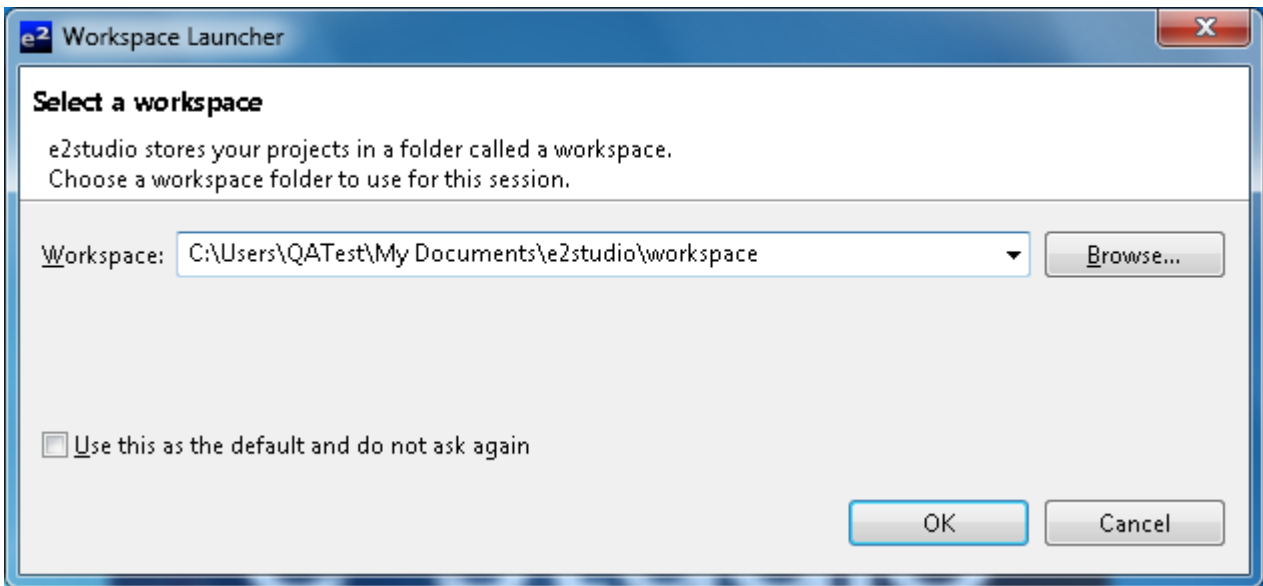
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1. Installation

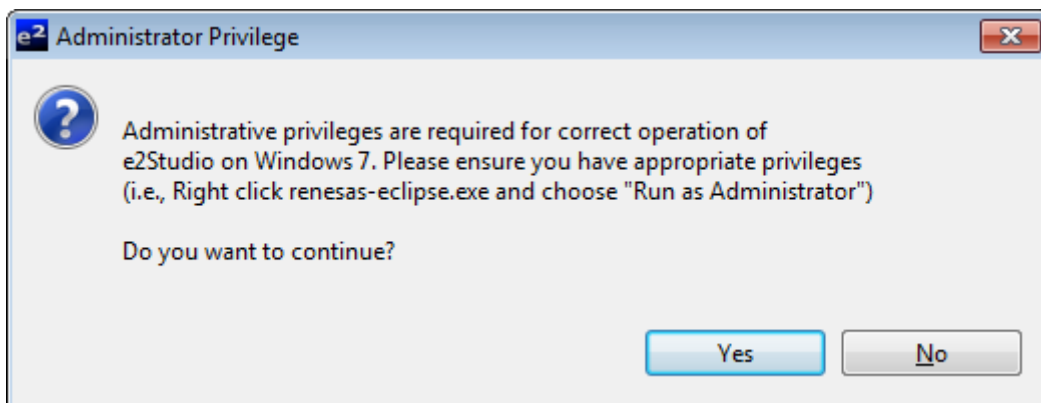
This section assumes e2studio IDE is already installed on the user's personal computer (PC). Create a new folder and name it as 'RSKRL78L12_Workspace'. Copy the zipped file 'an_r01an1590eg0100_r178112_serial.zip', available in the Application Note package downloaded from the website, to this folder. Extract the 'an_r01an1590eg0100_r178112_serial.zip' file to the RSKRL78L12_Workspace folder.

2. Creating the Project Workspace

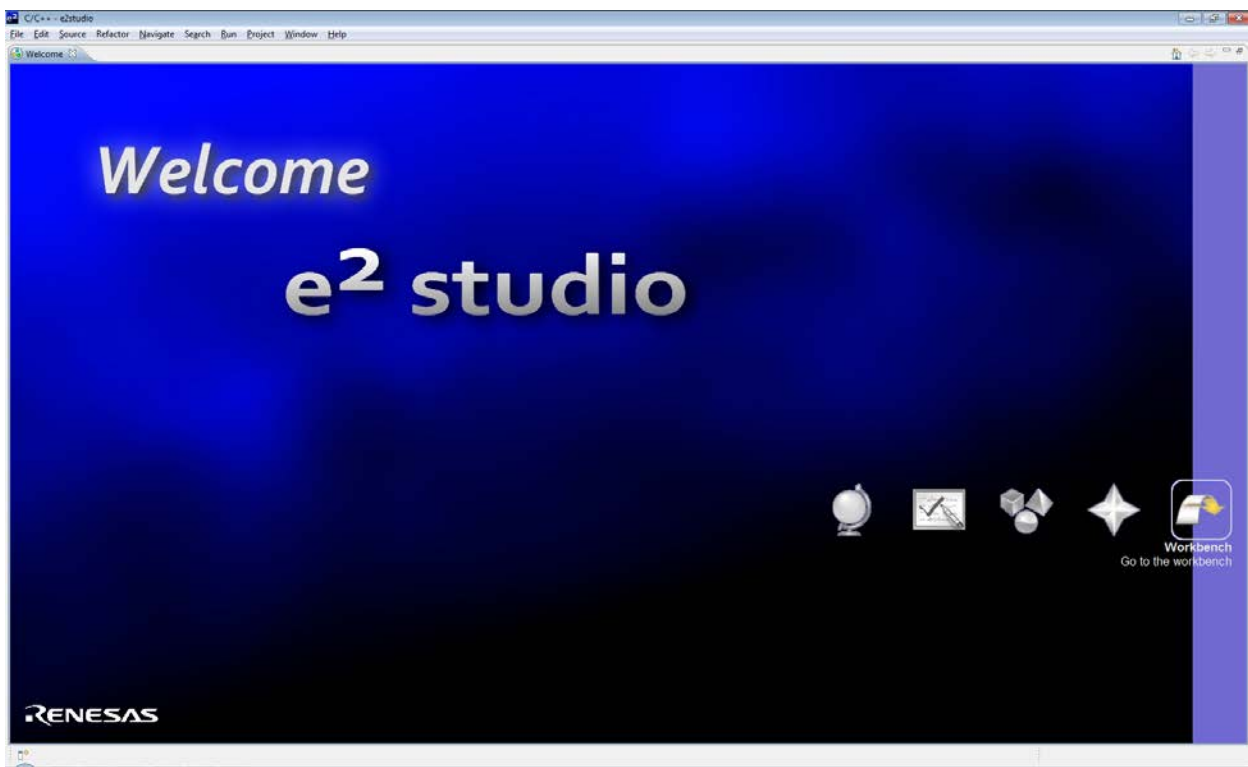
Open e2studio IDE by clicking the Windows Start button, select All Programs > Renesas Electronics e2studio > Renesas e2studio.



Select <OK>.

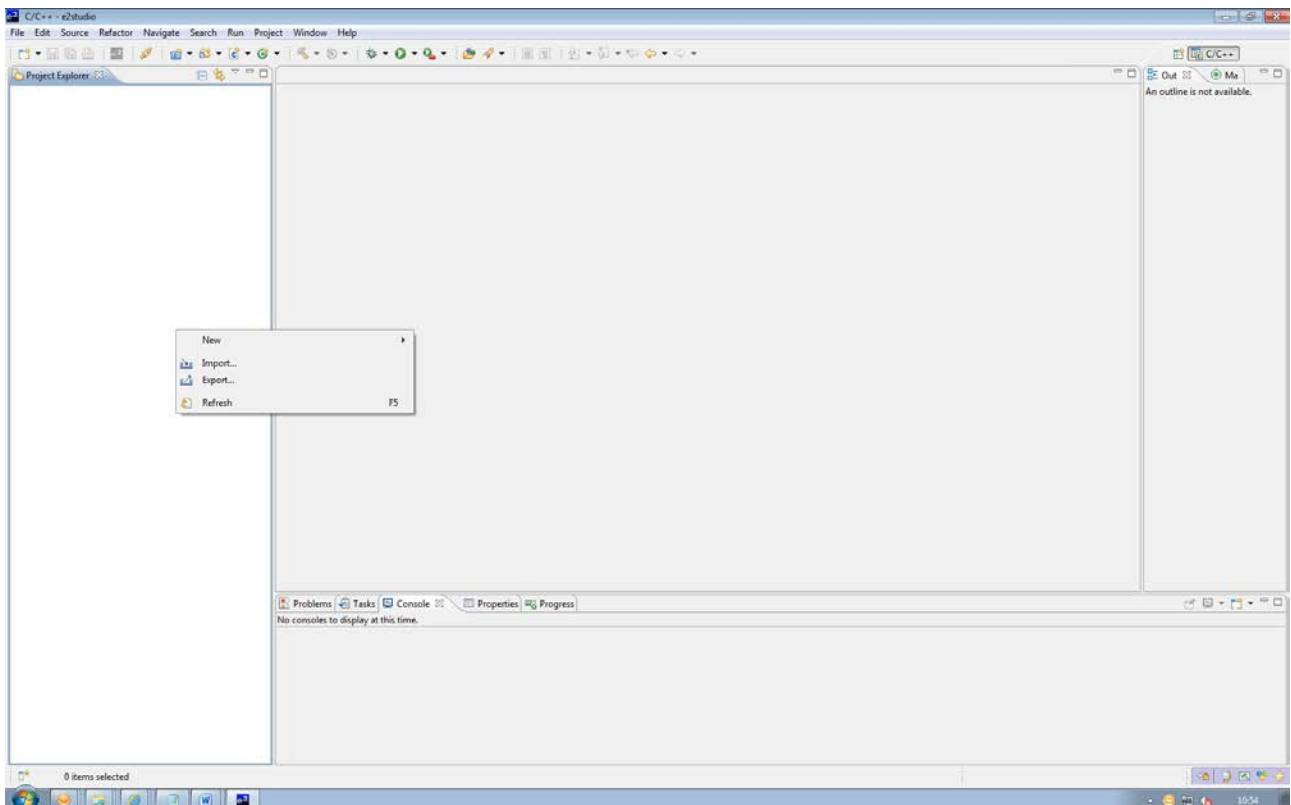


Select <Yes> to Administrator Privilege dialog.

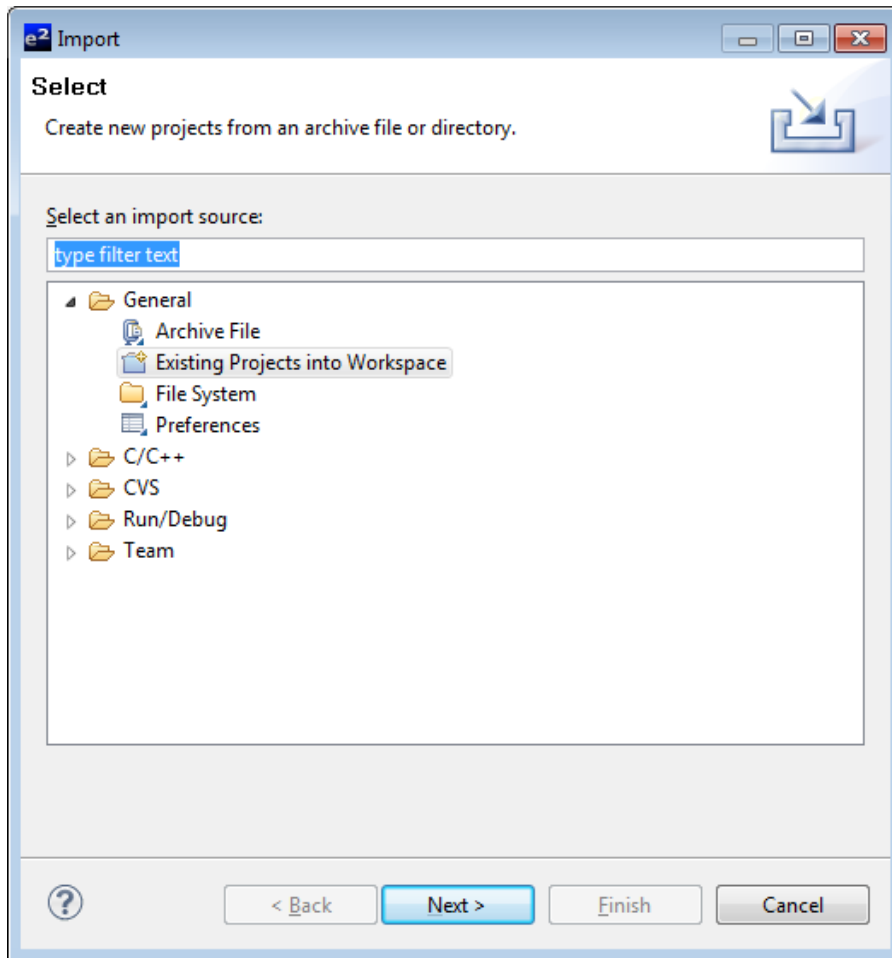


On the welcome screen select 'Go to the Workbench' icon as shown above.

1. Once the e2studio environment has initialised, right click in the project explorer window and click <Import...>



- The Import dialog will now appear. Expand the “General” folder icon, and select “Existing Projects into Workspace”, then click ‘Next’.

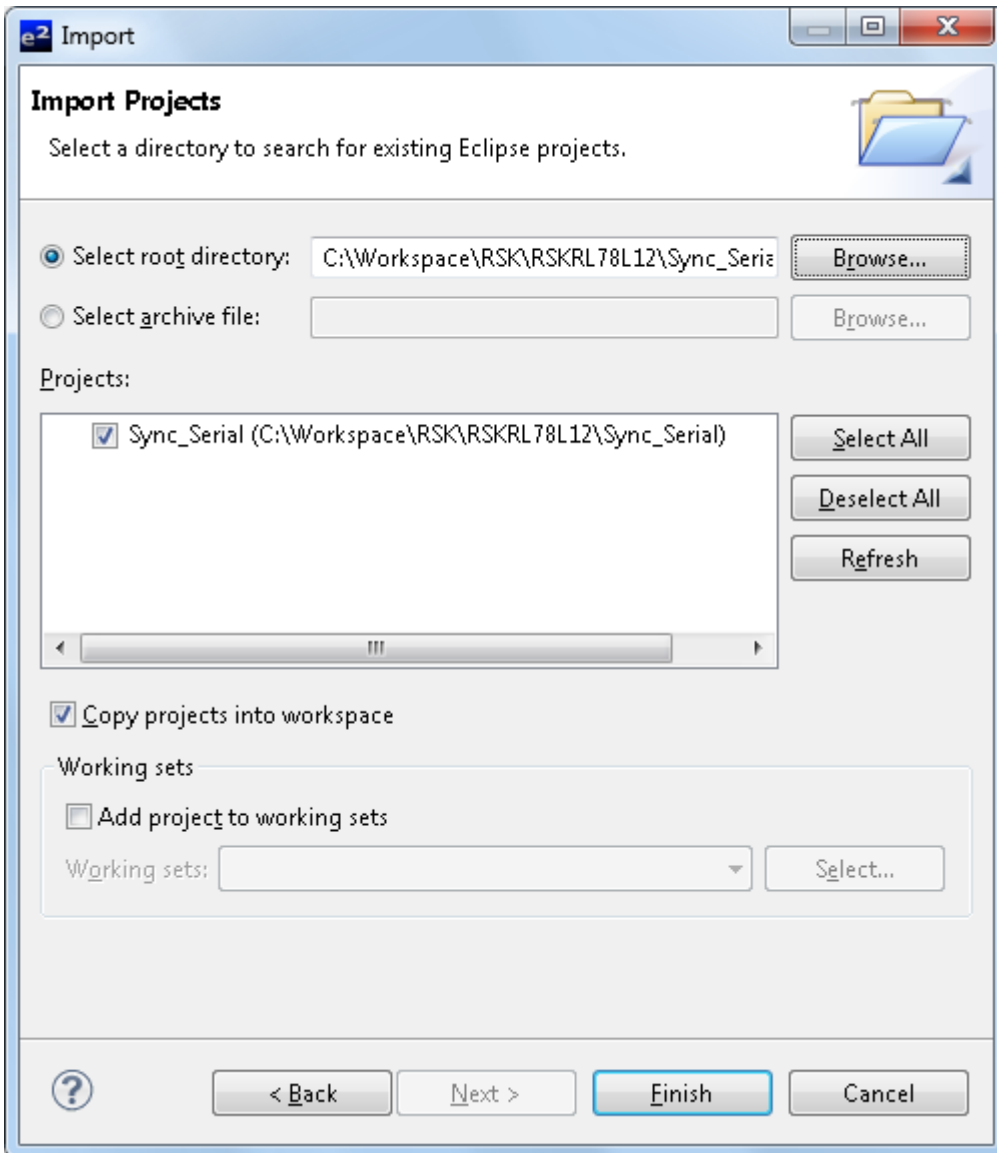


- The Import Dialog will now appear ,specify the project to import .Click the “Browse” button and locate the directory: C:\Workspace\RSK\RSKRL78L12

Navigate to the unzipped Sync_Serial folder located folder located in RSKRL78L12 Workspace folder.
Select the Sync_Serial folder located folder.

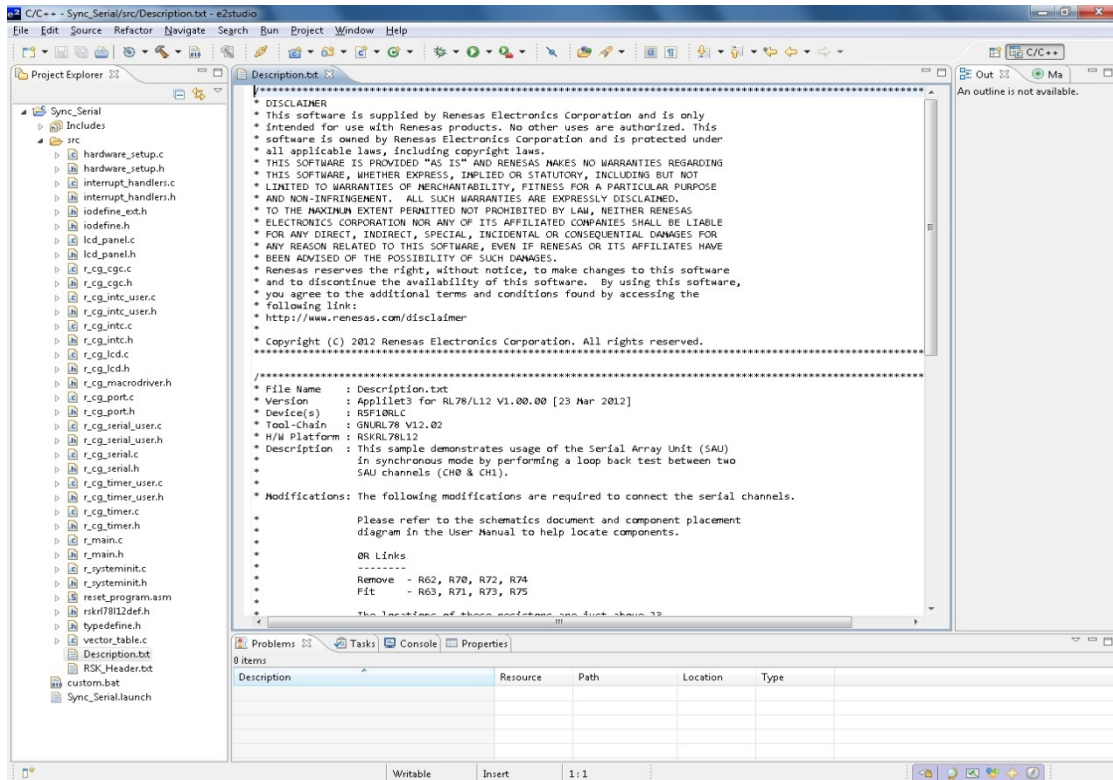
And also ensure that the ‘Copy projects into workspace’ option is ticked, and then click <Finish>

The IDE e2studio will load the project.



3. Opening Sample Code and Source Files

Once the project has been opened, the source code and all dependant files can be opened in the editor by expanding the folders in the Project Tree window and double clicking the files listed. All files have been grouped according to their file type.



4. Source Code Functionality

The source code project is specifically written to run on the appropriate RSK. However this source code can be useful as an example even without the RSK.

The project was written using source files containing API functions generated using Code Generator. The project will contain a C source file 'r_main.c'. This source file includes the C function main(). All source files and dependent files whose filenames are prefixed with 'r_' were generated using Applilet3 (Application Leading Tool). For more information, refer to Description.txt.

5. Code Execution

The sample code demonstrates usage of the Serial Array Unit (SAU) in synchronous mode by performing a loop back test between two SAU channels (CH0 & CH1).

Modifications:

The following required changes assume there has been no prior modification to the board. Please refer to the RSKRL78L12 User Manual and Schematics if in doubt.

Please refer to the schematics document and component placement diagram in the User Manual to help locate components.

OR Links

Remove - R62, R70, R72, R74

Fit - R63, R71, R73, R75

The locations of these resistors are just above J3.

Jumper Links

Short pins 1 and 2 of J10, J11, J12 and J13.

As the serial lines are shared with the LCD segment lines, some of the characters cannot be displayed. The main ones are the alphanumeric display. For this reason, the numeric display is used for displaying the test status.

Instructions:

1. Build and download the sample code to the RSK.
2. Open the 'Expressions' window from the menu bar: Window > Show View > Expressions.
3. Copy the variable `g_csi00_rx_buffer` to the 'Expressions' window. Do the same for the variable `g_csi00_tx_buffer`, `g_csi01_rx_buffer` and `g_csi01_tx_buffer`.
4. Click the 'Resume' button to start the program execution. The name of the sample ("SyC") will be displayed on the LCD.
5. Press SW1 to start the test.
6. The sample will display "PAS" for pass, "FAL" for fail or ERx for communication errors.
7. You can observe the contents of `g_csi00_rx_buffer` being filled with the contents of `g_csi01_tx_buffer`. The contents should match. The same should be done regarding `g_csi00_tx_buffer` and `g_csi01_rx_buffer`.
8. If the LCD reports "ERx", or "FAL", then check the resistor placement and the wire links.
9. Press SW1 to restart the test.

6. Website, Inquiries and Support

Renesas Electronics Website

<http://www.renesas.com/>

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<http://www.renesas.com/inquiry>

Support

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Revision Record

Rev.	Date	Description	
		Page	Summary
1.00	November 01, 2013	—	First edition issued

General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

Handle unused pins in accord with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.

In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.

In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable.

When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different type number, confirm that the change will not lead to problems.

- The characteristics of an MPU or MCU in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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