

RENESAS TOOL NEWS on June 16, 2014: 140616/tn5

Revision to Device Driver for Enabling Clock-Synchronous Serial Communications Using Serial Array Unit (SAU) in CSI Mode of RL78 Family MCUs

We have revised the device driver for enabling clock-synchronous serial communications using the SAU in CSI Mode of MCUs of RL78 Family from V.2.03 Release 00 to V.2.03 Release 01.

We supply this device driver free of charge.

Click on the URL for the driver you require below and refer to the block diagram at the link for the software configuration. These pages are to be updated on June 20.

SPI Serial EEPROM Driver

https://www.renesas.com/driver/spi_serial_eeprom

SPI/QSPI Serial Flash Memory, QSPI Serial Phase Change Memory Driver

https://www.renesas.com/driver/spi_serial_flash

1. Description of Revision

MCUs of the following groups have been supported:

- In RL78 Family: RL78/G1C, RL78/L12, RL78/L13 and RL78/L1C groups

2. Obtaining Device Driver

(1) Open the following web page for the driver you require below:

https://www.renesas.com/a_eeprom_driver_app_notes

(page for the SPI serial EEPROM drivers)

https://www.renesas.com/a_serial_flash_driver_app_notes

(page for the SPI/QSPI serial flash memory and QSPI serial phase change memory drivers)

You can also see the list of titles of Application Notes.

(2) Select RL78 Family in the Select by Products box and click on Search by Products.

Only the application notes for the RL78 Family will be listed.

(3) In the list, find the following application note:

"RL78/G14, RL78/G1C, RL78/L12, RL78/L13, RL78/L1C Group Clock Synchronous Single Master Control Software Using CSI Mode of Serial Array Unit"

This application note will be updated on June 20, 2014.

(4) Click the link in the Project Files column to the right of the above title.

(5) Read through the AGREEMENT; then click on Agree.

(6) Download the sample program from the Download page.

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