

RENESAS TOOL NEWS on May 16, 2014: 140516/tn16

Parameter Files for Flash Memory Programmer PG-FP5 Released

We have published new parameter files and have revised several of the existing ones for flash memory programmer PG-FP5. These parameter files, written in text format, contain the information about MCUs that is necessary when you program the flash memories on those MCUs.

For an overview of PG-FP5, go to:

https://www.renesas.com/pg_fp5

1. Newly Published Parameter Files

(1) For RH850 family

- PR5-RH850F1X V1.00 (for RH850/F1x series)

MCU support:

R7F701002, R7F701007, R7F701010, R7F701013, R7F701017, R7F701018,
R7F701022, R7F701023, R7F701025, R7F701027, R7F701028, R7F701029,
R7F701032, R7F701033, R7F701034, R7F701054, R7F701056 and
R7F701057

(2) For RX family

- PR5-R5F564M V1.00 (for RX64M group)

MCU support: R5F564MF, R5F564MG, R5F564MJ and R5F564ML

2. Revised Parameter Files

2.1 Increase in Device Support for RL78 Family

PR5-R5F10Y (for RL78/G10 group) revised from V1.00 to V1.01.

Increased support: R5F10Y17, R5F10Y44, R5F10Y46 and R5F10Y47

2.2 Functions Introduced on Parameter Files for RX Family

You can program data via the FINED port on your target MCU and PG-FP5 when using any of the following MCUs:

(1) PR5-R5F5210 (for RX210 group) revised from V1.02 to V1.03.

MCU support: R5F52103B, R5F52104B, R5F52105B, R5F52106B, R5F52107B,
R5F52107C, R5F52108B, R5F52108C, R5F5210AB and
R5F5210BB

(2) PR5-R5F521A (for RX21A group) revised from V1.00 to V1.01.

MCU support: R5F521A6B, R5F521A7B and R5F521A8B

2.3 Correction of Error on Parameter Files for R8C Family

(1) PR5-R8C3X (for R8C/3x series) revised from V1.04 to V1.05.

- The following problem has been fixed:

If any command was executed for the MCU in which the forced erase function (one of the ID code check functions) was enabled, the following error occurred during ID code authentication after the standard serial I/O mode was specified.

ERROR(E017): Connection or Synchronisation failed.

- Involved MCUs:

R5F21321C, R5F21322C, R5F21324C, R5F21324G, R5F21326G, R5F21324H, R5F21326H, R5F21321M, R5F21322M, R5F21324M, R5F21331C, R5F21332C, R5F21334C, R5F21335C, R5F21336C, R5F21334G, R5F21336G, R5F21334H, R5F21336H, R5F21331M, R5F21332M, R5F21334M, R5F21335M, R5F21336M, R5F21334T, R5F21335T, R5F21336T, R5F21344C, R5F21345C, R5F21346C, R5F21348K, R5F2134CK, R5F21344M, R5F21345M, R5F21346M, R5F21344P, R5F21346P, R5F21344R, R5F21346R, R5F21346U, R5F21348U, R5F21346W, R5F21347W, R5F21348W, R5F2134AW, R5F2134CW, R5F21346X,

R5F21347X,

R5F21348X, R5F2134AX, R5F2134CX, R5F21346Y, R5F21347Y, R5F21348Y, R5F2134AY, R5F2134CY, R5F21346Z, R5F21347Z, R5F21348Z, R5F2134AZ, R5F2134CZ, R5F21354C, R5F21355C, R5F21356C, R5F21357C, R5F21358C, R5F2135AC, R5F2135CC, R5F21354M, R5F21355M, R5F21356M, R5F21357M, R5F21358M, R5F2135AM, R5F2135CM, R5F21364C, R5F21365C, R5F21366C, R5F21367C, R5F21368C, R5F2136AC, R5F2136CC, R5F21364M, R5F21365M, R5F21366M, R5F21367M, R5F21368M, R5F2136AM, R5F2136CM, R5F21368S, R5F2136AS, R5F2136CS, R5F21368W, R5F2136AW, R5F2136CW, R5F21368X, R5F2136AX, R5F2136CX, R5F21368Y, R5F2136AY, R5F2136CY, R5F21368Z, R5F2136AZ, R5F2136CZ, R5F21386C, R5F21387C, R5F21388C, R5F2138AC, R5F2138CC, R5F21386M, R5F21387M, R5F21388M, R5F2138AM, R5F2138CM, R5F21388S, R5F2138AS, R5F2138CS, R5F21388W, R5F2138AW, R5F2138CW, R5F21388X, R5F2138AX, R5F2138CX, R5F21388Y, R5F2138AY, R5F2138CY, R5F21388Z, R5F2138AZ, R5F2138CZ, R5F213G1C, R5F213G2C, R5F213G4C, R5F213G5C, R5F213G6C, R5F213G2M, R5F213G4M, R5F213G5M, R5F213G6M, R5F213J2C, R5F213J4C, R5F213J5C, R5F213J6C, R5F213J2M, R5F213J4M, R5F213J5M, R5F213J6M, R5F213J4T, R5F213J5T, R5F213J6T, R5F213M8K, R5F213MCK, R5F213M6Q, R5F213M7Q, R5F213M8Q, R5F213MAQ,

R5F213MCQ,

R5F213M6U, R5F213M8U, R5F213N7T, R5F213N8T, R5F213NAT and R5F213NCT

(2) PR5-R8C5X (for R8C/5x series) revised from V1.01 to V1.02.

- The following problem has been fixed:

If any command was executed for the MCU in which the forced erase function (one of the ID code check functions) was enabled, the following error occurred during ID code authentication after the standard serial I/O mode was specified.

ERROR(E017): Connection or Synchronisation failed.

- Involved MCUs:

R5F21546E, R5F21547E, R5F21548E, R5F2154AE, R5F2154CE, R5F21546F, R5F21547F, R5F21548F, R5F2154AF, R5F2154CF, R5F21546G, R5F21547G, R5F21548G, R5F2154AG, R5F2154CG, R5F21546H, R5F21547H, R5F21548H, R5F2154AH, R5F2154CH, R5F21566E, R5F21567E, R5F21568E, R5F2156AE, R5F2156CE, R5F21566F, R5F21567F, R5F21568F, R5F2156AF, R5F2156CF, R5F21566G, R5F21567G, R5F21568G, R5F2156AG, R5F2156CG, R5F21566H, R5F21567H, R5F21568H, R5F2156AH and R5F2156CH

(3) PR5-R8CLX (for R8C/Lx series) revised from V1.02 to V1.03.

- The following problem has been fixed:

If any command was executed for the MCU in which the forced erase function (one of the ID code check functions) was enabled, the following error occurred during ID code authentication after the standard serial I/O mode was specified.

ERROR(E017): Connection or Synchronisation failed.

- Involved MCUs:

R5F2L357C, R5F2L358C, R5F2L35AC, R5F2L35CC, R5F2L357M, R5F2L358M, R5F2L35AM, R5F2L35CM, R5F2L367C, R5F2L368C, R5F2L36AC, R5F2L36CC, R5F2L367M, R5F2L368M, R5F2L36AM, R5F2L36CM, R5F2L387C, R5F2L388C, R5F2L38AC, R5F2L38CC, R5F2L387M, R5F2L388M, R5F2L38AM, R5F2L38CM, R5F2L3A7C, R5F2L3A8C, R5F2L3AAC, R5F2L3ACC, R5F2L3A7M, R5F2L3A8M, R5F2L3AAM, R5F2L3ACM, R5F2LA32A, R5F2LA34A, R5F2LA36A, R5F2LA38A, R5F2LA52A, R5F2LA54A, R5F2LA56A, R5F2LA58A, R5F2LA64A, R5F2LA66A, R5F2LA67A, R5F2LA68A, R5F2LA6AA, R5F2LA6CA, R5F2LA84A, R5F2LA86A, R5F2LA87A, R5F2LA88A, R5F2LA8AA, R5F2LA8CA, R5F2LAP6S, R5F2LAP7S, R5F2LAP8S, R5F2LAPAS and R5F2LAPCS

3. Obtaining Parameter Files

Download the parameter files you need from the web site at:

https://www.renesas.com/pg_fp5_download

The above-mentioned parameter files will be published on this site on May 20, 2014.

For details of how to install them, see Chapter 3 of PG-FP5 Flash Memory Programmer User's Manual.

4. Firmware Products, FPGA Data Items, and Programming GUIs

For the versions of the firmware products, FPGA data items, and programming GUIs necessary to use the above-mentioned parameter files, see the Parameter Files Release Note on the web page at:

https://www.renesas.com/pg_fp5_documentation

The Release Note will be published on this site on May 20, 2014.

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