# [Notes]

CS+ Code Generator for RH850,

# AP4 Coding Assistance Tool for RH850

R20TS0622EC0100 Rev.1.00 Oct. 01, 2020

## Outline

When using the products in the title, note the following points.

- 1. When using Clocked Serial Interface H
- 2. When using Clocked Serial Interface G

## When using Clocked Serial Interface H

# 1.1 Applicable Products

- CS+ Code Generator for RH850 V1.00.00 (CS+ for CC V4.00) or later
- AP4 for RH850 V1.01.00 or later

# 1.2 Applicable Devices

RH850 family: RH850/F1K group

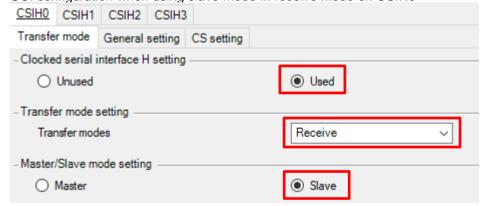
#### 1.3 Details

When using CSIH with slave mode and receive mode or transmit/receive mode selected on the following peripherals, transmission processing will not work from the second time because the variable of receive count initialization is incorrect.

> RH850/F1K:

CSIH0, CSIH1, CSIH2, CSIH3

GUI configuration when using slave mode in receive mode on CSIH0



## 1.4 Workaround

Manually modify the variable name of receive count from "g\_<*csihn*>\_tx\_num" to "g\_<*csihn*>\_rx\_num" in the following source file <sup>(Note)</sup>. <*csihn*> varies depending on the selected peripheral.

- Source file: "r\_cg\_csih.c".
- Function: "MD STATUS R < CSIHn>\_Slave\_Receive (uint16 t\* rx buf, uint16 t rx num)"

Note: If code is generated again, the previous state is restored. Modification is necessary each time you perform code generation.

The following is an example of the required modification when *CSIHn* is CSIH0. Manually modify the wrong code in red to correct code in blue.

#### Before modification

```
MD STATUS R CSIHO Slave Receive(uint16 t* rx buf, uint16 t rx num)
MD_STATUS status = MD OK;
if (rx num < 1U)
   status = MD ARGERROR;
else
{
   g_csih0_rx_total_num = rx_num;
   gp csih0 rx address = rx buf;
   g csih0 tx num = 0U;
}
return (status);
```

#### After modification

```
MD STATUS R CSIHO Slave Receive(uint16 t* rx buf, uint16 t rx num)
MD STATUS status = MD OK;
if (rx num < 1U)
   status = MD ARGERROR;
}
else
   g_csih0_rx_total_num = rx_num;
   gp csih0 rx address = rx buf;
   g_csih0_rx_num = 0U;
return (status);
```

## 1.5 Schedule for Fixing the Problem

This problem will be fixed in a later version.

# When using Clocked Serial Interface G

## 2.1 Applicable Products

- > CS+ Code Generator for RH850 V1.00.00 (CS+ for CC V4.00) or later
- AP4 for RH850 V1.01.00 or later

## 2.2 Applicable Devices

RH850 family: RH850/F1KM group

#### 2.3 Details

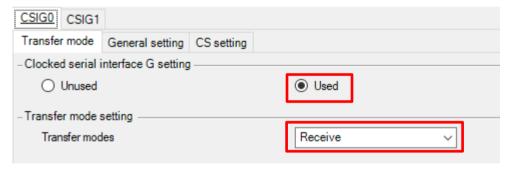
When using CSIG and receive mode or transmit/receive mode selected on the following peripherals, transmission processing will not work from the second time because the variable of receive count initialization is incorrect.

> RH850/F1K: 100-pin products

CSIG0

RH850/F1K: 144-pin, 176-pin products CSIG0, CSIG1

■ GUI configuration when using receive mode on CSIG0



## 2.4 Workaround

Manually modify the variable name of receive count from "g\_<csign>\_tx\_num" to "g\_<csign>\_rx\_num" in the following source file (Note). <csign> varies depending on the selected peripheral.

- Source file: "r\_cg\_csig.c".
- Function: "MD\_STATUS R\_<CSIGn>\_Receive (uint16\_t\* rx\_buf, uint16\_t rx\_num)"

Note: If code is generated again, the previous state is restored. Modification is necessary each time you perform code generation.

The following is an example of the required modification when <CSIGn> is CSIG0. Manually modify the wrong code in red to correct code in blue.

## Before modification

#### After modification

# 2.5 Schedule for Fixing the Problem

This problem will be fixed in a later version.

# **Revision History**

		Description	
Rev.	Date	Page	Summary
1.00	Oct.01.20	-	First edition issued

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TS Colophon 4.1