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A Correction to the User's Manual of the HI7000/4, HI7700/4, and HI7750/4 Real-Time OSes

We notify a correction to the user's manual of the HI7000/4, HI7700/4, and HI7750/4 real-time OSes as stated below.

The above real-time OSes are used for the SuperH RISC engine family of MCUs.

1. The Manual and Its Versions Concerned

- (1) HI7000/4 series User's Manual Rev.6.00 (Doc. No. REJ10B0060-0600)
- (2) HI7000/4 series User's Manual Rev.5.00 (Doc. No. REJ10B0060-0500)
- (3) HI7000/4 series User's Manual Rev.4.00 (Doc. No. REJ10B0060-0400H)
- (4) HI7000/4 series User's Manual Rev.3.00 (Doc. No. REJ10B0060-0300H)
- (5) HI7000/4 series User's Manual Rev.2.0 (Doc. No. ADJ-702-248A)
- (6) HI7000/4 series User's Manual Rev.1.0 (Doc. No. ADJ-702-248)

Here, "HI7000/4 series User's Manual" is used for the following three real-time OSes in common:

- HI7000/4 (Supporting the SH-1-, SH-2-, SH2-DSP-, SH-2E-, * SH-2A-, and SH2A-FPU-cored MCUs)
 - HI7700/4 (Supporting the SH-3-, SH3-DSP-, and SH4AL-DSP-cored MCUs)
 - HI7750/4 (Supporting the SH-4- and SH-4A-cored MCUs)
- * The SH-2E-FPU-cored MCUs excluded.

2. Correction

In the line beginning with FP under the heading of "Packet Structure" on Section 3.20.1 "Define Interrupt Handler (def_inh, idef_inh)," read inthdr for inhhdr in all the versions of the manual.

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