

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

# RENESESAS TECHNICAL UPD

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RenesasTechnology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-521A/EA	Rev.	1.0
Title	A usage notice on the SDRAM low frequency mode		Information Category	Usage Limitation		
Applicable Product	HD6417705	Lot No.	Reference Document	SH7705 Group Hardware Manual Rev.2.0 REJ09B0082-0200O		
		ALL				

SH7705 SDRAM low frequency mode has following usage notes.

[Condition]

- Setting "1" to the SLOW bit in SDRAM Control Register(SDCR)  
(Setting for the low frequency mode)

and

- Setting "1" to the WM bit in CSn Space Wait Control Register(CSnWCR) of any CSn spaces except for SDRAM space  
(Setting for masking external WAIT)

[Phenomena]

On the above condition, when an access to the CSn space (not for SDRAM) and an access to the SDRAM space occurred consecutively, the negate timing of the CSn may be delayed for a half CKIO cycle.

[Workaround]

Either of followings is workaround.

- Please set "0" to the SLOW bit in SDCR (setting for normal mode) .
- Please set "0" to the WM bit in CSnWCR (setting for enabling external WAIT).