## Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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## HITACHI SEMICONDUCTOR TECHNICAL UPDATE

DATE	9th August, 1999	No		TN-SH7-179 A /E
THEME	Revision of BCR1 description			
CLASSIFICATION	☐ Spec. change ☐ Limitation on Use  Supplement of Documents			
PRODUCT NAME	HD6417750BP200, HD6417750F167, HD6417750VF128			
REFERENCE	SH7750 Hardware Manual			Effective Date eternity
DOCUMENTS			From	
Revision of SH7750 Hardware Manual  Description of BCR1				
Di GOM				
Bit:30 Master  0: In a power-on reset, the master/slave setting external pin (MD7) is high, designating master mode for the SH7750  1: In a power-on reset, the master/slave setting external pin (MD7) is low, designating slave mode for the SH7750				
Bit 29: A0MPX 0: In a power-on reset, the external pin specifying the area 0 memory type (MD6) is high, designating the area 0 memory type as normal memory 1: In a power-on reset, the external pin specifying the area 0 memory type (MD6) is low, designating the area 0				
memory type as MPX				