To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

Date: Dec.14.2007

RENESAS TECHNICAL UPDATE

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Renesas Technology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-A628A/E	Rev.	1.00
Title	Notes on A/D Conversion-End Interrupt and DMA Transfer		Information Category	Technical Notification		
Applicable Product	SH7720 Group SH7721 Group SH7705 Group	Lot No. All	Reference Document	SH7720 hardware manual Rev2.00 (REJ09B0033-0200) SH7705 hardware manual Rev2.00 (REJ09B0082-0200)		

There are following notes about ADC of the SH7720 Group, the SH7721 Group and the SH7705 group.

Generation of an interrupt or activation of the DMAC upon the end of A/D conversion is only allowed once per end of A/D conversion. The conditions for the end of A/D conversion are the same as the setting conditions of the ADF bit of ADCSR. According to the following table, DMA transfer (Cycle steal mode) should be done for a data size best suited to the conversion mode and the number of channels.

Conversion Mode	Number of Channels for Conversion	Data Size	Transfer Size for DMAC
Single mode	1	1 Word	Word
Multi mode	1	1 Word	Word
Scan mode	2	2 Word	Longword
	3	3 Word	16 Bytes
	4	4 Word	16 Bytes