To our customers,

Old Company Name in Catalogs and Other Documents

   On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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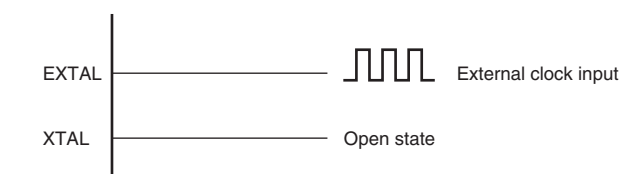RENESAS

RENESAS TECHNICAL UPDATE

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Renesas Technology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-A671A/E	Rev.	1.00
Title	Note on inputting external clock		Information Category	Technical Notification		
Applicable Product	SH7263 Group SH7203 Group	Lot No.	Reference Document	SH7263 Group Hardware Manual Rev.2.00 (REJ09B0290-0200) SH7203 Group Hardware Manual Rev.2.00 (REJ09B0313-0200)		
		ALL				

We would like to inform you of note on inputting external clock in the above-mentioned applicable products.

[Note on inputting external clock]

Figure 1 is an example of connecting the external clock input. When putting the XTAL pin in open state, make sure the parasitic capacitance is less than or equal to 10 pF. Even in the case of inputting external clock, to keep PLL oscillation stabilizing time, please wait longer than the oscillation stabilizing time at power on, releasing the standby, changing the multiplication rate of PLL circuit, or start inputting external clock.

EXTAL — External clock input
XTAL — Open state

Example of connection with XTAL pin open

Figure 1   Example of Connecting External Clock

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