

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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# RENESAS TECHNICAL UPDATE

Classification of Production	MPU&MCU		No	TN-SH7-486A/E	Rev	1
THEME	An Errata in the SH7705 Hardware Manual	Classification of Information	<ol style="list-style-type: none"> <li>1. Spec change</li> <li>②. Supplement of Documents</li> <li>3. Limitation of Use</li> <li>4. Change of Mask</li> <li>5. Change of Production Line</li> </ol>			
PRODUCT NAME	HD6417705	Lot No.	Reference Documents	SH7705 Hardware manual Rev.1.0 ADE-602-276	term of validity	
		ALL			Eternity	

There is an errata in the SH7705 Hardware Manual (Rev.1.0) on SDRAM mode register access.

The section 7.8.10 Power-On Sequence in the manual shows that the SDRAM mode register access is byte-size access. However, it must be word-size access.

7.8.10 Power-On Sequence (page 229 of 690)

Line 8~11

Errata:

In this operation the data is ignored, the mode write is performed as a byte-size access. To set burst read/single write, CAS latency 2 to 3, wrap type = sequential, and burst length 1 supported by the LSI, arbitrary data is written in a byte-size access to the addresses shown in table 7.17.

Correction:

In this operation the data is ignored, the mode write is performed as a word-size access. To set burst read/single write, CAS latency 2 to 3, wrap type = sequential, and burst length 1 supported by the LSI, arbitrary data is written in a word-size access to the addresses shown in table 7.17.