

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SH7-A804A/E	Rev.	1.00
Title	Correction of Errors in Pin Assignment for the SH7266 Group, SH7267 Group		Information Category	Technical Notification		
Applicable Product	SH7266 Group SH7267 Group	Lot No.	Reference Document	SH7266 Group, SH7267 Group User's Manual :Hardware Rev. 1.00 (REJ09B0566-0100)		
		All lots				

Thank you for your consistent patronage of Renesas semiconductor products.

We would like to inform you of the corrections to errors in the hardware manual of the above applicable products.

Please pay attention to these corrections when using the products.

The following items in the SH7266 Group, SH7267 Group User's Manual (Hardware) are corrected.

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Figure 1.2 (1) Pin Assignment for the SH7266 Group

[Before correction] Pin No. 22 PB15/A15/TIOC4D/LCD_DATA3

[After correction] Pin No.22 PB15/A15/TIOC3D/LCD_DATA3

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Figure 1.2 (2) Pin Assignment for the SH7267 Group

[Before correction] Pin No.26 PB15/A15/TIOC4D/LCD_DATA3

[After correction] Pin No.26 PB15/A15/TIOC3D/LCD_DATA3

[Before correction] Pin No.43 PB22/A22/CS4/RxD4 (CS4 without Overbar)

[After correction] Pin No.43 PB22/A22/ $\overline{\text{CS4}}$ /RxD4 (CS4 with Overbar)

[Before correction] Pin No.124 PF12/BS/MISO0/TIOC3D/SPDIF_OUT/QMI/QIO1 (BS without Overbar)

[After correction] Pin No.124 PF12/ $\overline{\text{BS}}$ /MISO0/TIOC3D/SPDIF_OUT/QMI/QIO1 (BS with Overbar)