

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A0215A/E	Rev.	1.00
Title	Changes to the Procedure of Transition to Software Standby Mode for RX64M, RX65N, and RX71M Group Products		Information Category	Technical Notification		
Applicable Product	RX64M Group, RX65N Group, RX71M Group	Lot No.	Reference Document	User's Manual: Hardware for applicable products (see the table of Reference Documents)		
		All				

This document describes a modification to the procedure of transition to software standby mode described in section 11.6.3.1, “Transition to Software Standby Mode” in the User’s Manual: Hardware for the applicable products stated above.

Page and section numbers are based on the manual for the RX64M Group. Refer to the table of “Reference Documents” for the corresponding page and section numbers in the other groups.

•Page 383 of 2936

The waiting process after stopping the ethernet controller is deleted from step (1) as follows.

For the note on entering software standby mode regarding the ethernet controller, refer to section 37.5.2, “Stopping the EDMAC during Operations”.

Before correction

- (1) When the ethernet controller is in use, set bits ETHERC0.ECMR.TE and ETHERC0.ECMR.RE to 0. After checking that bits EDMAC0.EDTRR.TR and EDMAC0.EDRRR.RR have been set to 0, wait for 100 ns, and then set the PSW.I bit*1 of the CPU to 0.

After correction

- (1) Set the PSW.I bit*1 of the CPU to 0.

Reference Documents

Group	Manual Title (Document Number)	Page Number	Note for EDMAC
RX64M Group	RX64M Group User’s Manual: Hardware Rev.1.10 (R01UH0377EJ0110)	Page 383 of 2936	37.5.2
RX65N Group	RX65N Group, RX651 Group User’s Manual: Hardware Rev.2.30 (R01UH0590JJ0230)	Page 399 of 2728	35.5.2
RX71M Group	RX71M Group User’s Manual: Hardware Rev.1.10 (R01UH0493EJ0110)	Page 389 of 2952	37.5.2

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