

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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# RENESAS TECHNICAL UPDATE

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Renesas Technology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-A624A/E	Rev.	1.00
Title	Amendments of a timer pulse unit (TPU)		Information Category	Technical Notification		
Applicable Product	SH7720 Group SH7721 Group SH7705 Group SH7730 Group	Lot No.	Reference Document	SH7720 hardware manual Rev2.00 ( REJ09B0033-0200 ) SH7705 hardware manual Rev2.00 ( REJ09B0082-0200 ) SH7730 hardware manual Rev1.00 ( REJ09B0359-0100 )		
		All				

There are the amendments of PWM Mode of a timer pulse unit (TPU) in the SH7720 Group, the SH7721 Group, the SH7705 Group and the SH7730 Group.

## (1) SH7720, SH7721 and SH7705 Group

Amendment

Original

Conditions of duty cycle 0% and 100% are shown below.

- Duty cycle 0% : The set value of the period register (TGRB) is TGRA + 1 for the duty register (TGRA).

Amended

Conditions of duty cycle 0% and 100% are shown below.

- Duty cycle 0% : The set value of the duty register (TGRA) is TGRB + 1 for the period register (TGRB).

## (2) SH7730 Group

Amendment

Original

Conditions of duty cycle 0% and 100% are shown below.

- 0% duty: When periodic register (TPUn\_TGRB) is set to the value equal to duty register TGRA + 1.

Amended

Conditions of duty cycle 0% and 100% are shown below.

- 0% duty: When duty register (TPUn\_TGRA) is set to the value equal to periodic register TGRB + 1.