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April 1st, 2010 Renesas Electronics Corporation

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720 Series Absolute Assembler ASM72 V.1.20.03 Release note Second Edition

Renesas Solutions Corporation
Microcomputer Tool Development Department

March 16, 2007

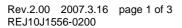
Abstract

Welcome to ASM72 V.1.20.03 (called ASM72).

This material explains the contents of this software and supplements to the user's manual and help. When using this software, please take a look at this release note as well as the user's manual.

Contents

Contents	1
1. Components of the Product	2
2. Operating Environment	2
3. Supplements to the User's Manual	3
3.1. A difference with an old version	3





1. Components of the Product

ASM72 V.1.20.03 comprises the following:

- (1) Program
 - ●asm72.exe
- V.1.20.03
- (2) Release note
 - ●asm72ne.pdf

2. Operating Environment

ASM72 runs on the host machine environments given below.

Host Machine	IBM¹ PC/AT compatibles	
Operating System	Microsoft Windows ² 98	
	Microsoft Windows 2000	
	Microsoft Windows Me	
	Microsoft Windows XP	
CPU	Pentium ³ II 233MHz or higher CPU is	
	recommended.	
Memory	128MB or more system memory is recommended.	

Table 1 Host machine environments

³ Intel and Pentium are registered trademarks of Intel Corporation.



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3. Supplements to the User's Manual

3.1 A difference with an old version

In this version, the following specifications were changed from former version ASM72 V.1.20.02.

3.1.1 Colon designation by a symbol definition

A function has been changed that symbol definition of pseudo instruction. When there was a colon after a symbol name, ASM72 ignores the colon. (Same as V.1.10.05) About a format of a new symbol definition, please refer to the following.

Туре	Format		
Immediate Symbol	SymbolName [:]	.equ	Value
XY Symbol	SymbolName [:]	.equ	ValueX, ValueY
ZXY Symbol	SymbolName [:]	.equ	ValueZ, ValueX, ValueY
Bitsymbol	SymbolName [:]	.equ	ValueBit, ValueZ, ValueX, ValueY

[:]Triangle means a space or tab code.

3.1.2 Forward-referenced Symbol

A function has been changed that forward-referenced symbol of pseudo instruction. (Same as V.1.10.05)

Pseudo Instruction	Contents
.org	Declaring Address
.dw	Setting up data
.line	Indicates the Number of Lines per page
.col	Indicating Columns

ASM72 treats to forward-referenced symbol of instruction (Pseudo Instruction, Machine Language and Macro Instruction) as follows.

Instruction	Contents	ASM72 Version		
		V.1.10.05	V.1.20.02	V.1.20.03
Pseudo Instruction	.col	Support	-	Support
	.dw	Support	-	Support
	.equ	-	-	-
	.if	-	-	-
	.line	Support	-	Support
	.org	Support	-	Support
Machine	LXY	-	Support	Support
Language	LZ	-	Support	Support
	SB,RB,SZB,TAM, XAM,XAMD,XAMI	-	Support	Support
	LA,A	-	Support	Support
	TABP	Support	Support	Support
	SEA	Support	Support	Support
	SZD	Support	Support	Support
	Branch Instruction	Support	Support	Support
Macro	.clb	-	-	-
Instruction	.seb	-	-	-
	.lzxy	-	-	-
	.szxyb	-	-	-

Support: Forward-referenced is possible
-: Forward-referenced is impossible



^{[]:} The portion inside the [] can be omitted.