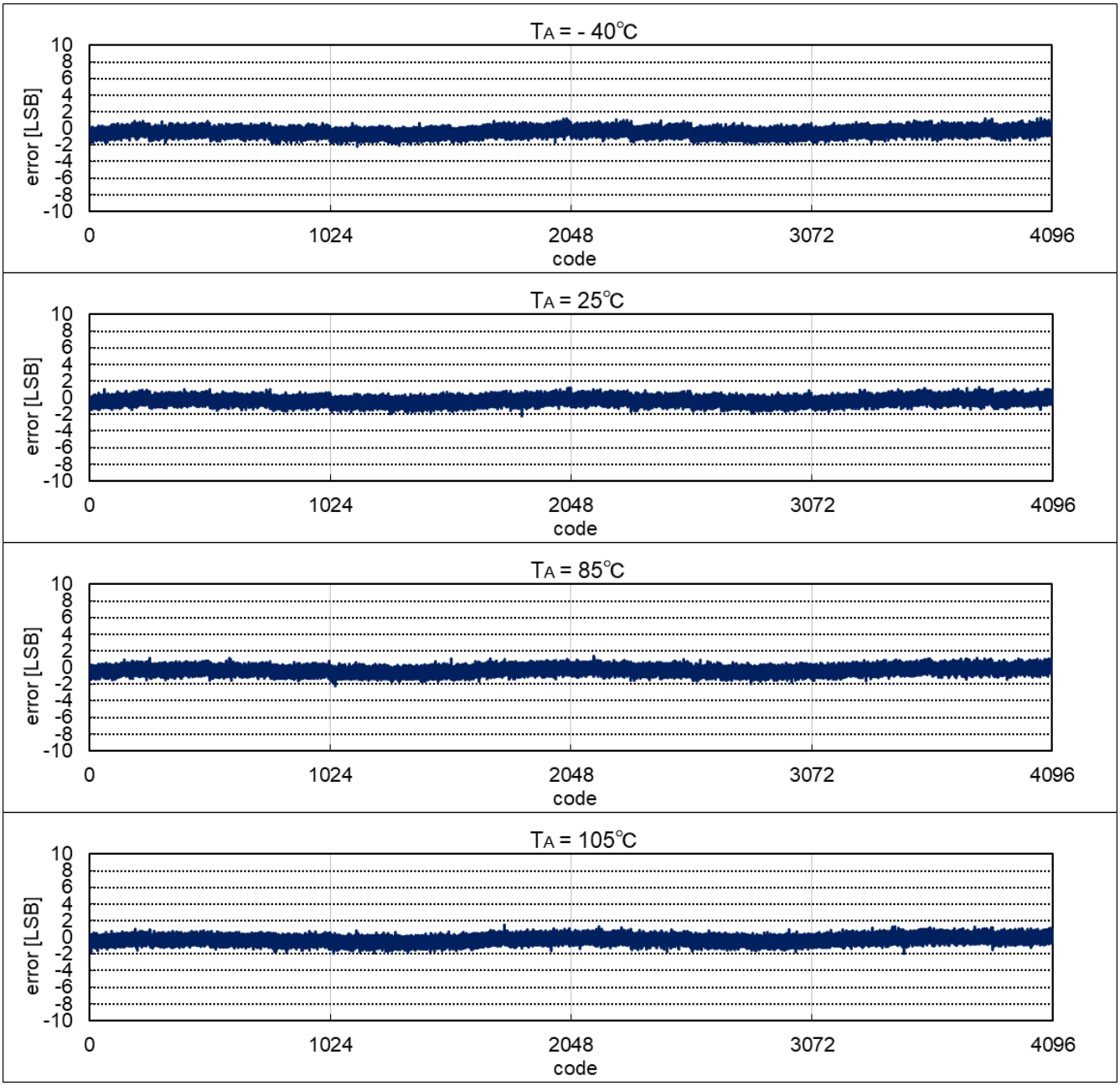
RL78/G23 group
12-bit A/D converter overall error

V_{DD} = EV_{DD0} = EV_{DD1} = 5.0 V
T_A = -40℃, +25℃, +85℃, +105℃
CPU : HS mode, RUN
f_{CLK} = 32 MHz (High-speed OCO)

reference voltage (+) = AV_{REFP} = 5.0 V, reference voltage (-) = AV_{REFM} = 0 V
f_{AD} = 32 MHz
conversion time = 2.0625 μs
mode : Normal mode 1

Prepared on January 10th, 2023



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RL78/G23 group
12-bit A/D converter overall error

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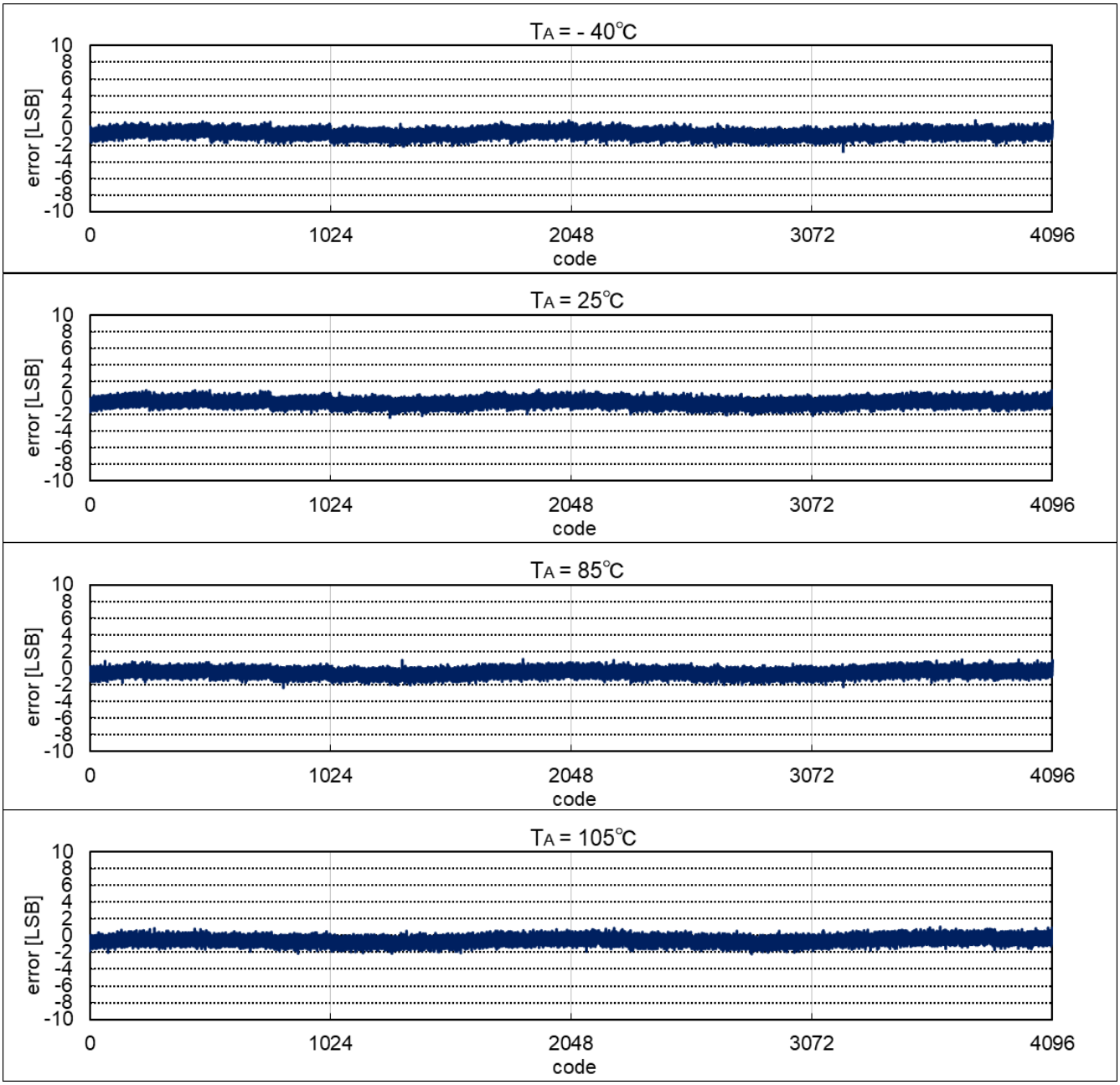

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RL78/G23 group
12-bit A/D converter overall error

V_{DD} = EV_{DD0} = EV_{DD1} = 3.0 V
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CPU : HS mode, RUN
f_{CLK} = 32 MHz (High-speed OCO)

reference voltage (+) = AV_{REFP} = 3.0 V, reference voltage (-) = AV_{REFM} = 0 V
f_{AD} = 32 MHz
conversion time = 2.0625 μs
mode : Normal mode 1

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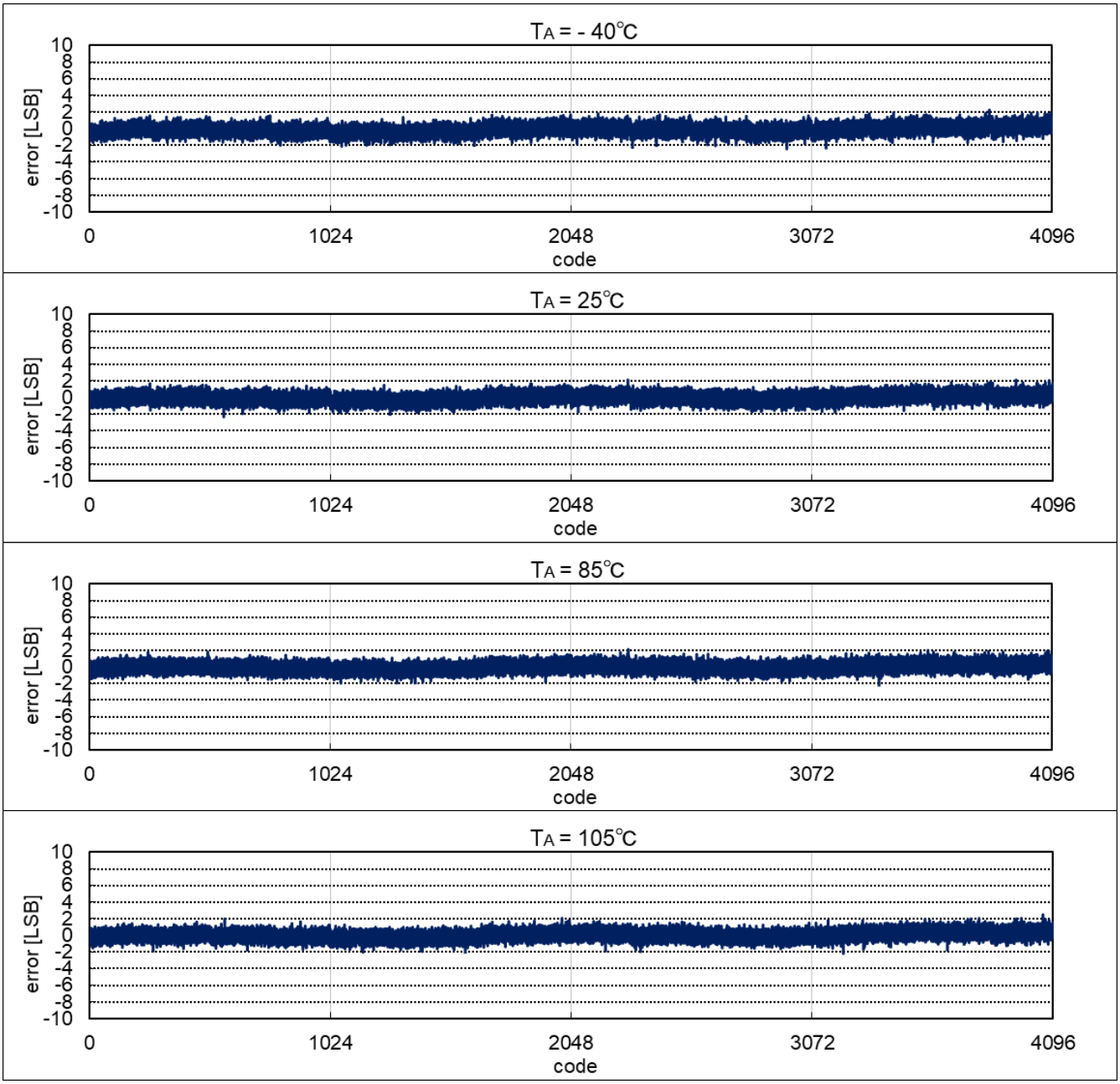

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RL78/G23 group
12-bit A/D converter overall error

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T_A = -40℃, +25℃, +85℃, +105℃
CPU : HS mode, HALT
f_{CLK} = 32 MHz (High-speed OCO)

reference voltage (+) = AV_{REFP} = 3.0 V, reference voltage (-) = AV_{REFM} = 0 V
f_{AD} = 32 MHz
conversion time = 2.0625 μs
mode : Normal mode 1

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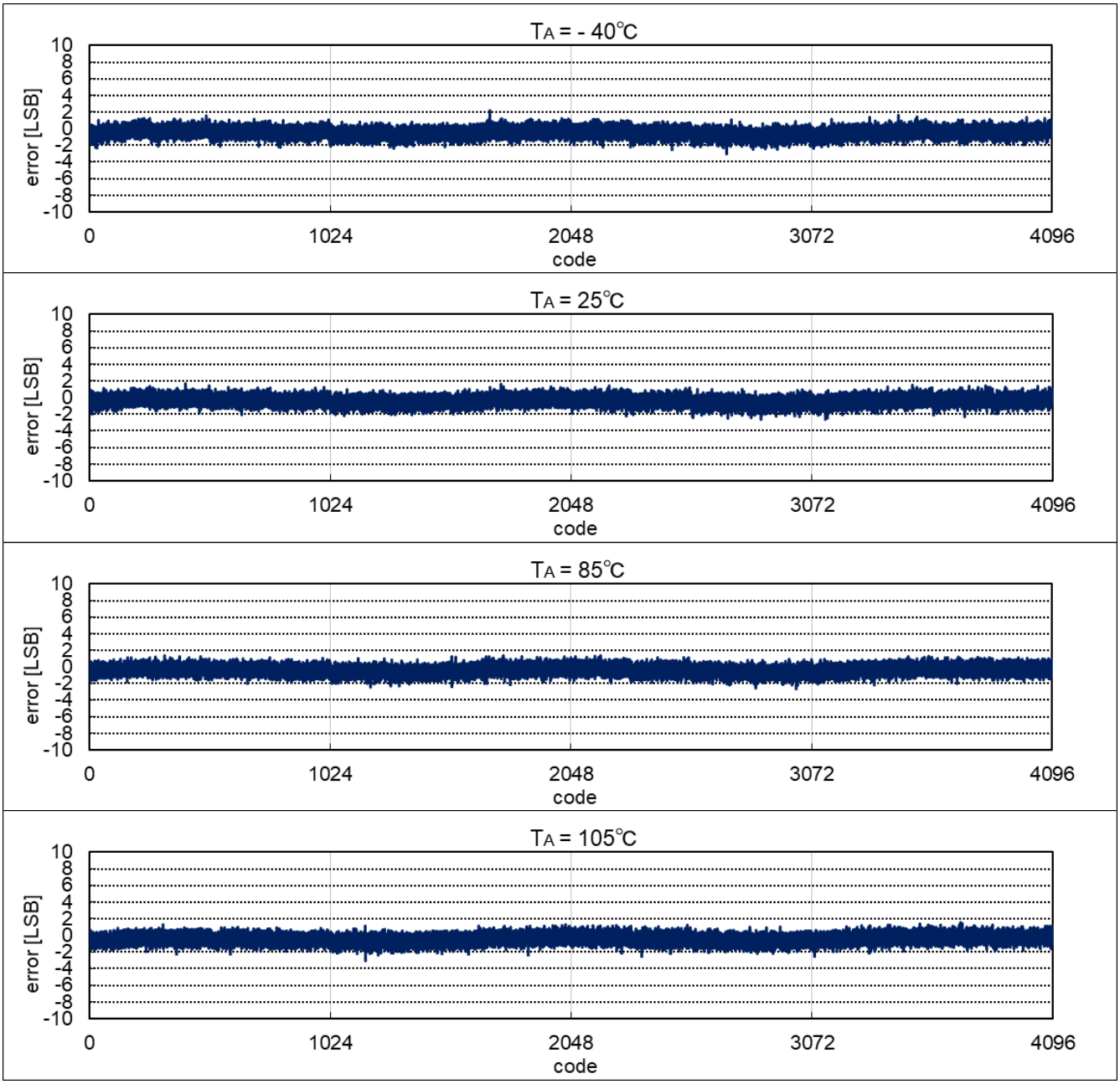

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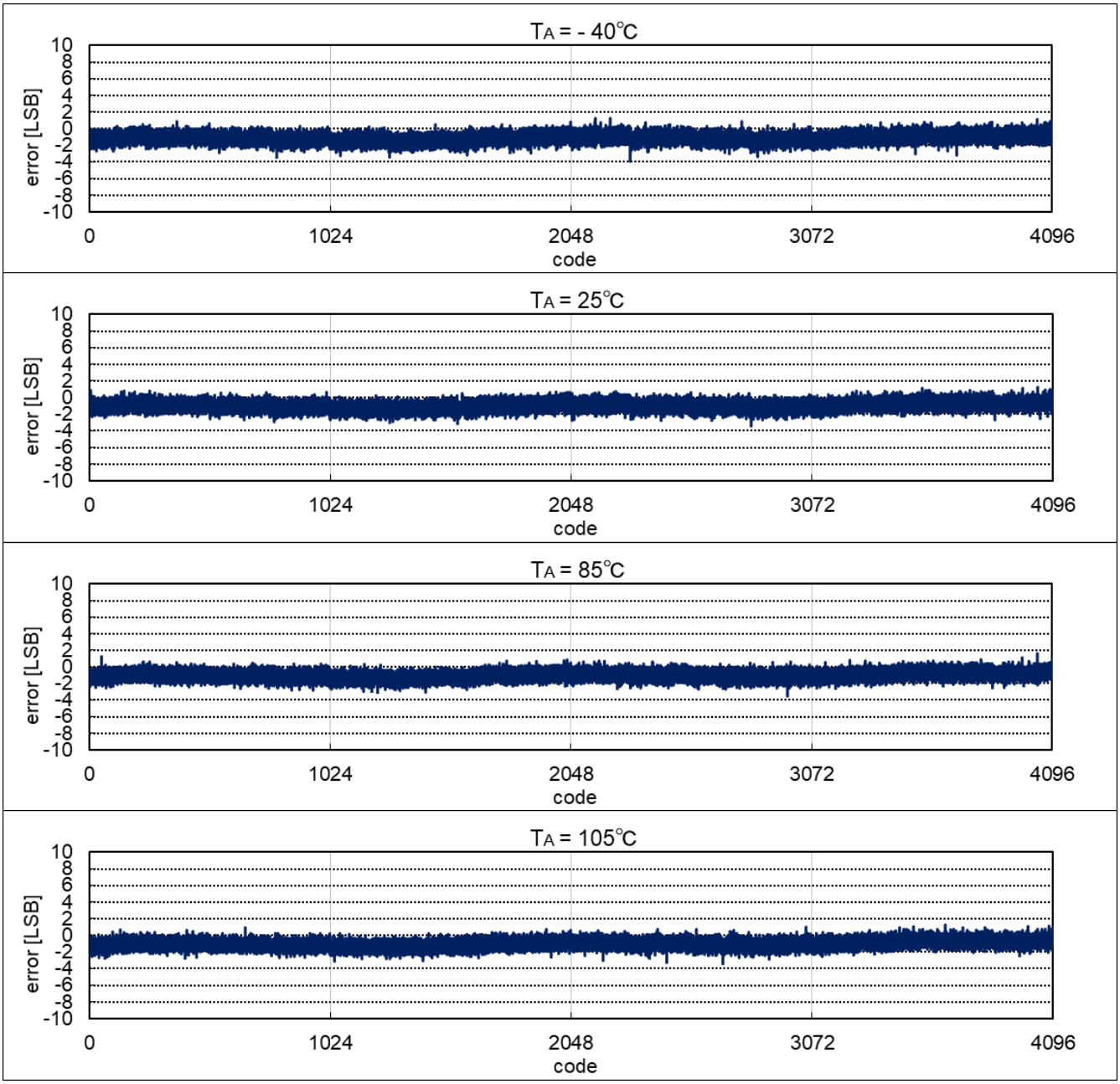
RL78/G23 group
12-bit A/D converter overall error

V_{DD} = EV_{DD0} = EV_{DD1} = 3.0 V
T_A = -40℃, +25℃, +85℃, +105℃
CPU : LS mode, RUN
f_{CLK} = 24 MHz (High-speed OCO)

reference voltage (+) = AV_{REFP} = 3.0 V, reference voltage (-) = AV_{REFM} = 0 V
f_{AD} = 24 MHz
conversion time = 2.75 μs
mode : Normal mode 1

Prepared on January 10th, 2023



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RL78/G23 group
12-bit A/D converter overall error

V_{DD} = EV_{DD0} = EV_{DD1} = 3.0 V
T_A = -40℃, +25℃, +85℃, +105℃
CPU : LS mode, HALT
f_{CLK} = 24 MHz (High-speed OCO)

reference voltage (+) = AV_{REFP} = 3.0 V, reference voltage (-) = AV_{REFM} = 0 V
f_{AD} = 24 MHz
conversion time = 2.75 μs
mode : Normal mode 1

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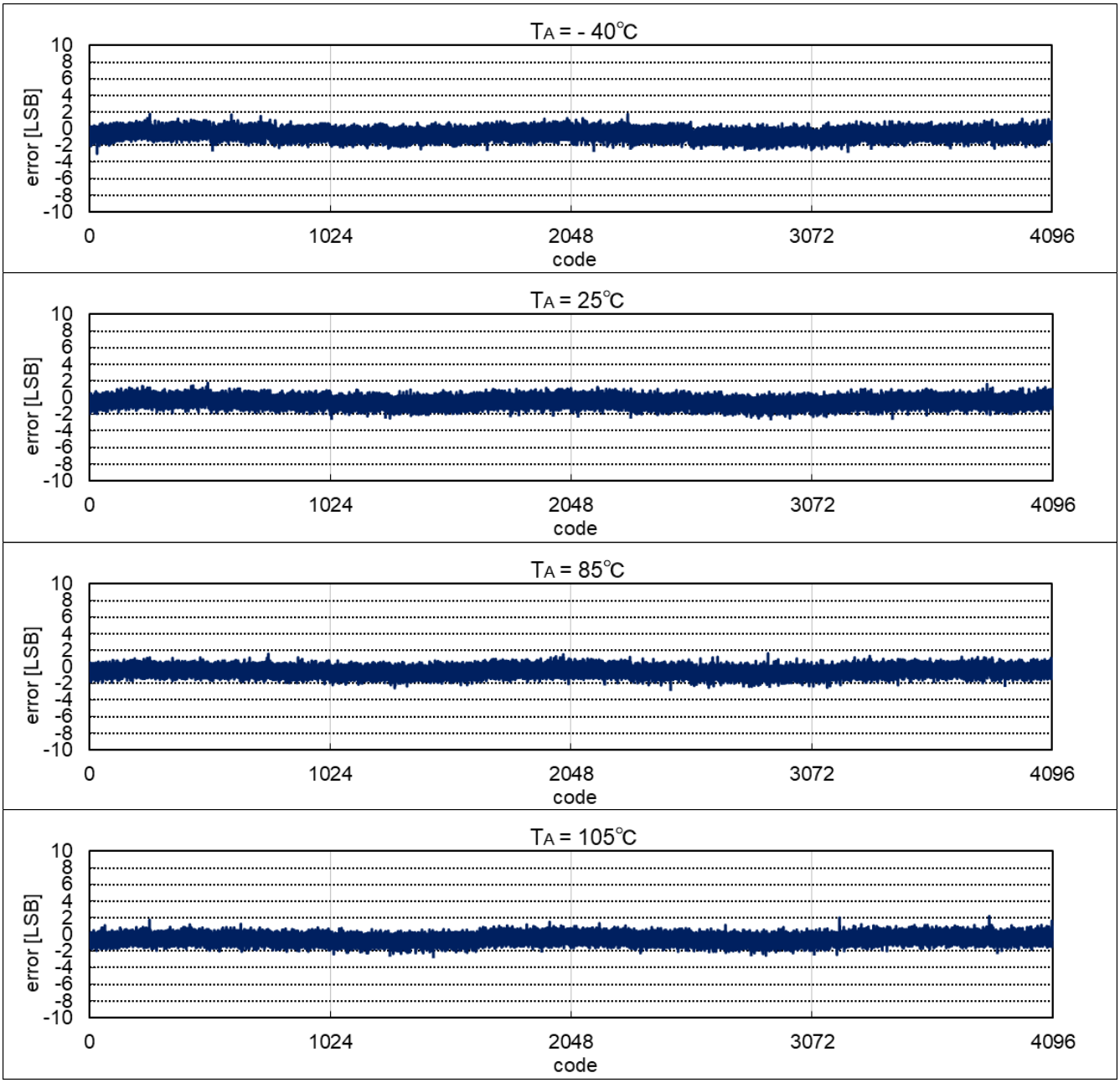

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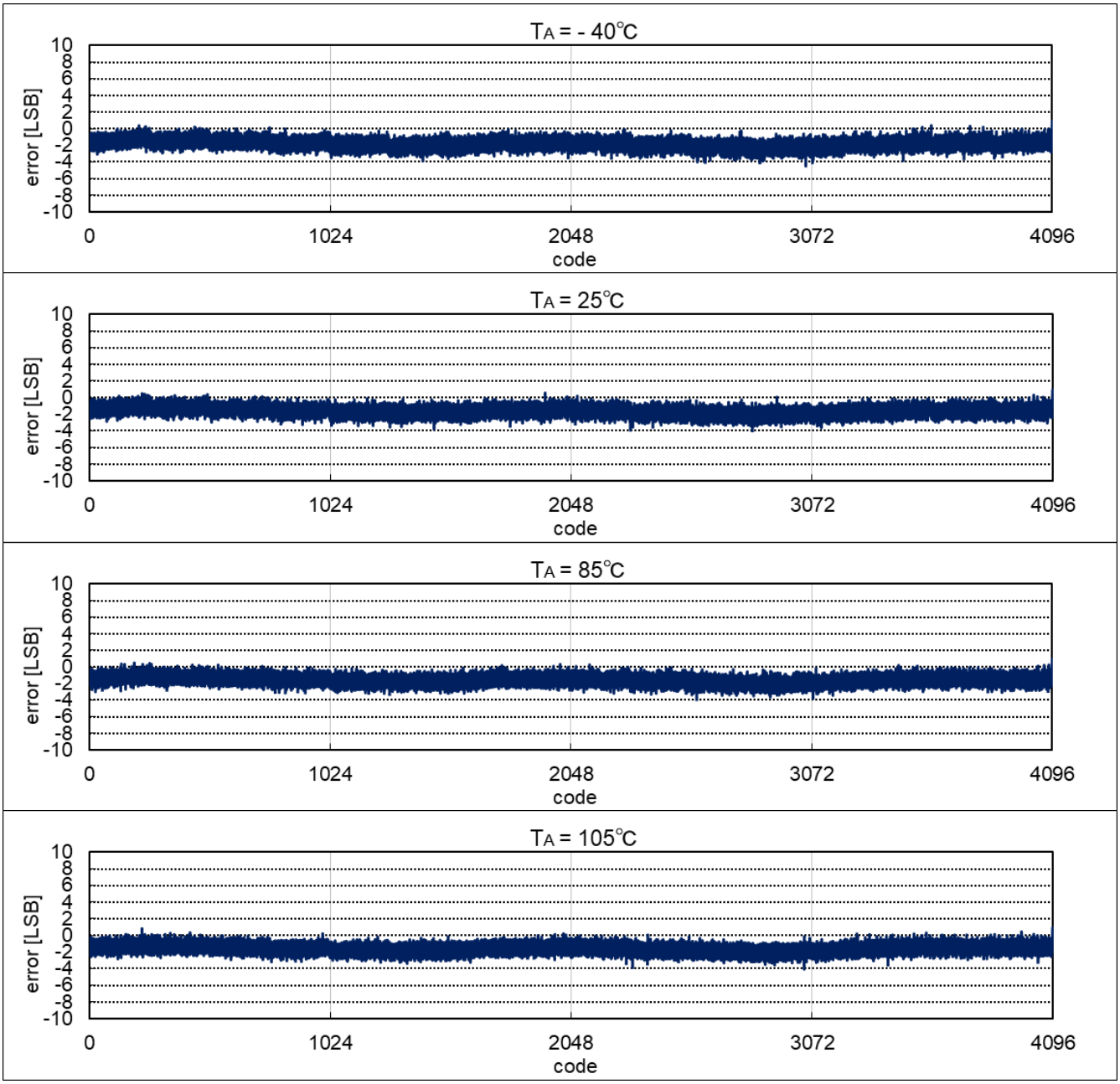
RL78/G23 group
12-bit A/D converter overall error

V_{DD} = EV_{DD0} = EV_{DD1} = 3.0 V
T_A = -40℃, +25℃, +85℃, +105℃
CPU : LS mode, RUN
f_{CLK} = 8 MHz (High-speed OCO)

reference voltage (+) = AV_{REFP} = 3.0 V, reference voltage (-) = AV_{REFM} = 0 V
f_{AD} = 8 MHz
conversion time = 8.25 μs
mode : Normal mode 1

Prepared on January 10th, 2023



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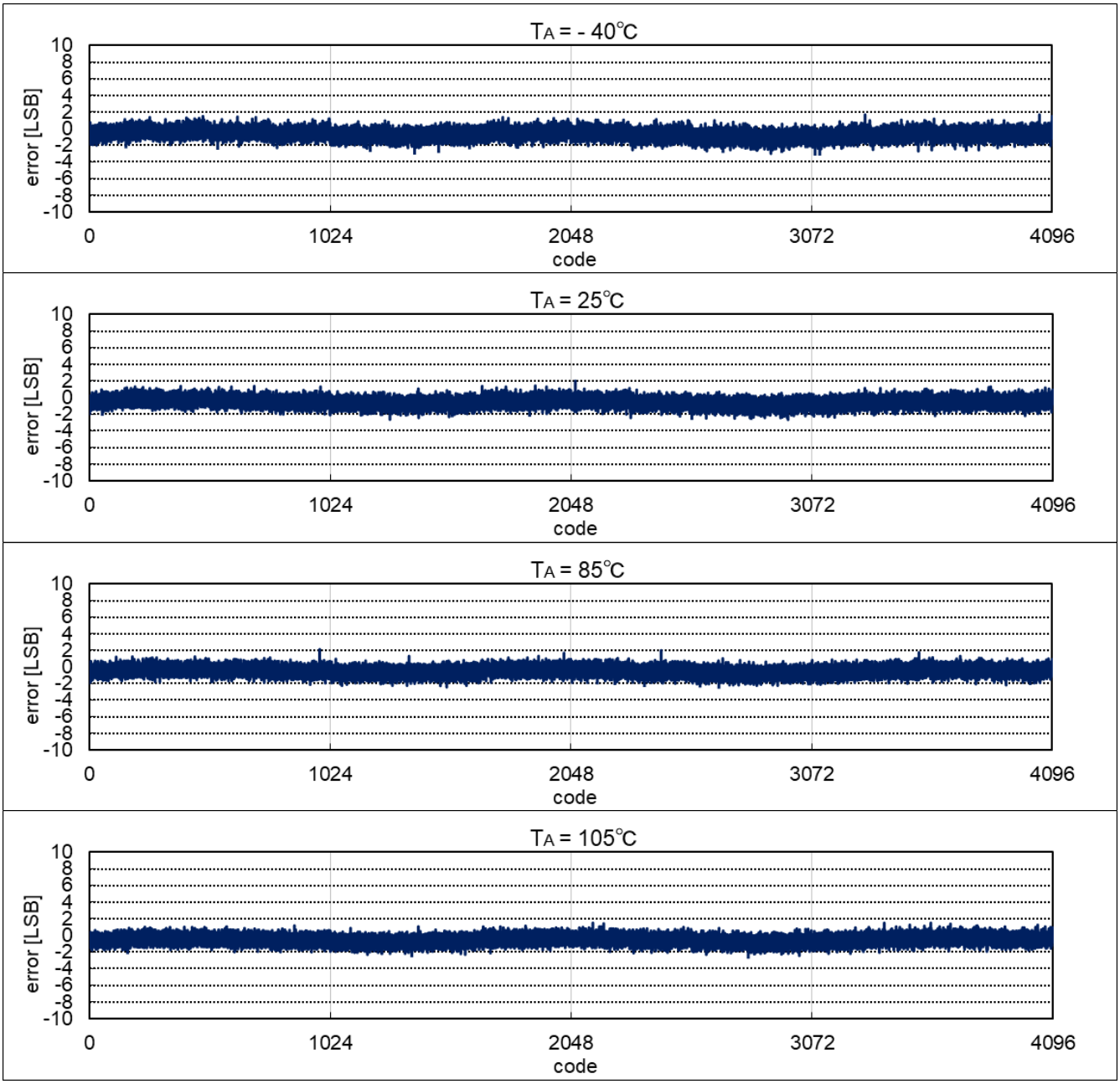
RL78/G23 group
12-bit A/D converter overall error

V_{DD} = EV_{DD0} = EV_{DD1} = 3.0 V
T_A = -40℃, +25℃, +85℃, +105℃
CPU : LS mode, HALT
f_{CLK} = 8 MHz (High-speed OCO)

reference voltage (+) = AV_{REFP} = 3.0 V, reference voltage (-) = AV_{REFM} = 0 V
f_{AD} = 8 MHz
conversion time = 8.25 μs
mode : Normal mode 1

Prepared on January 10th, 2023



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