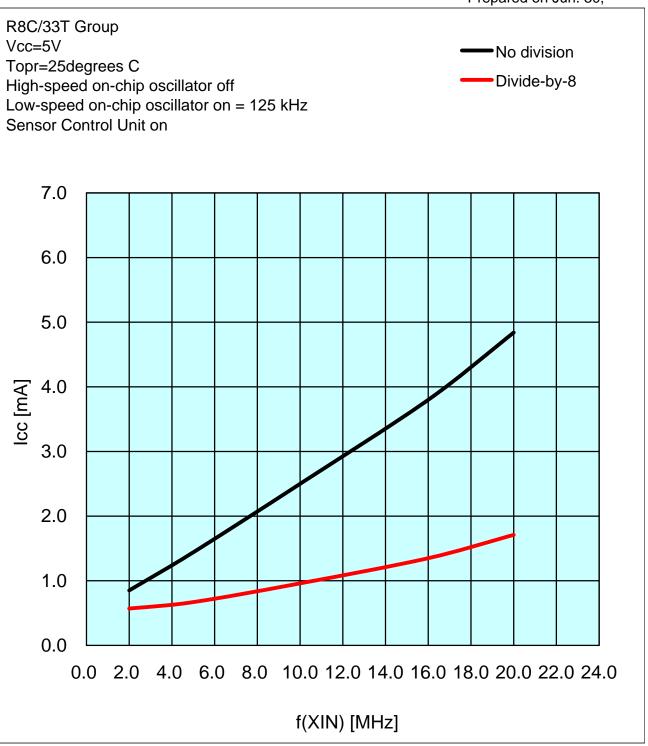
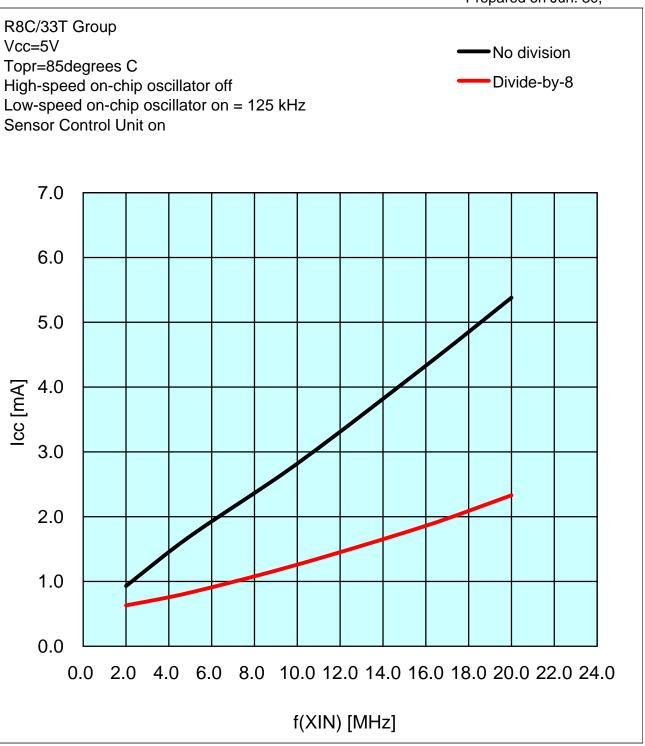
#### R8C/33T Group OCVREF characteristics

Page	Prepared on Jun. 30, 2010
1 <u>lcc VS f(XIN) (High-speed clock mode) Topr= 25 degrees C Vcc=5V</u>	
2 Icc VS f(XIN) (High-speed clock mode) Topr= 85 degrees C Vcc=5V	
3 <u>Icc VS f(XIN) (High-speed clock mode) Topr= -40 degrees C Vcc=5V</u>	
4 <u>Icc VS f(XIN) (High-speed clock mode) Topr= 25 degrees C Vcc=3V</u>	
5 Icc VS f(XIN) (High-speed clock mode) Topr= 85 degrees C Vcc=3V	
6 <u>lcc VS f(XIN) (High-speed clock mode) Topr= -40 degrees C Vcc=3V</u>	
7 Icc VS f(XIN) (High-speed clock mode) Topr= 25 degrees C Vcc=1.8V	
8 Icc VS f(XIN) (High-speed clock mode) Topr= 85 degrees C Vcc=1.8V	
9 <u>Icc VS f(XIN) (High-speed clock mode) Topr= -40 degrees C Vcc=1.8V</u>	
10 Icc VS Topr (Low-speed on-chip oscillator mode)	
11 <u>Icc VS Topr (Stop mode)</u>	
12 Icc VS Topr (Low-speed on-chip oscillator wait mode) Peripheral clock oper	<u>ation</u>
13 <u>lcc VS Topr (Low-speed on-chip oscillator wait mode) Peripheral clock off</u>	
14 Icc VS Topr (Xin wait mode) SCU on	
15 Icc VS Topr (High-speed on-chip oscillator wait mode) SCU on	
16 Icc VS Topr (High-speed clock mode) Vcc=5V	
17 Icc VS Topr (High-speed clock mode) Vcc=3V	
18 Icc VS Topr (High-speed clock mode) Vcc=1.8V	
19 Icc VS Topr (High-speed on-chip oscillator mode) Vcc=5V	
20 Icc VS Topr (High-speed on-chip oscillator mode) Vcc=3V	
21 Icc VS Topr (High-speed on-chip oscillator mode) Vcc=1.8V	
22 Icc VS Vcc (Low-speed on-chip oscillator mode)	
23 Icc VS Vcc (Stop mode)	
24 Icc VS Vcc (Low-speed on-chip oscillator wait mode) Peripheral clock opera	<u>ition</u>
25 Icc VS Vcc (Low-speed on-chip oscillator wait mode) Peripheral clock off	
26 Icc VS Vcc (Xin wait mode) SCU on	
27 Icc VS Vcc (High-speed on-chip oscillator wait mode) SCU on	
28 Icc VS Vcc (High-speed clock mode) XIN = 20MHz No division	
29 Icc VS Vcc (High-speed clock mode) XIN = 20MHz Division-by-8	
30 Icc VS Vcc (High-speed clock mode) XIN = 16MHz No division	
31 Icc VS Vcc (High-speed clock mode) XIN = 16MHz Division-by-8	
32 Icc VS Vcc (High-speed clock mode) XIN = 10MHz No division	
33 Icc VS Vcc (High-speed clock mode) XIN = 10MHz Division-by-8	
34 Icc VS Vcc (High-speed clock mode) XIN = 5MHz No division	
35 <u>lcc VS Vcc (High-speed clock mode) XIN = 5MHz Division-by-8</u> 36 lcc VS Vcc (High-speed on-chip oscillator mode) High-speed on-chip oscillator mode)	stor - 20MHz No division
37 Icc VS Vcc (High-speed on-chip oscillator mode) High-speed on-chip oscillator mode)	
38 Icc VS Vcc (High-speed on-chip oscillator mode) High-speed on-chip oscillator mode)	
39 Icc VS Vcc (High-speed on-chip oscillator mode) High-speed on-chip oscillator mode)	
40 lcc VS Vcc (High-speed on-chip oscillator mode) High-speed on-chip oscillator mode)	
41 lcc VS Vcc (High-speed on-chip oscillator mode) High-speed on-chip oscillator mode)	
42 lcc VS Vcc (High-speed on-chip oscillator mode) High-speed on-chip oscillator mode)	
43 Aicc VS Avcc	ILOI - TIVILIZ DIVISIOII-DY-10
TO MICE VO MVCC	

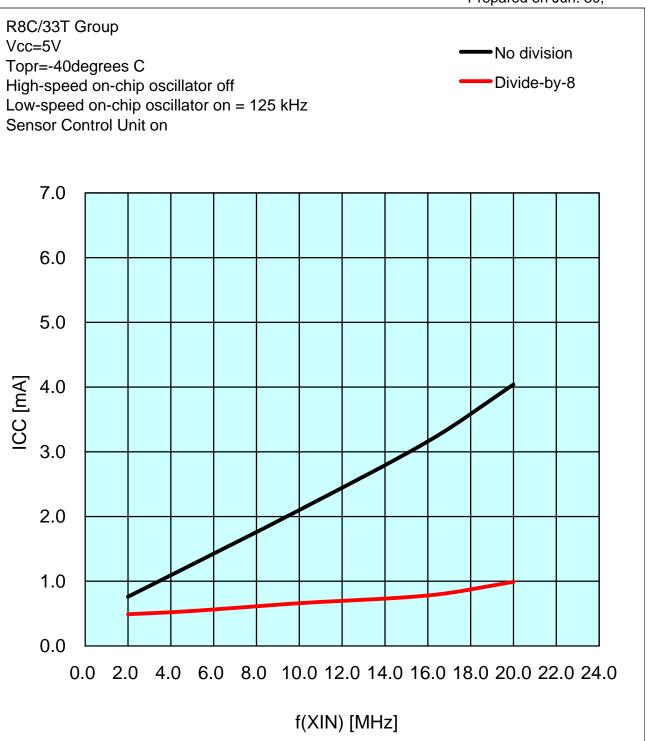
Prepared on Jun. 30,



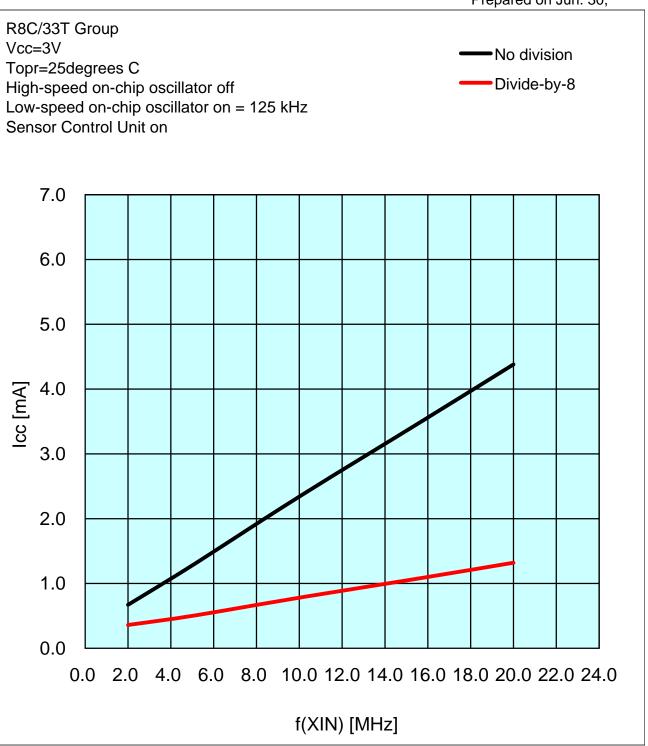
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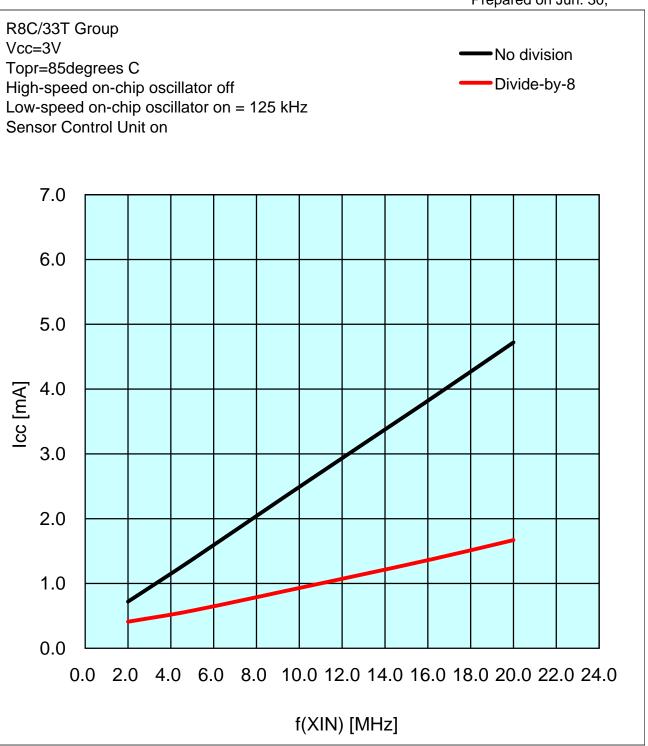
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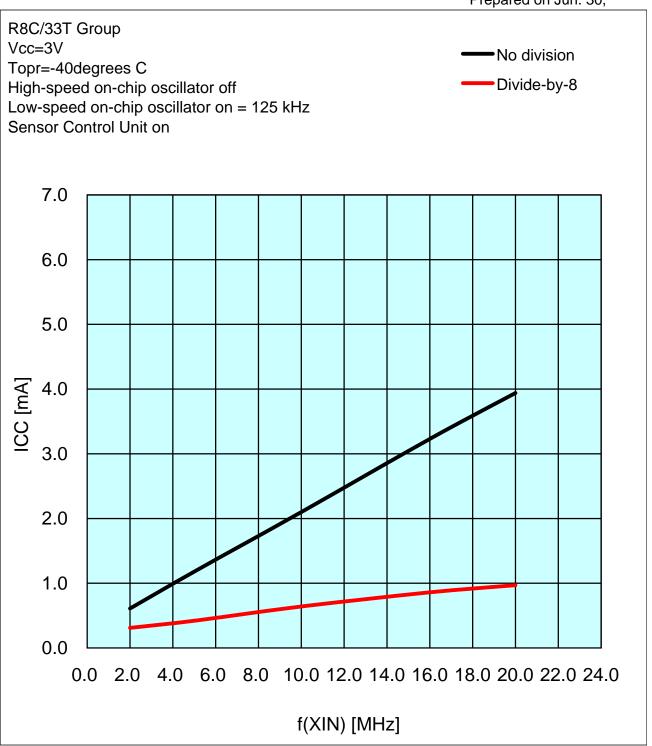
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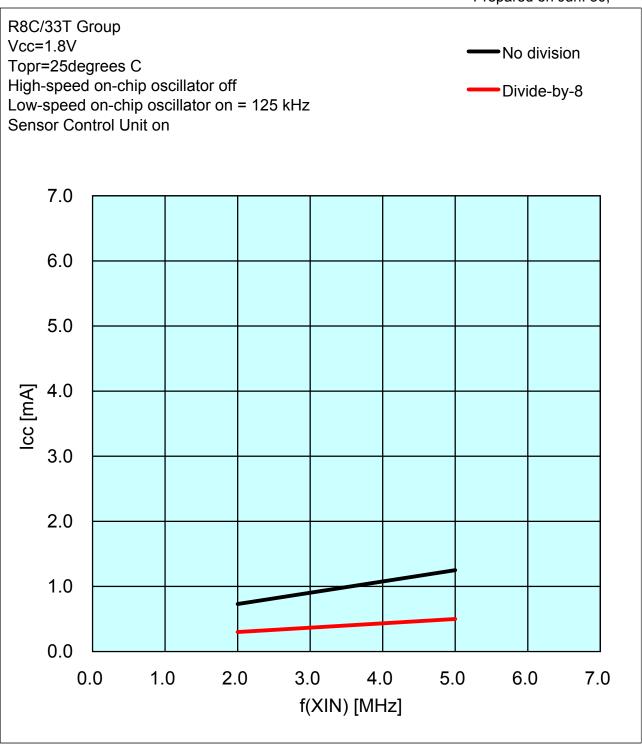
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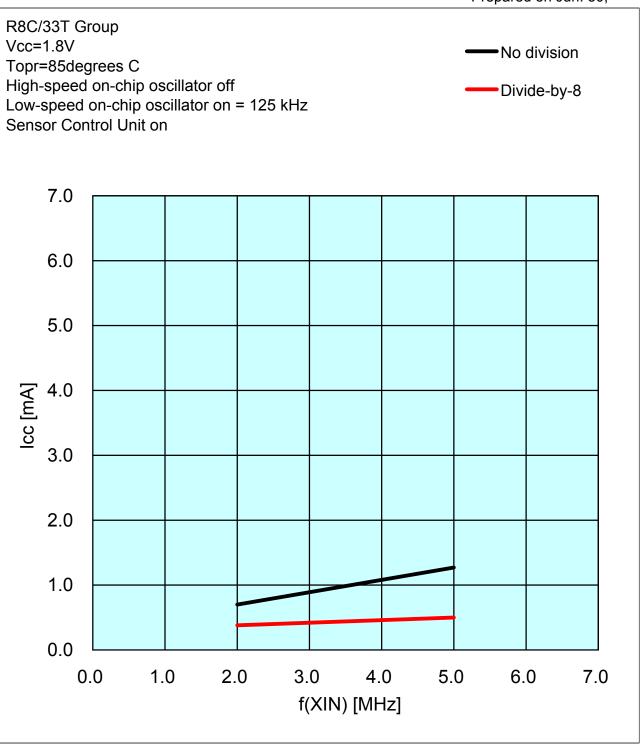
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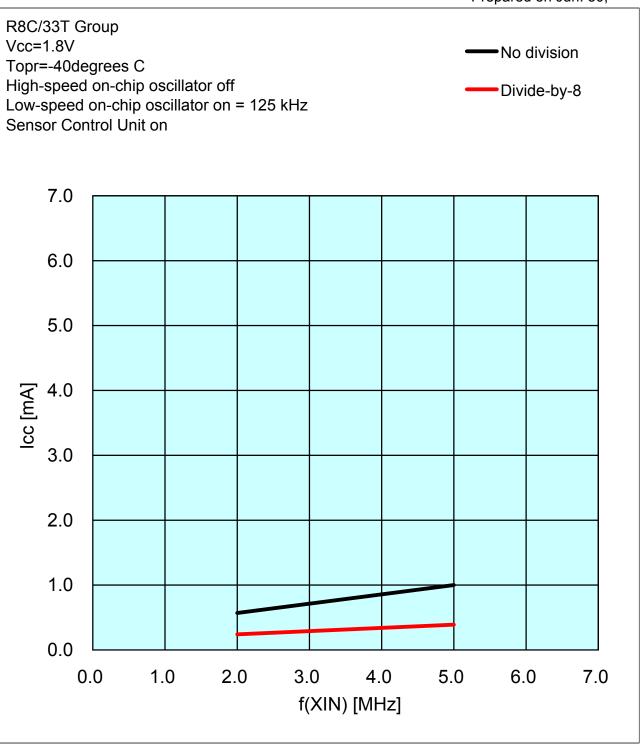
Prepared on Jun. 30,



Prepared on Jun. 30,

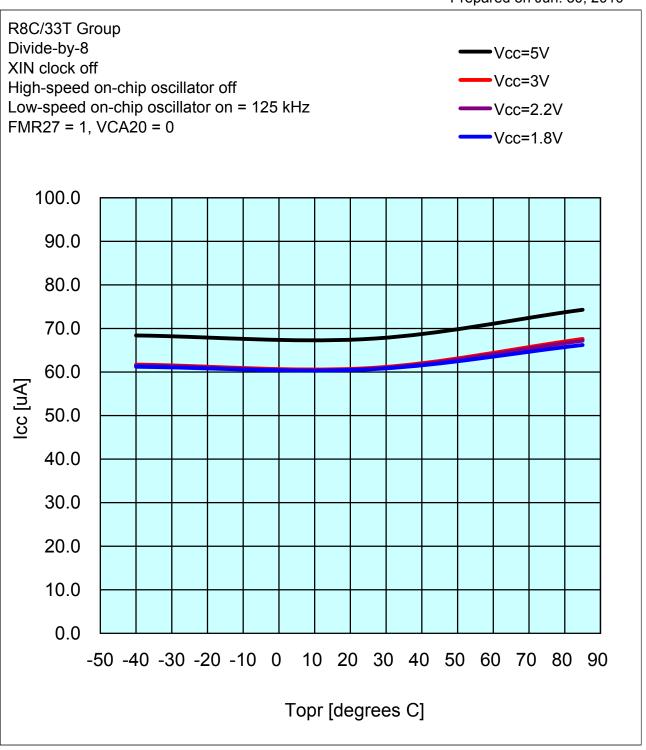


Prepared on Jun. 30,



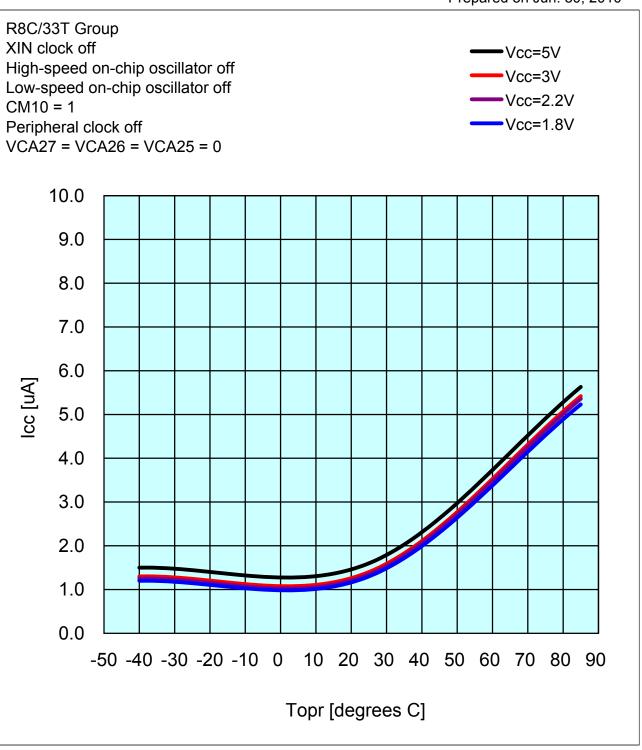
#### ICC VS Topr (Low-Speed On-Chip Oscillator mode)

Prepared on Jun. 30, 2010

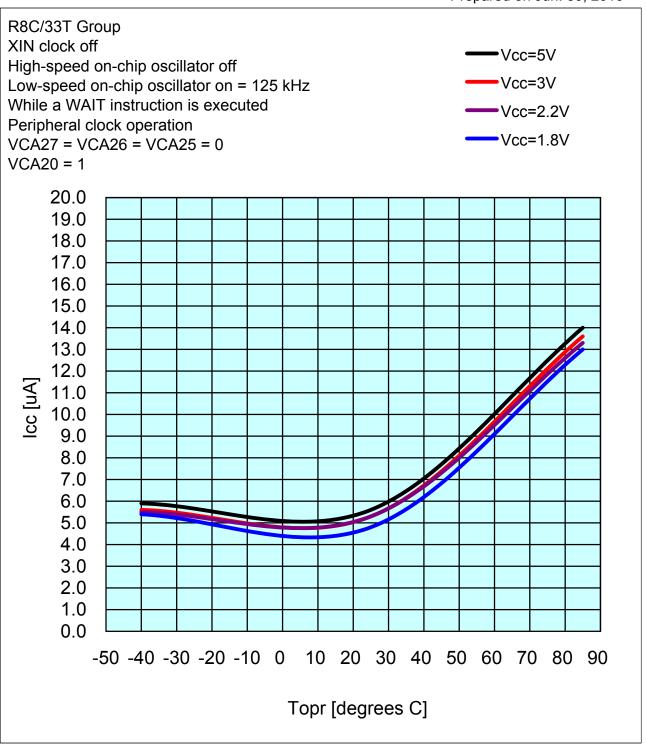


# lcc vs Topr (Stop mode)

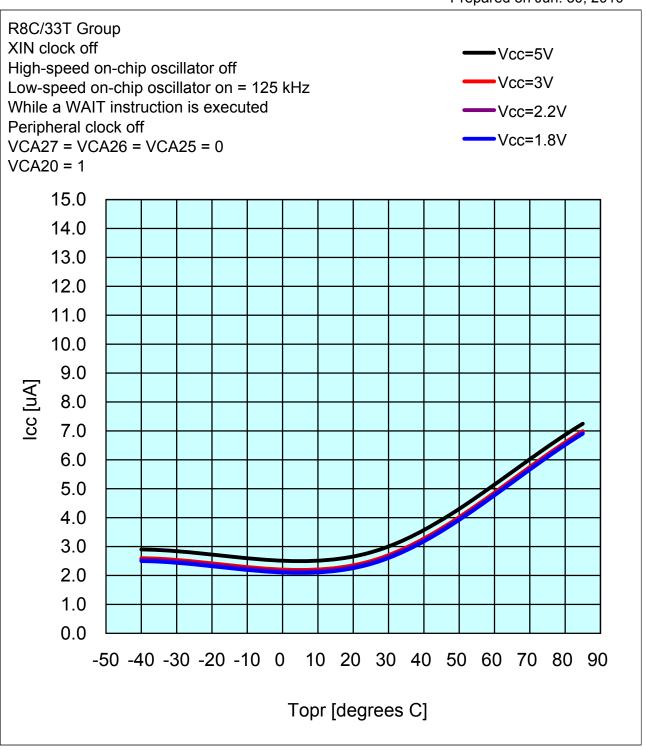
Prepared on Jun. 30, 2010



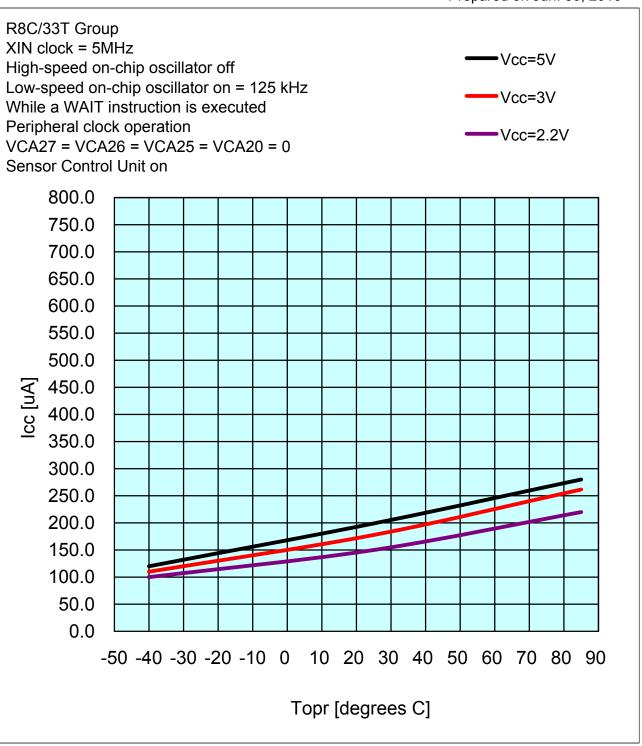
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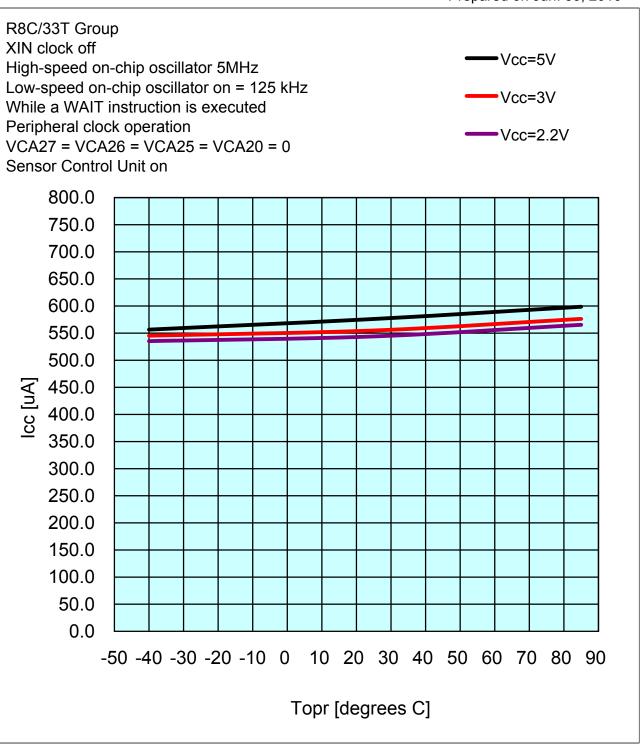
Prepared on Jun. 30, 2010



Prepared on Jun. 30, 2010

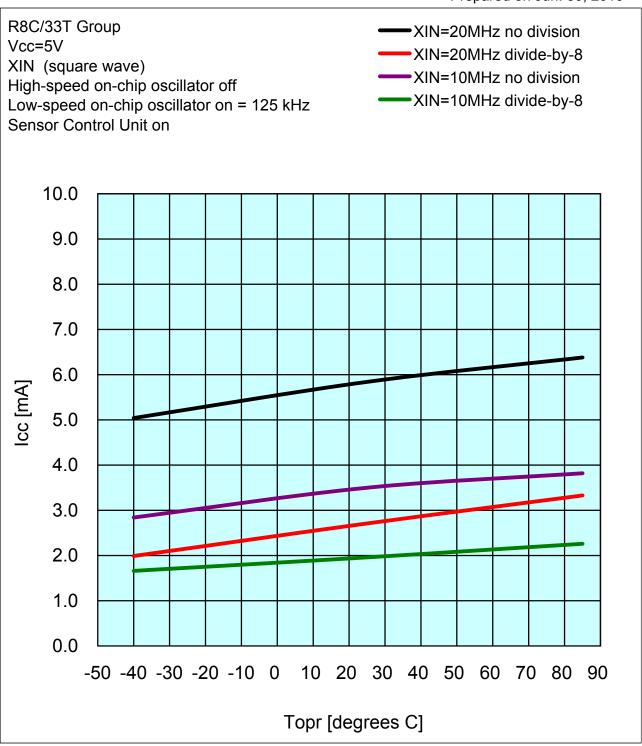


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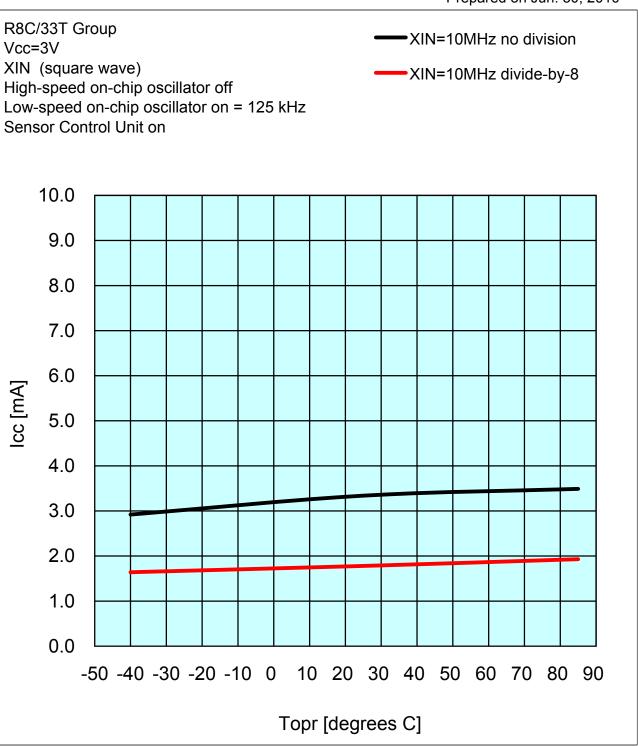
## Icc VS Topr (High-speed clock mode)

Prepared on Jun. 30, 2010



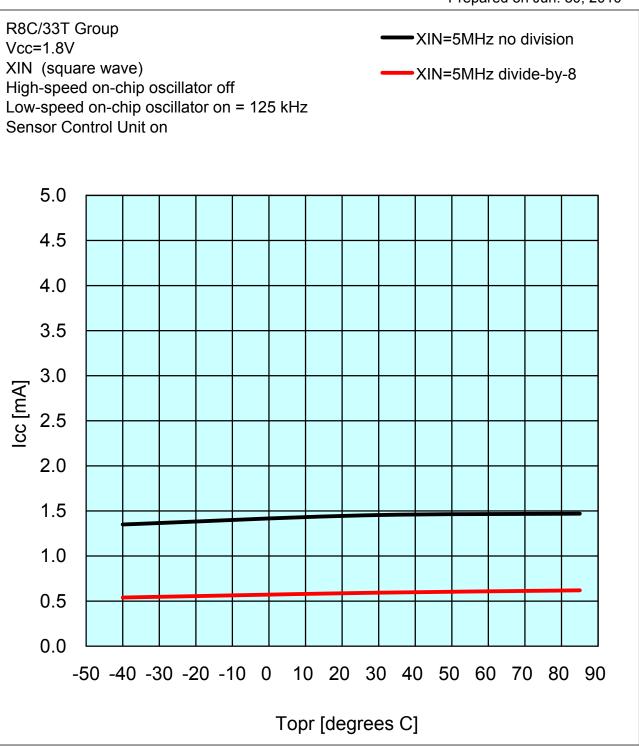
#### Icc VS Topr (High-speed clock mode)

Prepared on Jun. 30, 2010



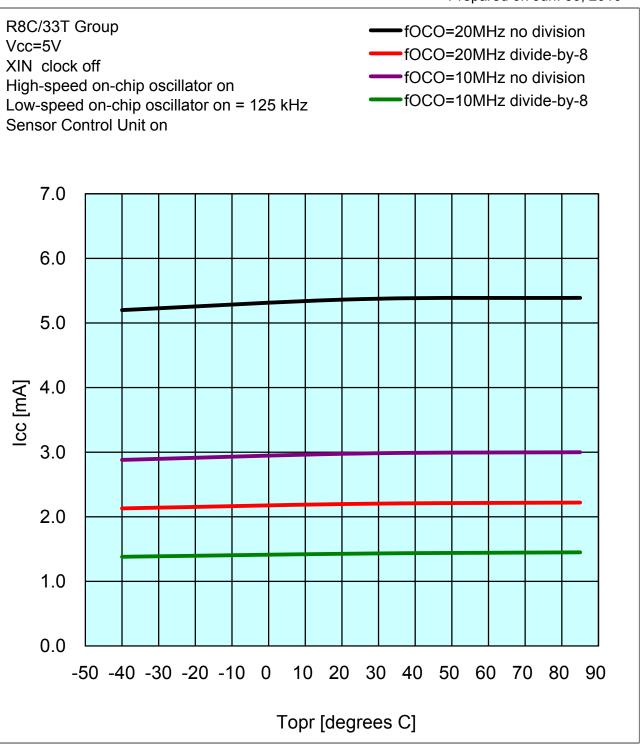
#### Icc VS Topr (High-speed clock mode)

Prepared on Jun. 30, 2010



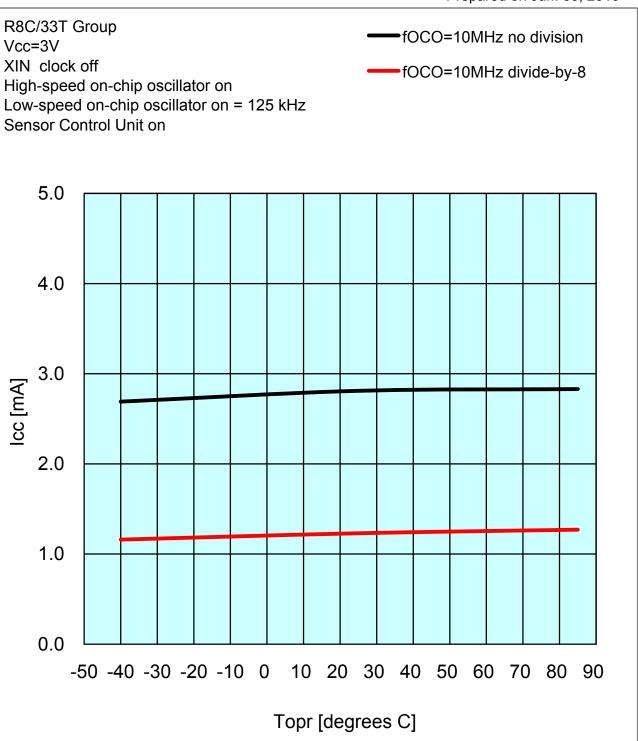
#### Icc VS Topr (High-speed on-chip oscillator mode)

Prepared on Jun. 30, 2010

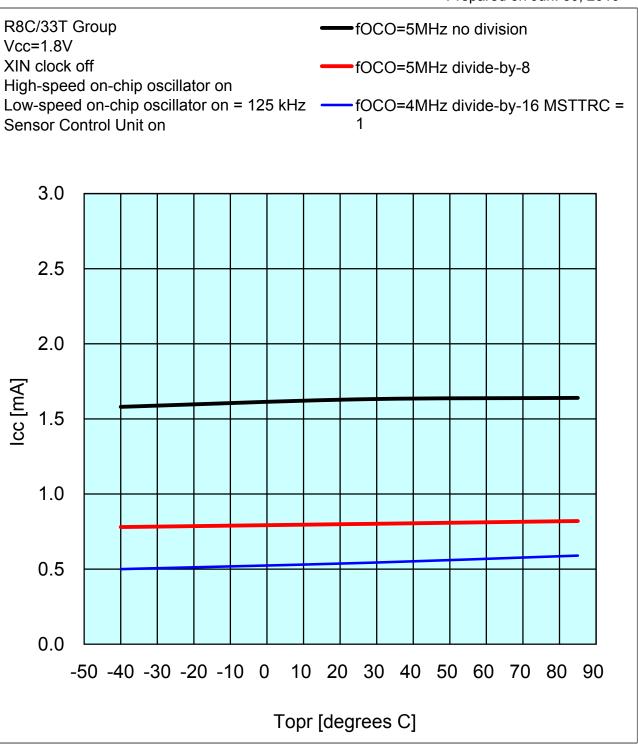


#### Icc VS Topr (High-speed on-chip oscillator mode)

Prepared on Jun. 30, 2010

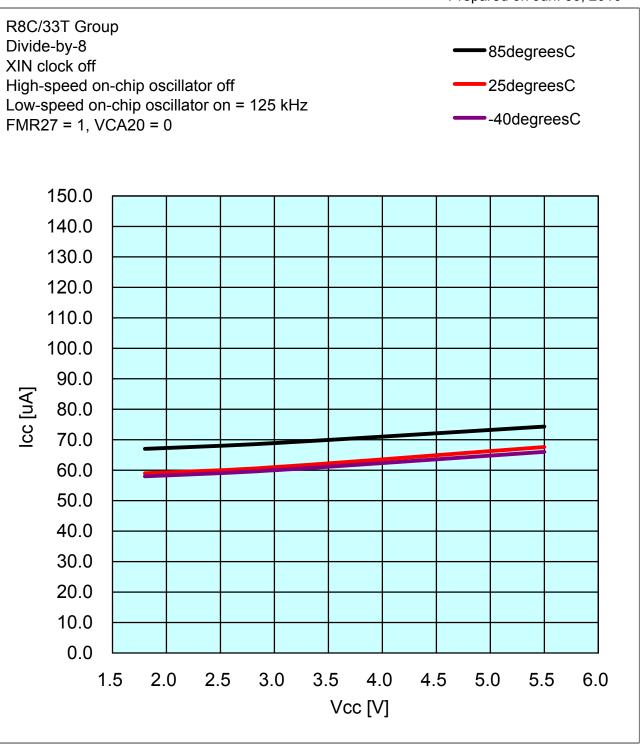


Prepared on Jun. 30, 2010



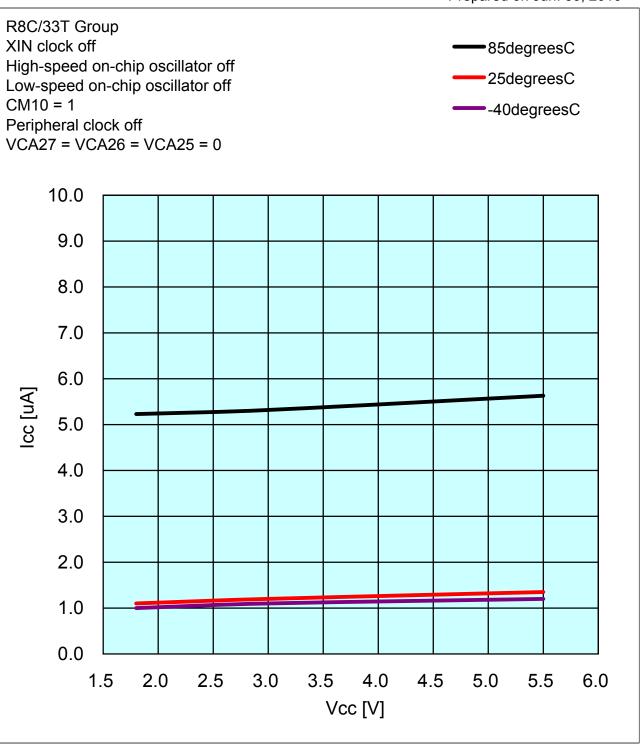
# ICC VS VCC (Low-Speed On-Chip Oscillator mode)

Prepared on Jun. 30, 2010

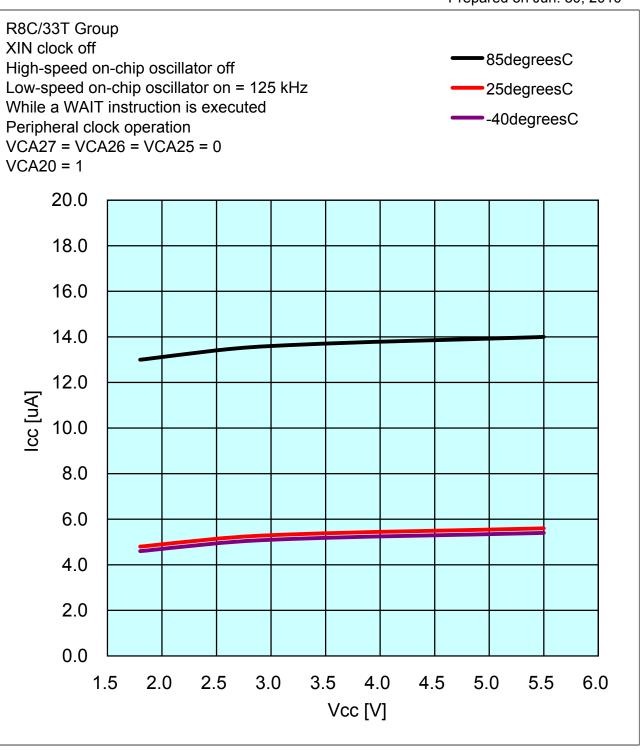


# ICC VS VCC (Stop mode)

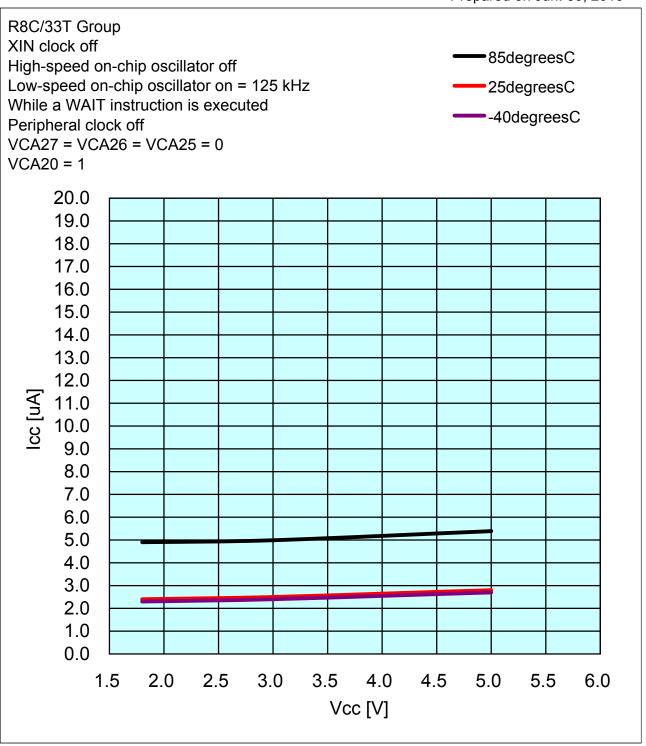
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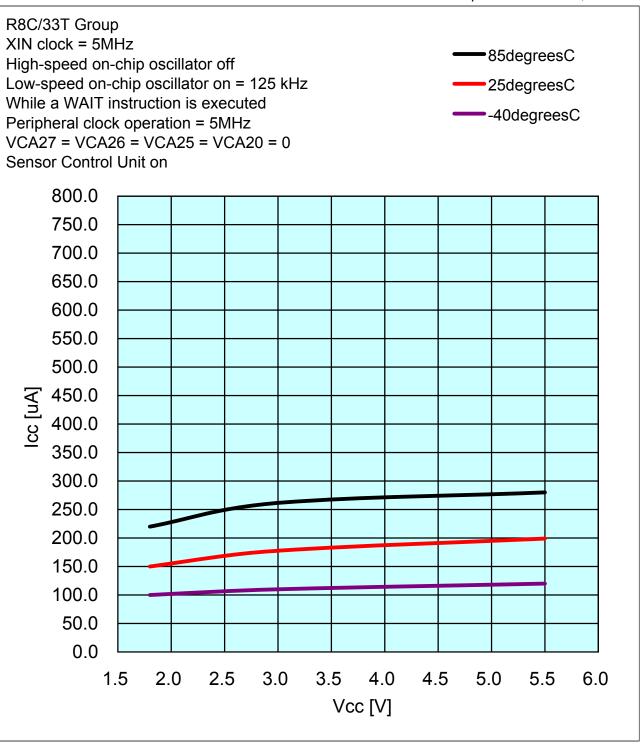
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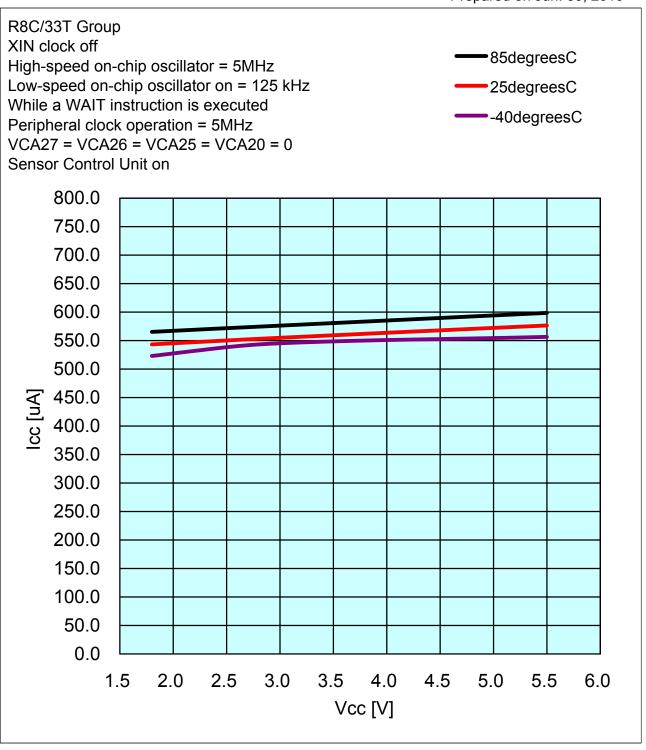
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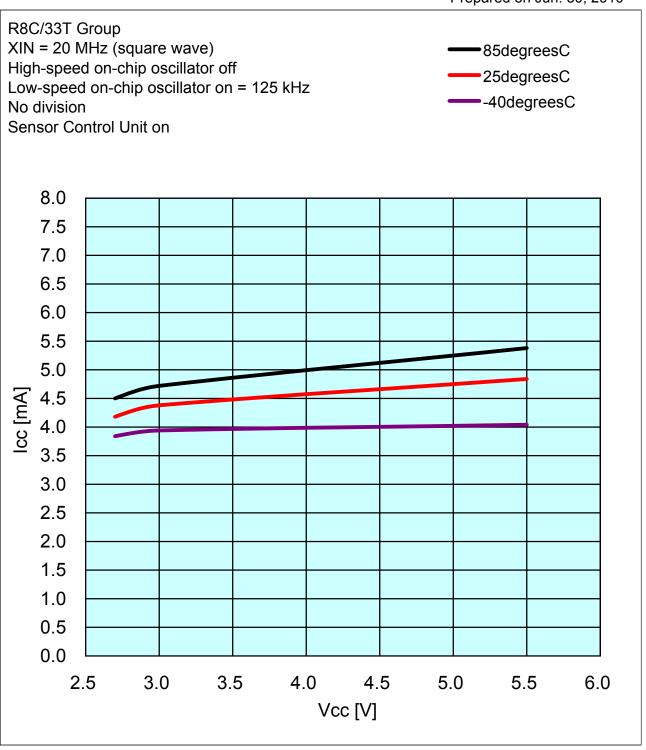
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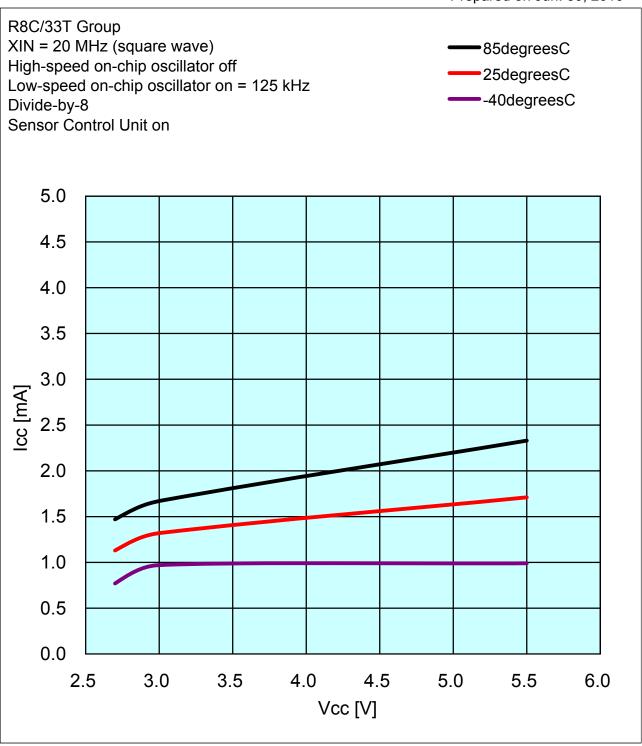
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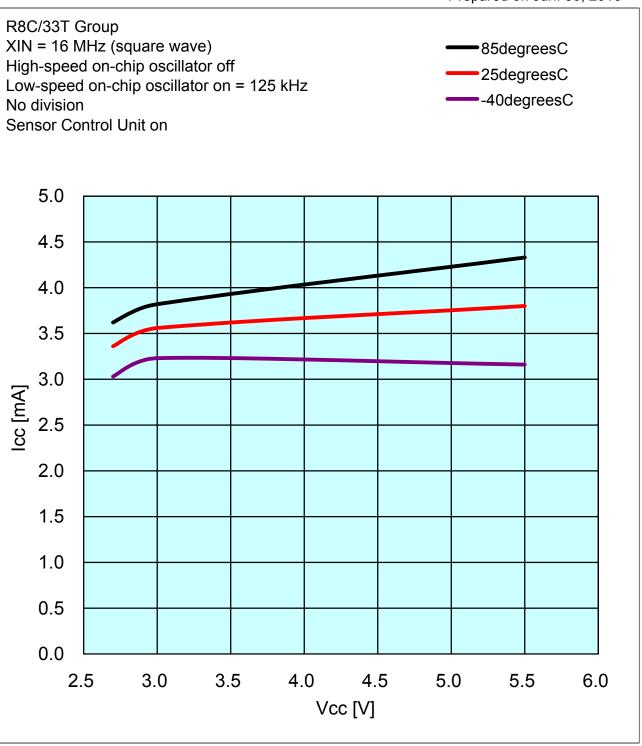
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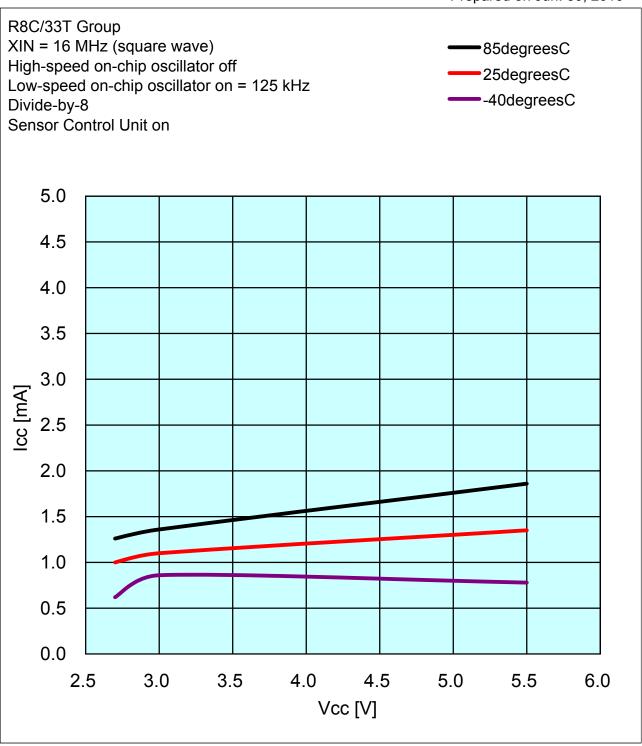
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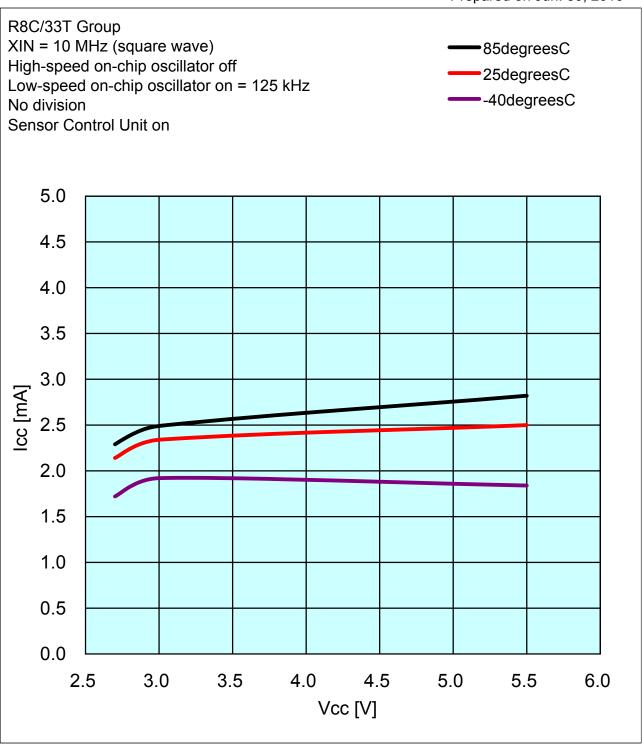
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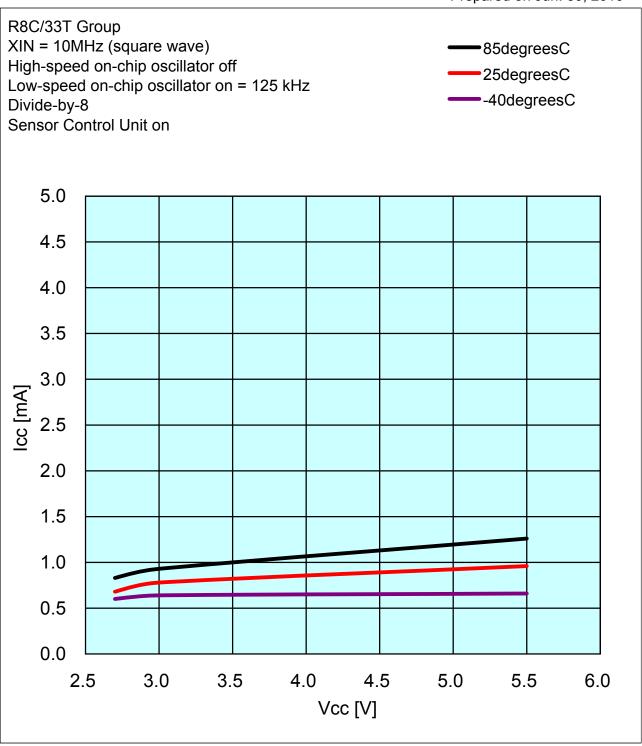
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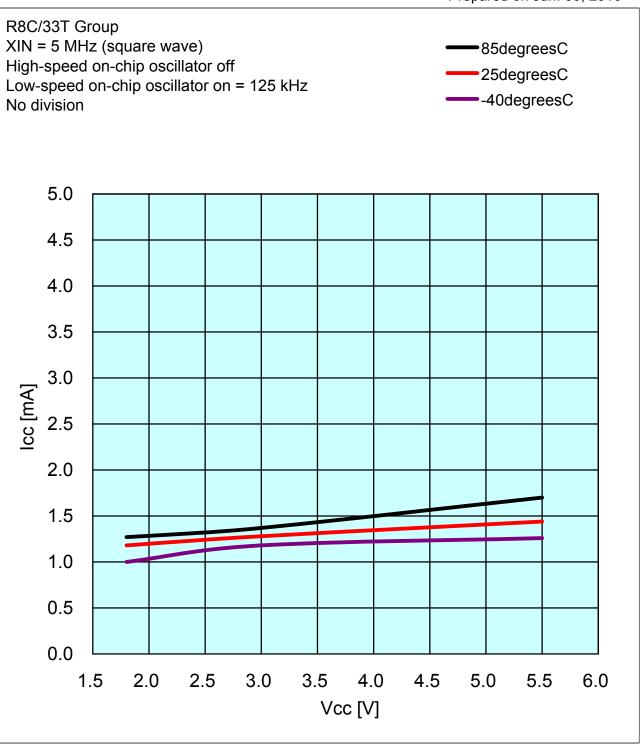
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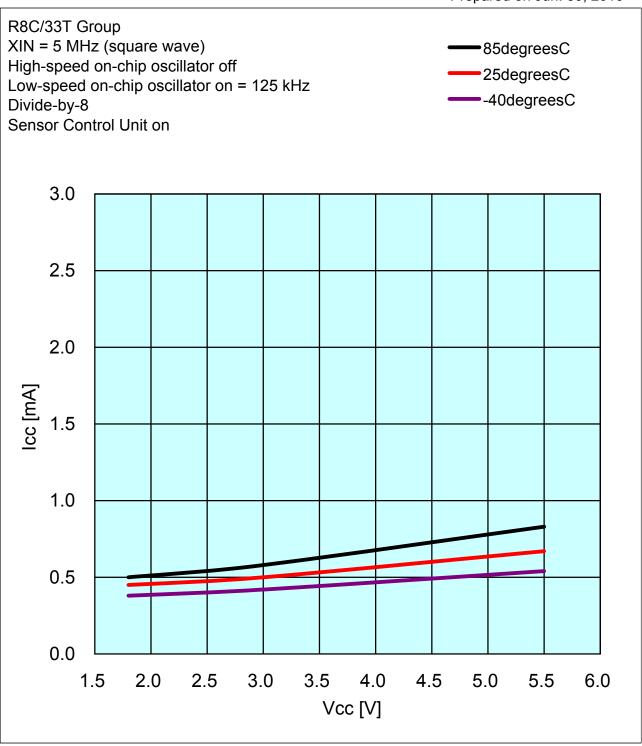
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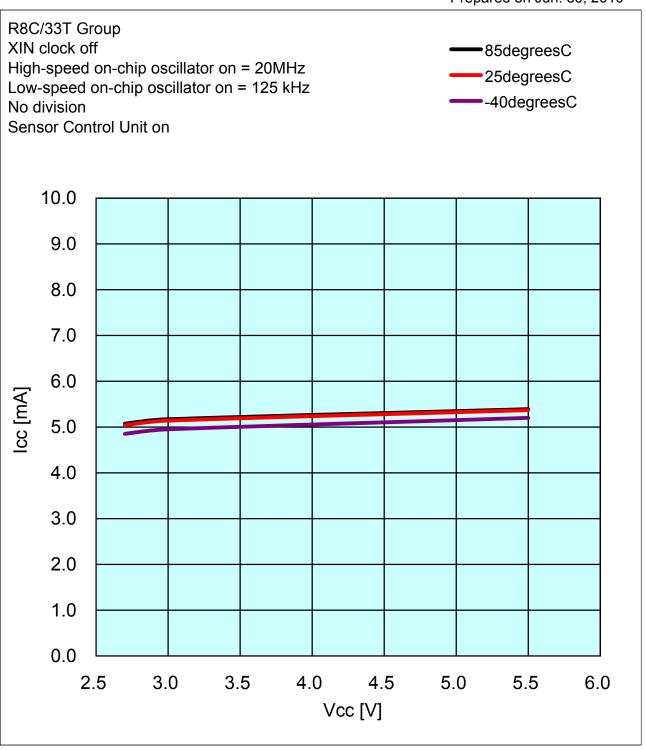
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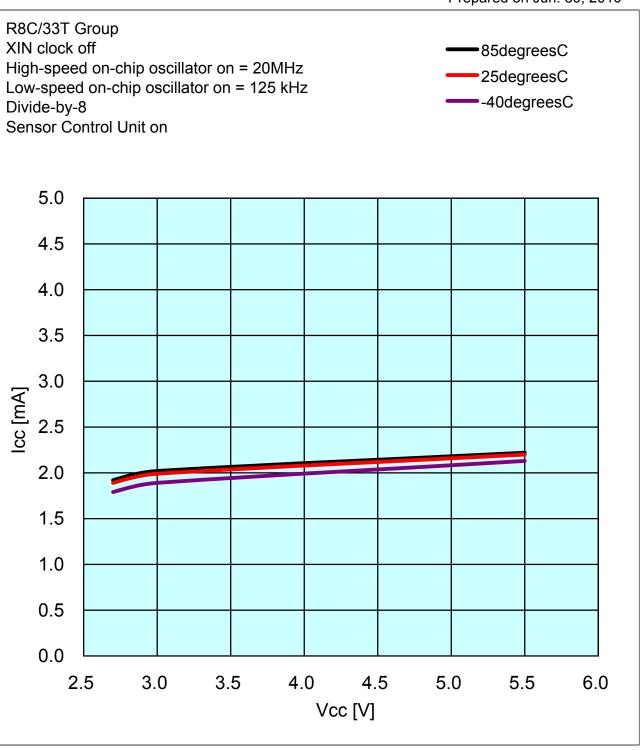
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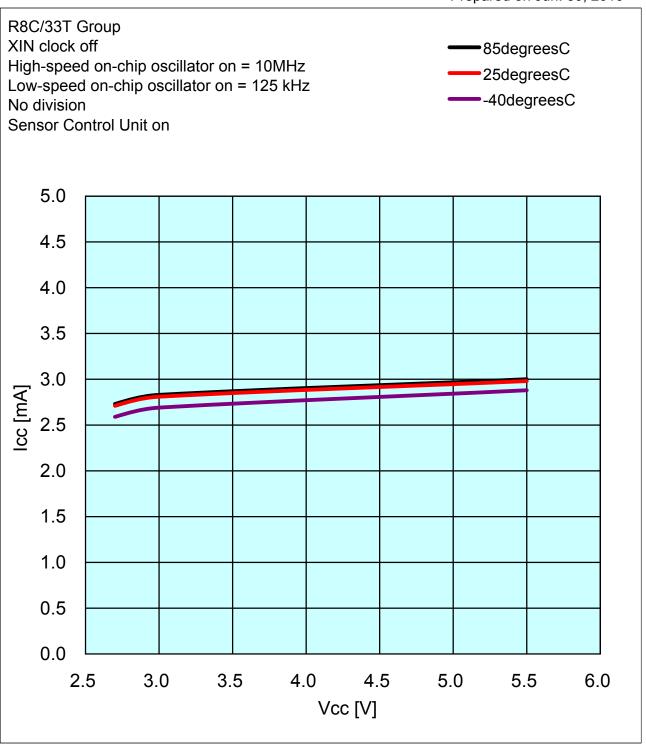
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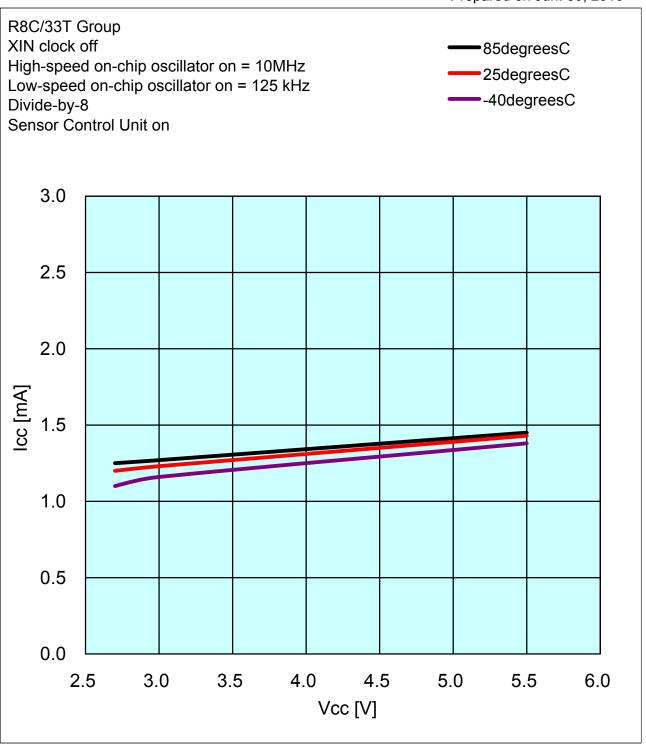
Prepared on Jun. 30, 2010



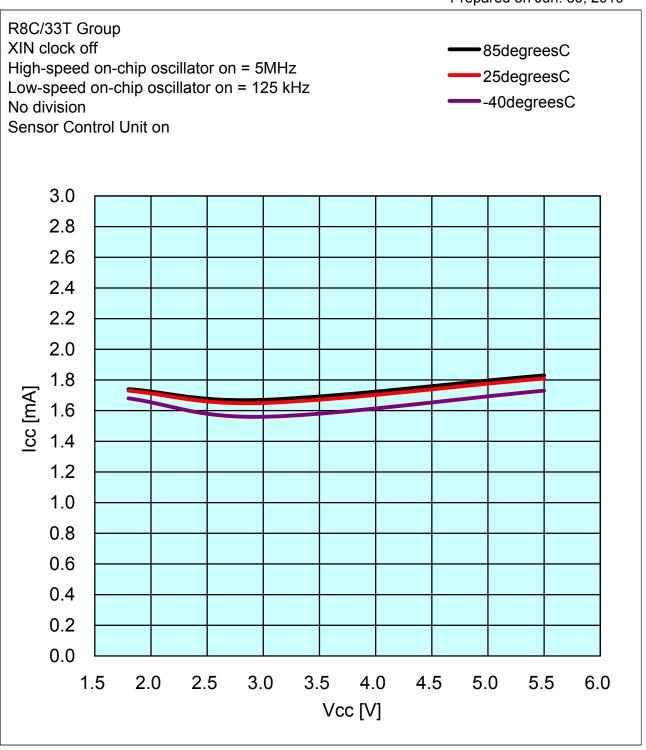
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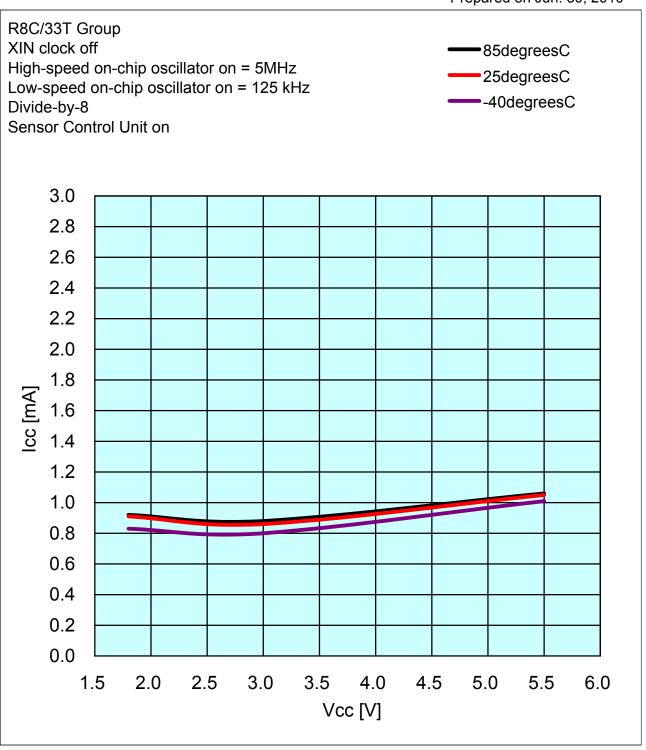
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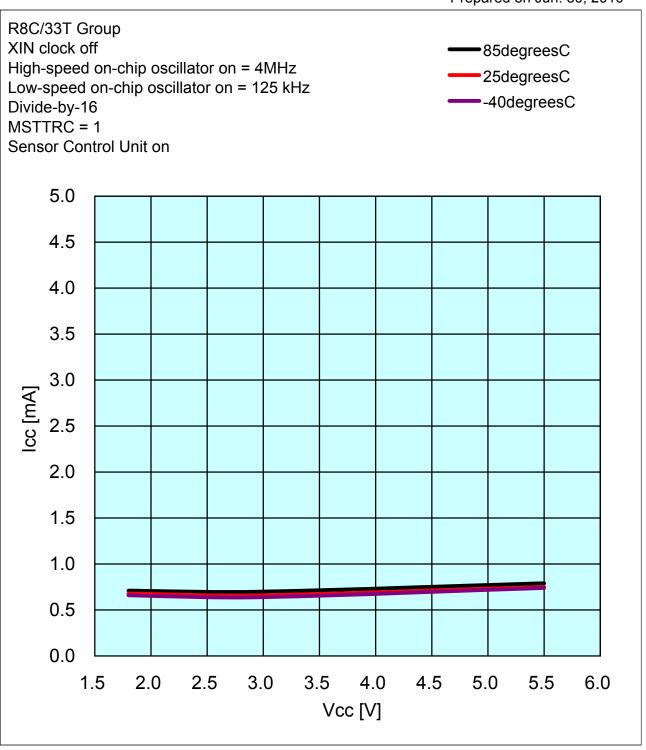
Prepared on Jun. 30, 2010



Prepared on Jun. 30, 2010



Prepared on Jun. 30, 2010



#### Alcc vs AVcc

(during A/D conversion)

Prepared on Jun. 30, 2010

