

Certify it! Functional Safety

RX63N/631 Microcontroller Series



To enable a seamless integration into safety applications, Renesas introduces a certification package for the RX63N/631 microcontroller series. It includes a safety manual containing the results from a comprehensive analysis based on each function part of the microcontroller, and all relevant information and procedures related to functional safety. A diagnostic software library contains all necessary self-tests for the CPU core, for RAM and ROM. Since the diagnostic coverage of the CPU core is already proven by fault simulation tests, an effective system integration is enabled and system development time is shortened.



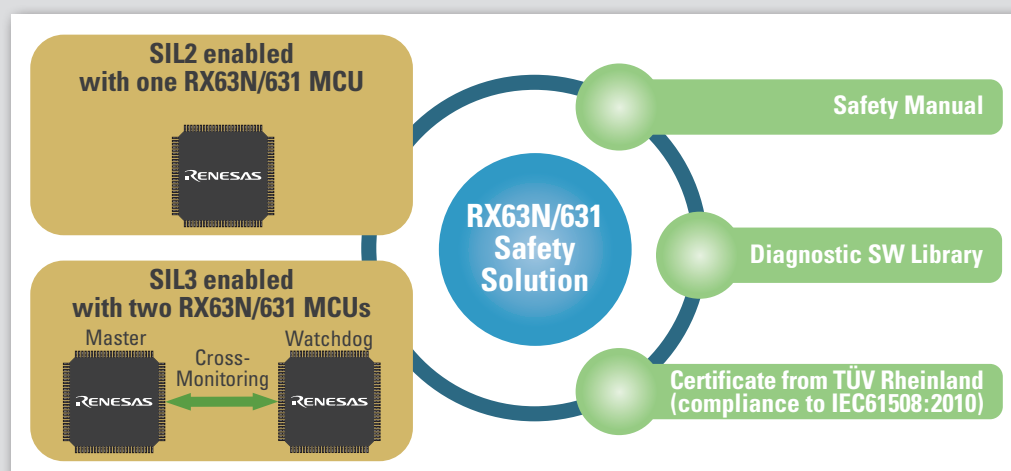
Applications

- Industrial Motor Drives
- Programmable Logic Controllers
- Safety Controllers
- Safety Sensors

Key Benefits

- Solution compliant to the safety standard IEC61508:2010
- Safety Manual with relevant information and procedures related to functional safety
- Diagnostic SW library containing test routines for RX CPU core, RAM, and ROM
- High quality RX CPU core self-test, diagnostic coverage proven by fault simulation tests
- Diagnostic SW developed for SIL3 using IEC61508 compliant IAR Embedded Workbench for RX
- Scalable solution applicable to all devices of RX63N/631
- Solution compliant to IEC61508
- Certified by TÜV Rheinland
- Enables faster time-to-market

Safety Solution Scheme



Solution Packages

Functional Safety Evaluation Kit (including Evaluation License)


- Renesas Starter Kit+ for RX63N
- Diagnostics library for CPU, RAM, and ROM
- Diagnostics Software User Guide
- Safety Manual Evaluation Version
- IAR Installer EWRX-FS v2.42.4 (Evaluation Version)
- Quick Start Guide
- Video

Functional Safety Production License

- Diagnostics library for CPU, RAM, and ROM (Source Code)
- Diagnostics Software User Guide
- Safety Manual Full Version
- Certificate & Test Report from TÜV Rheinland

About Self-test Diagnostic Software Library

| | |
|--------------------------------------|---|
| Diagnostic Functions | CPU Core, RAM, Flash ROM |
| Test Timing | At start up or during run time |
| Running of Test Condition | In sequences or in slices |
| Configurability | Independent CPU test segments, flexible memory sizes |
| Development Tool (C Compiler) | IAR Embedded Workbench for RX Functional Safety Version |
| Certification | IEC61508 SIL3 Certified by TÜV Rheinland |



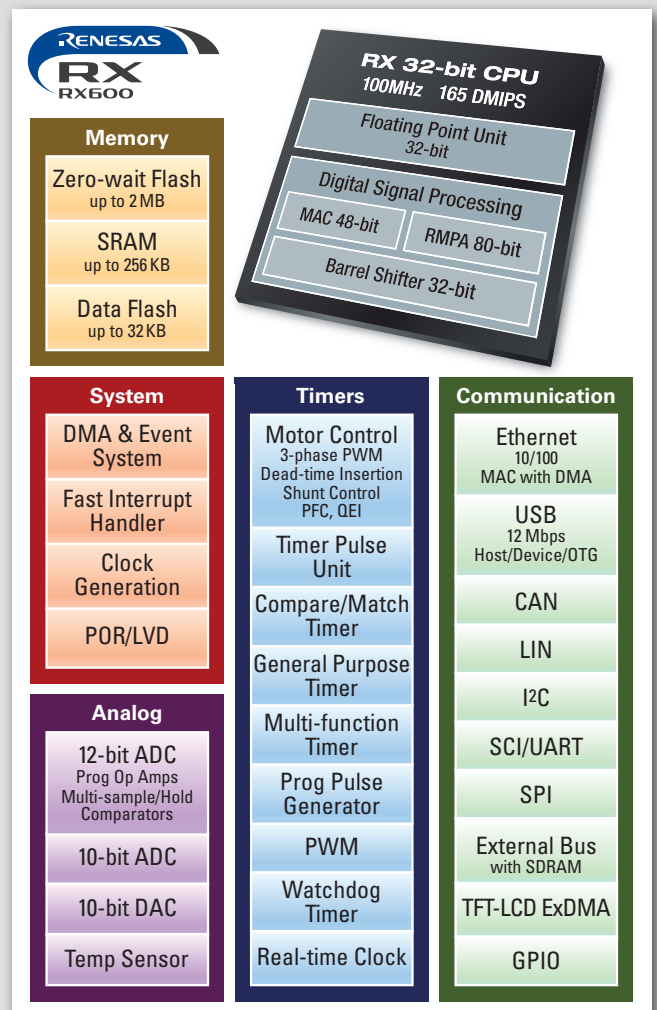
Functional Safety Type Approved

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Ordering Information

| Order Code | Product |
|----------------------|---------------------------|
| YCERTIFY-IT-RX63N-EV | Safety Evaluation Kit |
| YCERTIFY-IT-RX63N-PR | Safety Production License |

Block Diagram RX63N/631 Series



Before purchasing or using any Renesas Electronics products listed herein, please refer to the latest product manual and/or data sheet in advance.

