

RL78/G1C

R01AN1768EG0100

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Nov 01, 2013

Utilising the I2C in Slave Mode Sample Code for e2studio

Introduction

The purpose of this Application Note is to show the user how to add the associated RL78/G1C sample code to a new or existing e2studio workspace; as well as give an explanation of what the sample code does.

The sample code provided with this Application Note demonstrates usage of the I2C Interface in Slave Mode. The program runs on the RL78/G1C RSK and demonstrates usage by simulating an EEPROM memory device, similar to the Renesas R1EX24xxx series.

Target Device

RL78/G1C

Development environment

IDE: e2studio

Compiler: GNURL78 v13.01 -ELF

Hardware: Renesas Starter Kit for RL78/G1C

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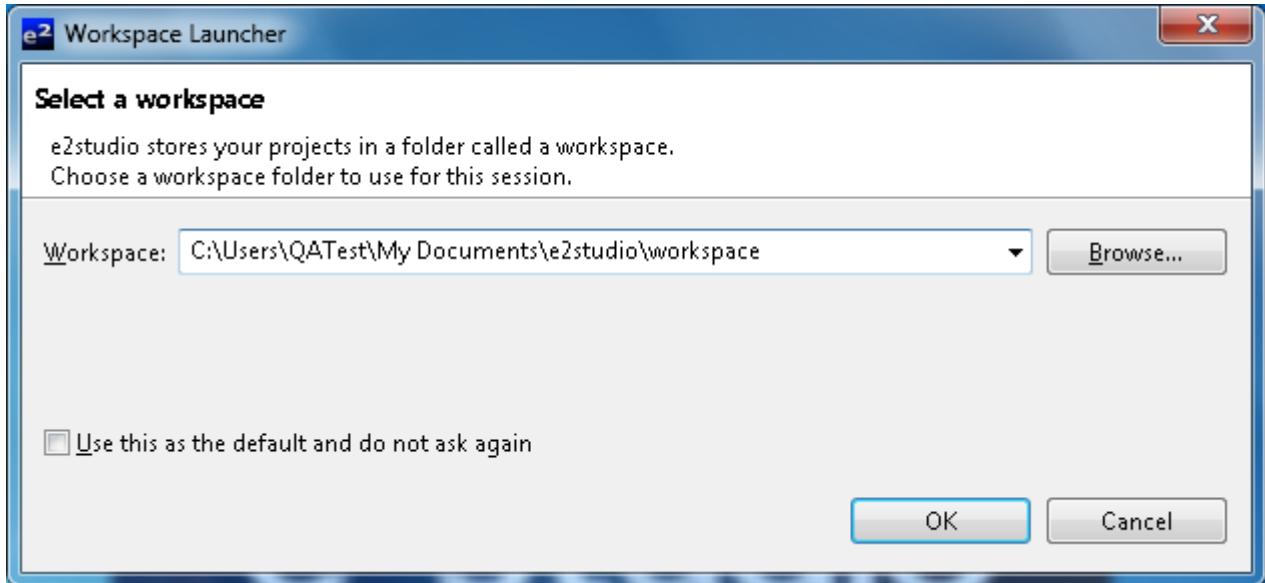
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1. Installation

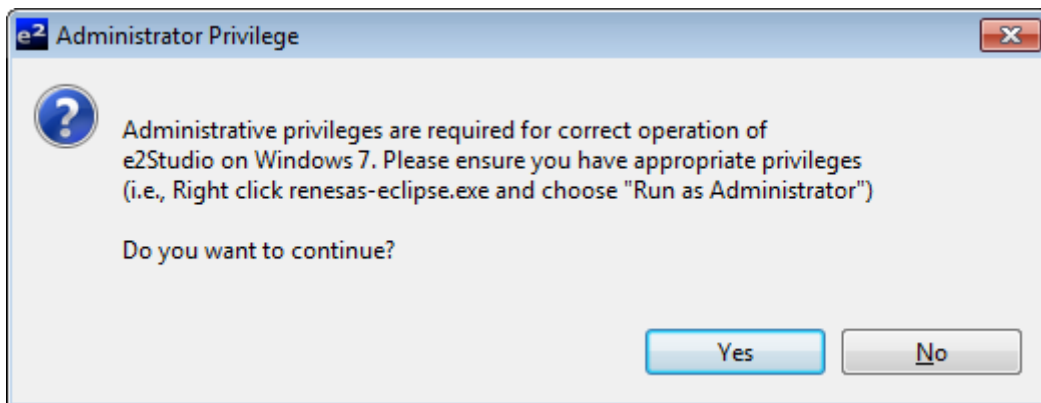
This section assumes e2studio IDE is already installed on the user's personal computer (PC). Create a new folder and name it as 'RSKRL78G1C_Workspace'. Copy the zipped file IIC_Slave.zip, available in the Application Note package downloaded from the website, to this folder. Extract the IIC_Slave.zip file to the RSKRL78G1C_Workspace folder.

2. Creating the Project Workspace

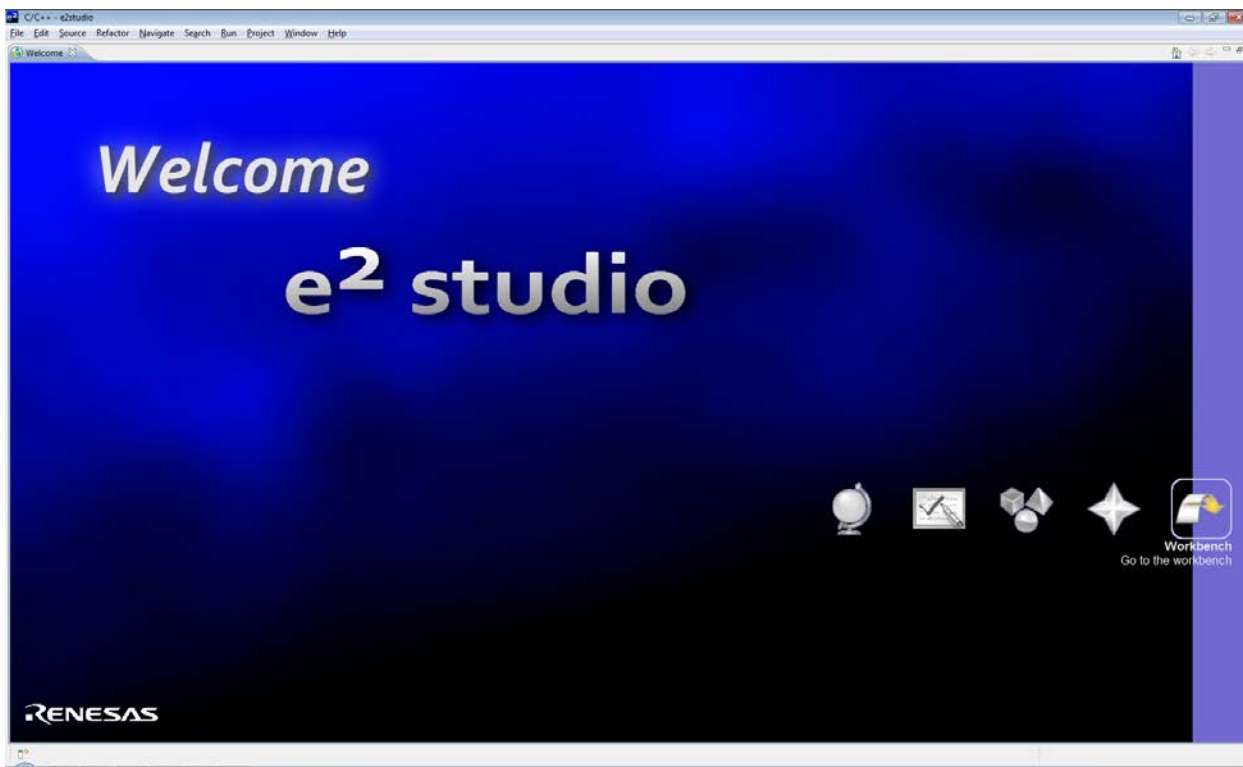
Open E2studio IDE by clicking the Windows Start button, select All Programs > Renesas Electronics e2studio > Renesas e2studio.



Select <OK>.

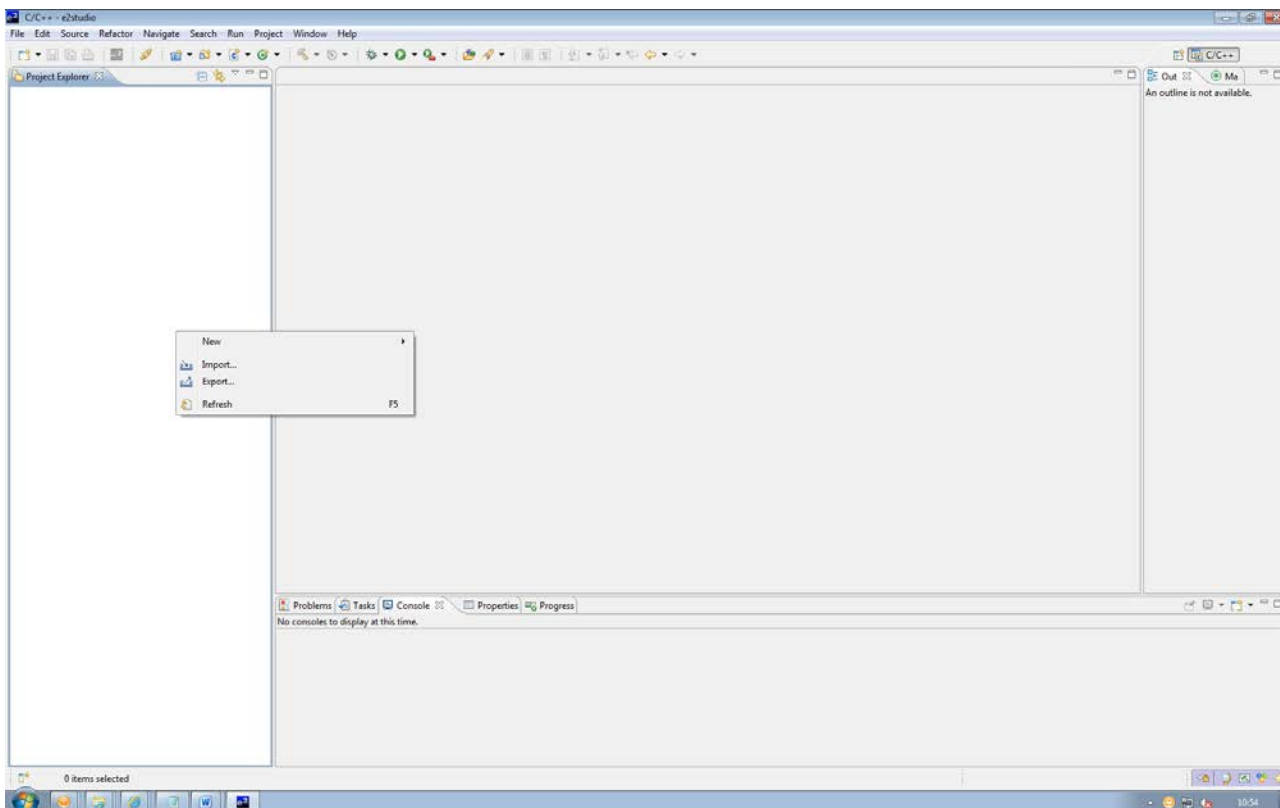


Select <Yes> to Administrator Privilege dialog.

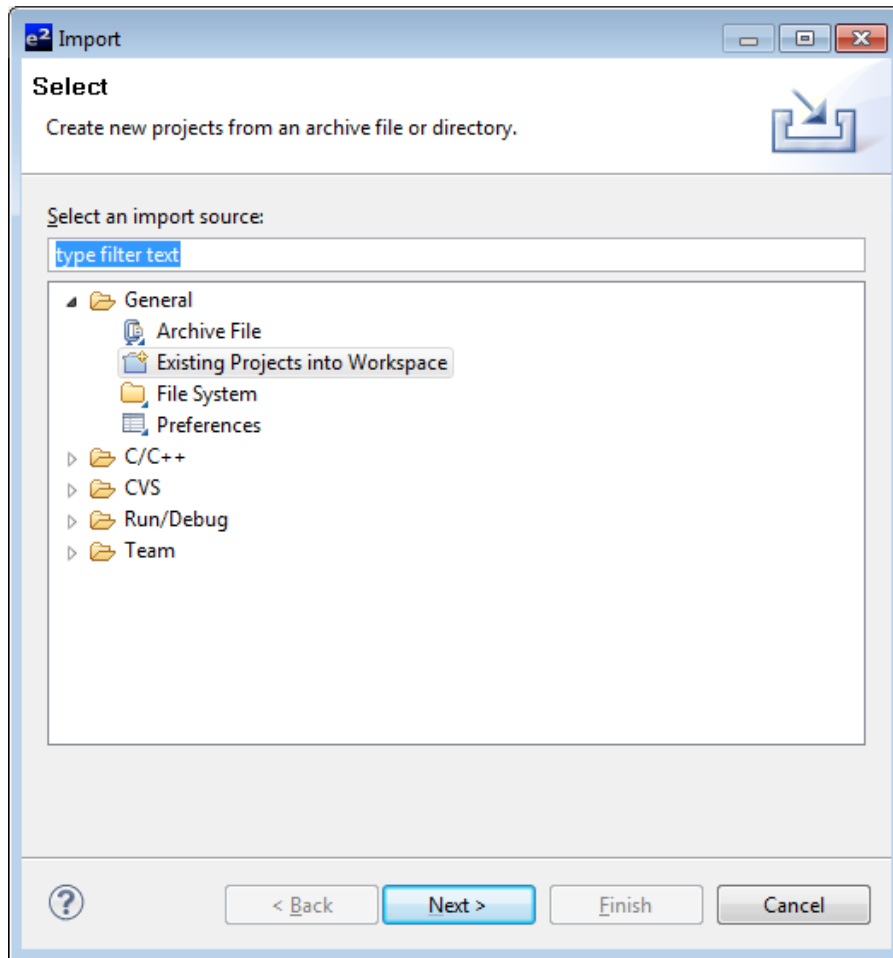


On the welcome screen select Go the Workbench icon as shown above.

1. Once the e2studio environment has initialised, right click in the project explorer window and click <Import...>



2. The Import dialog will now appear. Expand the “General” folder icon, and select “Existing Projects into Workspace”, then click ‘Next’.



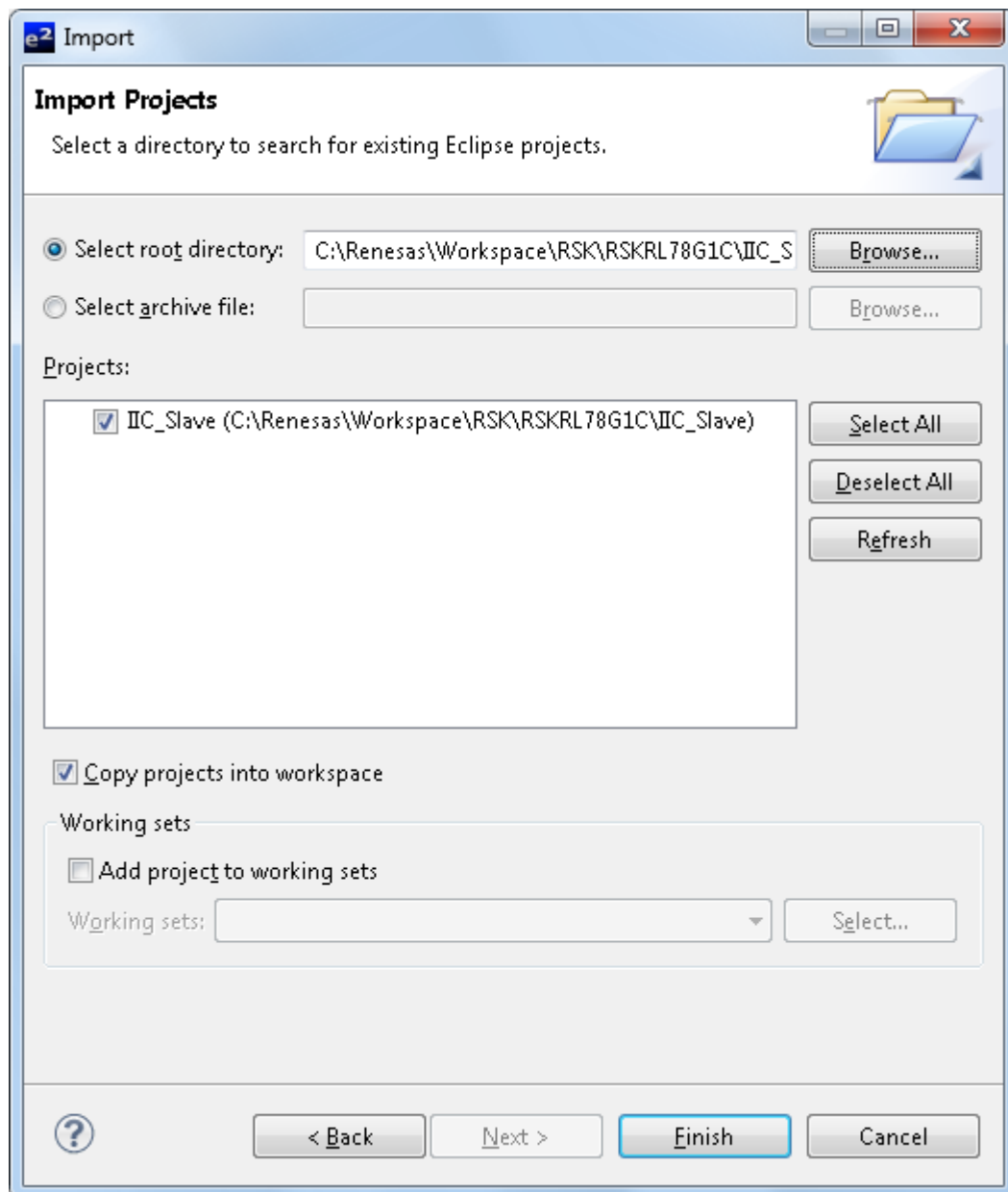
3. The Import Dialog will now appear ,specify the project to import .Click the “Browse” button and locate the directory: C:\Renesas\Workspace\RSK\RSKRL78G1C

Navigate to the unzipped IIC_Slave folder located in RSKRL78G1C Workspace folder.

Select the IIC_Slave folder.

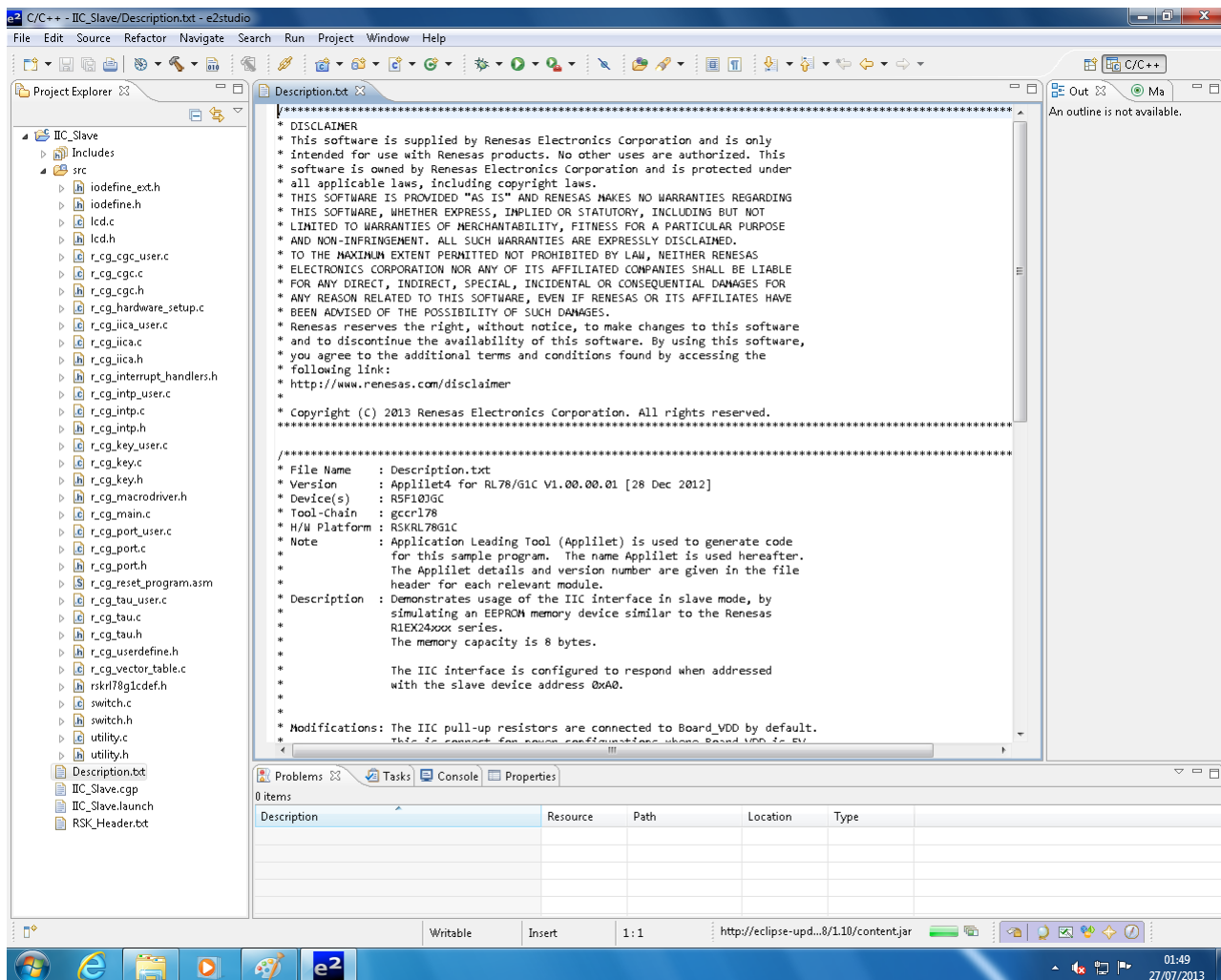
And also ensure that the ‘Copy projects into workspace’ option is ticked. Click<Finish>

The IDE e2studio will load the project.



3. Opening Sample Code and Source Files

Once the project has been opened, the source code and all dependant files can be opened in the editor by expanding the folders in the Project Tree window and double clicking the files listed. All files have been grouped according to their file type.



4. Source Code Functionality

The source code project is specifically written to run on the appropriate RSK. However this source code can be useful as an example even without the RSK.

The project was written using source files containing API functions generated using Code Generator. The project will contain a C source file 'r_cg_main.c'. This source file includes the C function main(). All source files and dependent files whose filenames are prefixed with 'r_' were generated using Application Leading Tool. For more information, refer Description.txt.

5. Code Execution

Demonstrates usage of the IIC interface in slave mode, by simulating an EEPROM memory device similar to the Renesas R1EX24xxx series. The memory capacity is 8 bytes.

The IIC interface is configured to respond when addressed with the slave device address 0xA0.

Modifications:

The IIC pull-up resistors are connected to Board_VDD by default. This is correct for power configurations where Board_VDD is 5V. For power configurations where Board_VDD is 3.3V, the IIC pull-up resistors should be connected to Board_5V, by removing R83 and fitting R84.

Please refer to the schematics document and component placement diagram in the User Manual to help locate components.

Instructions:

1. Connect a compatible IIC master device, such as another RSKRL78/G1C running the IIC_Master sample, as follows:

Master | RSK Slave

SDA -> SDA (JA1, pin 25)

SCL -> SCL (JA1, pin 26)

GND -> Any ground point.

(Ensure the device is powered as its datasheet specifies)

2. Build and download the sample code to the RSK.

3. Open the 'Expressions' window from the menu bar: Window > Show View > Expressions

Add the 'g_master_data' global variable to the 'Expressions' window by double-clicking on the variable then drag and drop into the 'Expressions' window. The variable will act as the simulated EEPROM memory.

4. Click the 'Resume' button to go to the main() function, and 'Resume' again to start program execution'. "IIC Slav" will be displayed on the LCD.

5. When a valid read request is sent from a master device, the RSK will respond correctly and display "Read M" on the LCD

6. When a valid write request is sent from a master device, the RSK will store the write data into the simulated EEPROM memory, and display "Write M" on the debug LCD.

7. If the RSK displays "Error", it means the program has detected a failed IIC transmission. Reset both the master and slave devices and retry.

6. Website, Inquiries and Support

Renesas Electronics Website

<http://www.renesas.com/>

Inquiries

<http://www.renesas.com/inquiry>

Support

<http://www.renesas.com/rskrl78g1c>

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Revision Record

Rev.	Date	Description	
		Page	Summary
1.00	November 01, 2013	—	First edition issued

General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products

1. Handling of Unused Pins

Handle unused pins in accord with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.

In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed.

In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable.

When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different type number, confirm that the change will not lead to problems.

- The characteristics of an MPU or MCU in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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Renesas Electronics America Inc.
2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A.
Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited
1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada
Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH
Arcadiastrasse 10, 40472 Düsseldorf, Germany
Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China
Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd.
13F, No. 363, Fu Shing North Road, Taipei, Taiwan
Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd.
11F., Samik Lavied' or Bldg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea
Tel: +82-2-558-3737, Fax: +82-2-558-5141