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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# M16C/80 Group Operation of Timer A (one-shot timer mode, external trigger)

# 1.0 Abstract

In one-shot timer mode, choose functions from those listed in Table 1. Operations of the circled items are described below.

Item		Set-up	
Count source	0	Internal count source (f1 / f8 / f32 / fc32)	
Pulse output function		No pulses output	
	0	Pulses output	
Count start condition		External trigger input (falling edge of input signal to the TAin pin)	
	0	External trigger input (rising edge of input signal to the TAin pin)	
		Timer overflow (TB2/TAj/TAk overflow)	
		Writing "1" to the one-shot start flag	

Note: j = i - 1, but j = 4 when i = 0; k = i + 1, but k = 0 when i = 4.

# 2.0 Introduction

- Operation (1) If the TAi<sub>IN</sub> pin input level changes from "L" to "H" with the count start flag set to "1", the counter performs a down count on the count source. At this time, the TAi<sub>OUT</sub> pin output level goes to "H" level.
  - (2) If the value of the counter becomes "0000<sub>16</sub>", the TAi<sub>OUT</sub> pin outputs an "L" level, and the counter reloads the content of the reload register and stops counting. At this time, the timer Ai interrupt request bit goes to "1".
  - (3) If a trigger occurs while a count is in progress, the counter reloads the value of the reload register again and continues counting. The reload timing is in step with the next count source input after the trigger.
  - (4) Setting the count start flag to "0" causes the counter to stop and to reload the content of the reload register. Also, the TAi<sub>OUT</sub> pin outputs an "L" level. At this time, the timer Ai interrupt request bit goes to "1".
- Note
- Set TAi<sub>IN</sub> pin's function select register A to I/O port and port direction register to "0".
  - When using pulse output, select  $TAi_{out}$  output function with the function select register A and B.
  - When setting the function select registers A, B, and C, sets the function select registers B and/or C first, and then sets the function select register A.

Figure 1 shows the operation timing

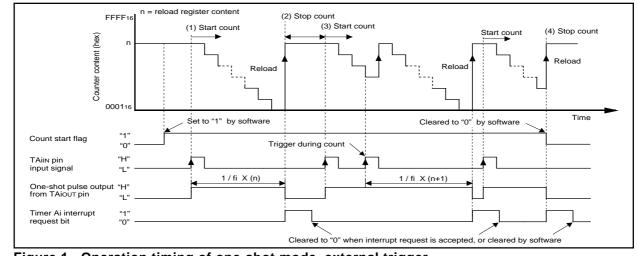
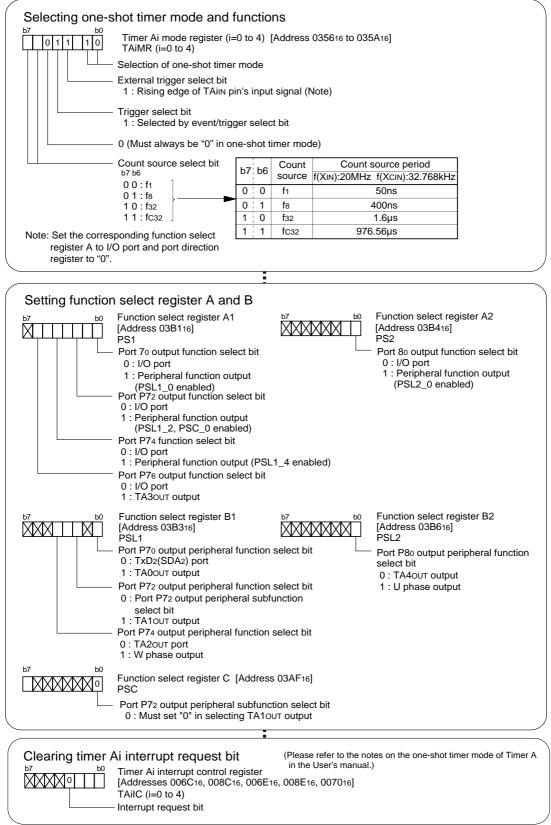


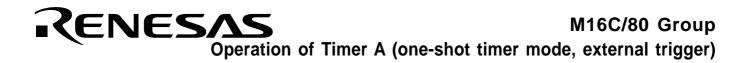
Figure 1. Operation timing of one-shot mode, external trigger

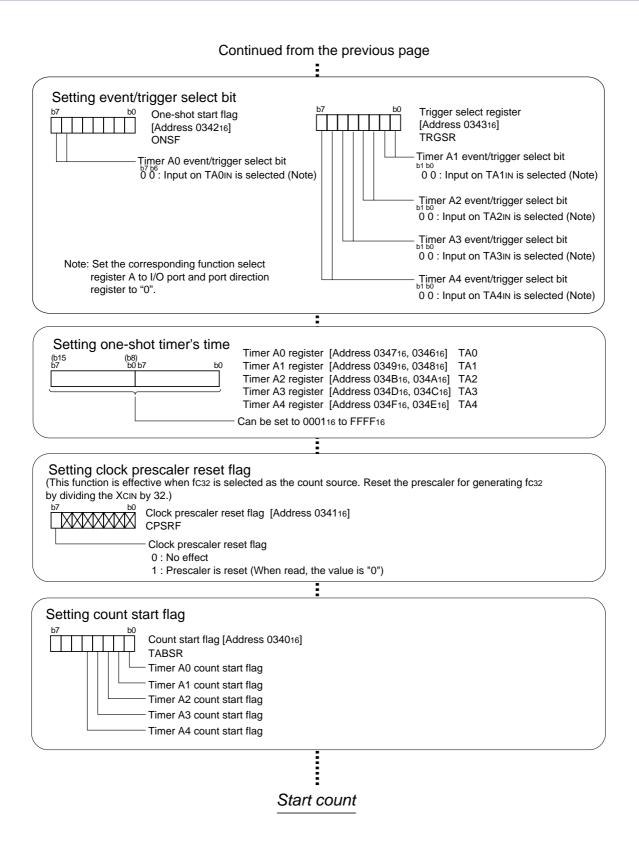


## 3.0 Set-up procedure



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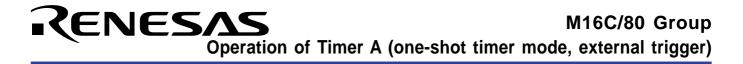




## 4.0 Programming Code

```
;
 M16C/80 Program Collection
;
 FILE NAME : rjj05b0131_src.a30
;
 CPU : M16C/80 Group
;
 FUNCTION : Operation of Timer A
;
        (one-shot timer mode, external trigger)
;
 HISTORY : 2003.06.16 Ver 1.00
;
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;
;
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;
;
    Include
.LIST OFF
                  ;Stops outputting lines to the assembler list file
    .INCLUDE sfr80100.inc ;Reads the file that defined SFR
                  ;Starts outputting lines to the assembler list file
    .LIST
         ON
;
Symbol definition
;
ROM_TOP .EQU OFFC000H ;Start address of ROM
FIXED_VECT_TOP .EQU OFFFFDCH ;Start address of fixed vector
;
;
   Program area
Start up
;_____
    .SECTION PROGRAM, CODE ;Declares section name and section type
    .ORG
         ROM_TOP
                  ;Declares start address
RESET:
    ; Sets Processor mode, System clock and Main clock division
    MOV.B #03H, prcr ;Removes protect
MOV.B #1000000B, pm0 ; Single-chip mode
    MOV.B #11000000B, pm1 ; Flash memory version
    MOV.B #00001000B, cm0 ; Xcin-Xcout High
    MOV.B #0010000B, cml ; Xin-Xout High
    MOV.B #00010010B, mcd ; No division mode
    MOV.B #00H, prcr ;Protects all registers
;
```

TimerA (one-shot timer mode, external trigger selected) ; ; Selecting one-shot timer mode and functions #01011010B, talmr MOV.B ; |||||++----;Selection of one-shot timer mode |||||+-----;This bit is invalid in M16C/80 series ; ; |||+----;External trigger select bit ; (1:Rising edge of TA1IN pin's input signal) (Note) |||+----;Trigger select bit ; (1:Selected by event/trigger select register) ; -----;Must always be "0" in one-shot timer mode ; | | + ++----;Count source (01:f8) ; ; Clearing timer A1 interrupt request bit #00000000B, talic MOV.B +----;Interrupt request bit ; ; Setting function select register A and B (Setting pulse output function) psl1\_2 ;Port P72 peripheral function select bit (TA10UT output) BSET BCLR psc\_0 ;Must set "0" in selecting TA10UT output ;Port P72 function select bit (peripheral function output) BSET ps1\_2 ; Setting event/trigger select bit MOV.B #0000000B, trgsr ++----;(00:Input on TA1IN is selected) (Note) ; ; (Note) Sets the corresponding function select register A to I/O port and ; port direction register to "0" BCLRpd7\_3;Port P73 direction registerBCLRps1\_3;Port P73 is I/O port ; Setting one-shot timer's time MOV.W #2500, tal ;(1msec @20MHz, f8) ; Setting clock prescaler reset flag ; (This function is effective when fC32 is selected as the count source) MOV.B #0000000B, cpsrf ; +----;Clock prescaler reset flag (0:No effect) ; Setting count start flag MOV.B #00000010B, tabsr ; +----;Timer A1 count start flag ; MAIN: JMP MAIN ; Dummy interrupt processing program ; dummy: RETT ; Setting of fixed vector ; \*\*\*\*\*\* ;\* .SECTION F VECT, ROMDATA FIXED\_VECT\_TOP .ORG ; .LWORD dummy ;Undefined instruction .LWORD dummy ;Overflow dummy .LWORD ;BRK instruction execution .LWORD dummy ;Address match .LWORD dummy ; .LWORD dummy ;Watchdog timer .LWORD dummy ; .LWORD dummy ;NMI .LWORD RESET ;Reset ; .END



## 5.0 Reference

#### Renesas Technology Corporation Semiconductor Home page

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### **Data Sheet**

M16C/80 group Rev. E3 (Use the latest version on the Home page: http://www.renesas.com/)

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M16C/80 group Rev. B (Use the latest version on the Home page: http://www.renesas.com/)

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