Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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DATA SHEET



MOS FIELD EFFECT TRANSISTOR NP90N04MUH, NP90N04NUH, NP90N04PUH

SWITCHING **N-CHANNEL POWER MOS FET**

DESCRIPTION

The NP90N04MUH, NP90N04NUH, NP90N04PUH are N-channel MOS Field Effect Transistors designed for high current switching applications.

ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE
NP90N04MUH-S18-AY Note		Tube	TO-220 (MP-25K) typ. 1.9 g
NP90N04NUH-S18-AY Note		50 p/tube	TO-262 (MP-25SK) typ. 1.8 g
NP90N04PUH-E1-AY Note	Pure Sn (Tin)	Tape	
NP90N04PUH-E2-AY Note		800 p/reel	TO-263 (MP-25ZP) typ. 1.5 g

Note Pb-free (This product does not contain Pb in the external electrode.)

FEATURES

• Enhancing Tch(MAX.) to 200°C (Operation time until 250 Hr)

- Super low on-state resistance
- NP90N04MUH, NP90N04NUH

 $R_{DS(on)} = 4.1 \text{ m}\Omega$ MAX. (VGS = 10 V, ID = 45 A)

- NP90N04PUH

 $R_{DS(on)}$ = 3.8 m Ω MAX. (VGS = 10 V, ID = 45 A)

- · High avalanche energy, High avalanche current
- Low input capacitance

Ciss = 5500 pF TYP. (VDS = 25 V)

(TO-220)



(TO-262)





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ABSOLUTE MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Drain to Source Voltage (Vgs = 0 V)	VDSS	40	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	I _{D(DC)}	±90	Α
Drain Current (pulse) (Tc = 25°C) Note1	I _{D(pulse)}	±360	Α
Total Power Dissipation (Tc = 25°C)	P _{T1}	230	W
Total Power Dissipation (T _A = 25°C)	P _{T2}	1.8	W
Channel Temperature	T _{ch1}	175	°C
Channel Temperature Note2	Tch2	200	°C
Storage Temperature	T _{stg}	-55 to +175	°C
Repetitive Avalanche Current Note3	I _{AR1}	65	Α
Repetitive Avalanche Current Note4	I _{AR2}	70	Α
Repetitive Avalanche Energy Note5	Ear	1000	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Reliability test condition

High temperature bias condition (V_{DS} = V_{DSS} , V_{GS} = 0 V, 250 Hr) High temperature gate bias condition (V_{DS} = 0 V, V_{GS} = 20 V, 250 Hr)

- **3.** L = 100 μ H, T_{ch} \leq 200°C
- **4.** L = 10 μ H, T_{ch} \leq 200°C
- **5.** Tch \leq 200°C, Rg = 25 Ω , Vgs = 20 \rightarrow 0 V

THERMAL RESISTANCE

<R>

<R>

Channel to Case Thermal Resistance	Rth(ch-C)	0.65	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W



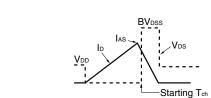
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 40 V, V _{GS} = 0 V			1	μΑ
Gate Leakage Current	Igss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate to Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2.0	3.0	4.0	V
Forward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 45 A	25	49		S
Drain to Source On-state Resistance Note	RDS(on)	V _{GS} = 10 V, I _D = 45 A NP90N04MUH, NP90N04NUH		3.3	4.1	mΩ
		V _{GS} = 10 V, I _D = 45 A NP90N04PUH		3.0	3.8	mΩ
Input Capacitance	Ciss	V _{DS} = 25 V,		5500	8300	pF
Output Capacitance	Coss	V _{GS} = 0 V,		1100	1700	pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		280	500	pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 20 V, I _D = 45 A,		27	60	ns
Rise Time	tr	V _{GS} = 10 V,		15	38	ns
Turn-off Delay Time	td(off)	$R_G = 0 \Omega$		70	140	ns
Fall Time	tf			13	33	ns
Total Gate Charge	Q _G	V _{DD} = 32 V,		90	135	nC
Gate to Source Charge	QGS	V _{GS} = 10 V,		31		nC
Gate to Drain Charge	Q _{GD}	I _D = 90 A		29		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I _F = 90 A, V _{GS} = 0 V		0.93	1.5	V
Reverse Recovery Time	trr	IF = 90 A, VGS = 0 V,		60		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		90		nC

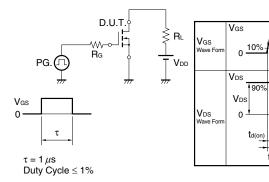
Note Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY

$\begin{array}{c} \text{D.U.T.} \\ \text{RG} = 25 \ \Omega \\ \text{V}_{\text{GS}} = 20 \rightarrow 0 \ \text{V} \end{array} \begin{array}{c} \text{D.U.T.} \\ \text{PG.} \\ \text{N} \end{array} \begin{array}{c} \text{D.U.T.} \\ \text{N} \end{array} \begin{array}{c} \text{D.U.T.} \\ \text{N} \end{array}$



TEST CIRCUIT 2 SWITCHING TIME

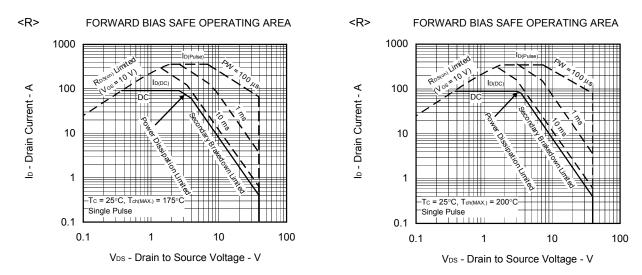


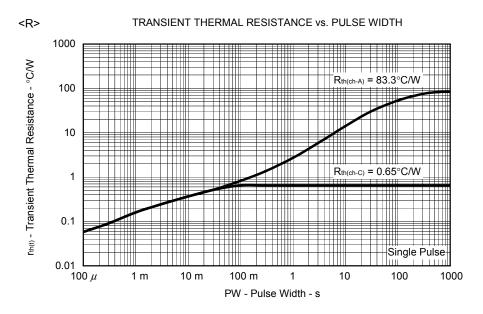
TEST CIRCUIT 3 GATE CHARGE

90%

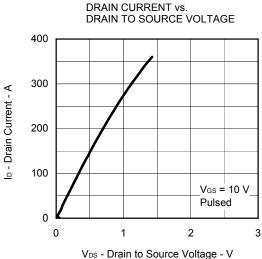


TYPICAL CHARACTERISTICS (TA = 25°C) (NP90N04MUH)





Note Confirm the operation tracks are in Safe Operating Area.

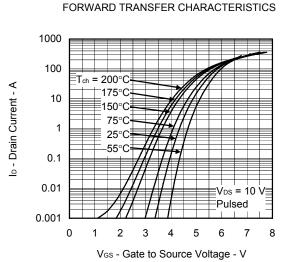


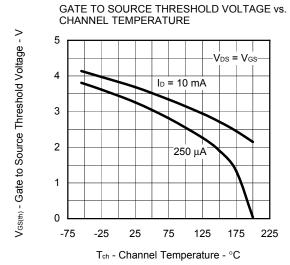


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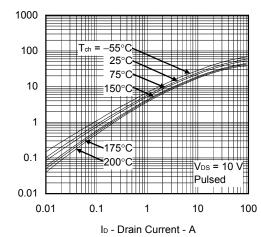
 \mid y*s \mid - Forward Transfer Admittance \cdot

R_{DS(on)} - Drain to Source On-state Resistance - mΩ

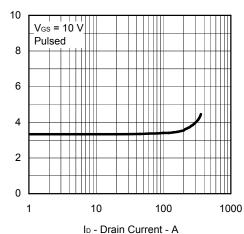






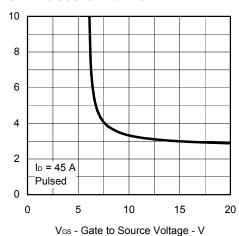




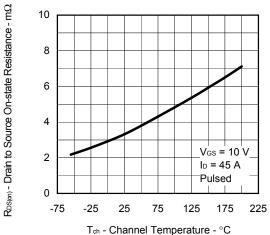


RDS(on) - Drain to Source On-state Resistance - m\Omega

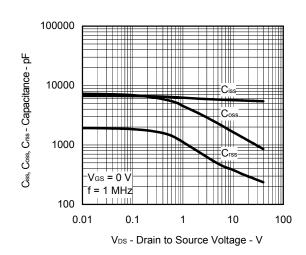




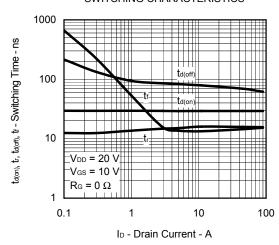




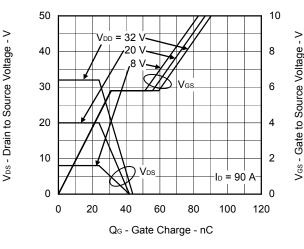
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



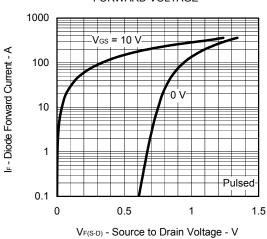
SWITCHING CHARACTERISTICS



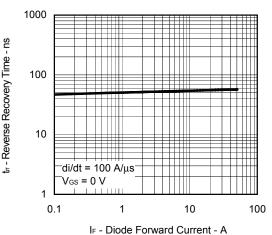
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE

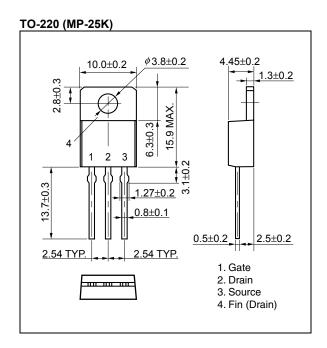


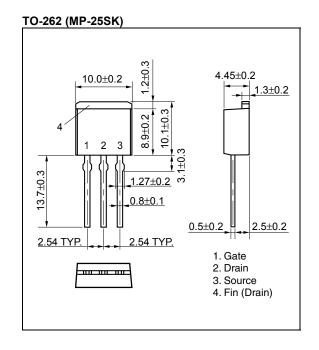
REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



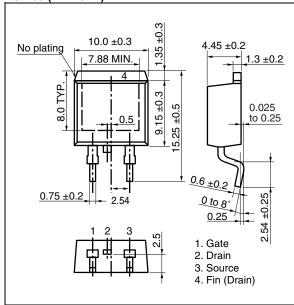


PACKAGE DRAWINGS (Unit: mm)

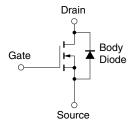




TO-263 (MP-25ZP)



EQUIVALENT CIRCUIT

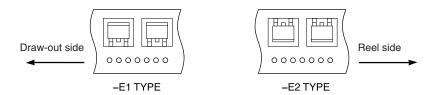


Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

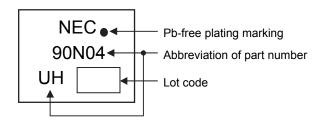


TAPE INFORMATION (NP90N04PUH)

There are two types (-E1, -E2) of taping depending on the direction of the device.



MARKING INFORMATION



RECOMMENDED SOLDERING CONDITIONS

These products should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, please contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Soldering Method	Soldering Conditions	Recommended Condition Symbol	
Infrared reflow NP90N04PUH	Maximum temperature (Package's surface temperature): 260°C or below Time at maximum temperature: 10 seconds or less Time of temperature higher than 220°C: 60 seconds or less Preheating time at 160 to 180°C: 60 to 120 seconds Maximum number of reflow processes: 3 times Maximum chlorine content of rosin flux (percentage mass): 0.2% or less	IR60-00-3	
Wave soldering NP90N04MUH, NP90N04NUH	Maximum temperature (Solder temperature): 260°C or below Time: 10 seconds or less Maximum chlorine content of rosin flux: 0.2% (wt.) or less	THDWS	
Partial heating NP90N04MUH, NP90N04NUH, NP90N04PUH	Maximum temperature (Pin temperature): 350°C or below Time (per side of the device): 3 seconds or less Maximum chlorine content of rosin flux: 0.2% (wt.) or less	P350	

Caution Do not use different soldering methods together (except for partial heating).

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