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April 1st, 2010 Renesas Electronics Corporation

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SILICON POWER TRANSISTOR 2SB963-Z

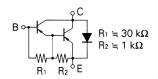
PNP SILICON EPITAXIAL TRANSISTOR (DARLINGTON CONNECTION)

DESCRIPTION

The 2SB963-Z is designed for switching, especially in Hybrid Integrated Circuits.

FEATURES

- High Gain hfe = 2000 to 3000
- Complement to 2SD1286-Z



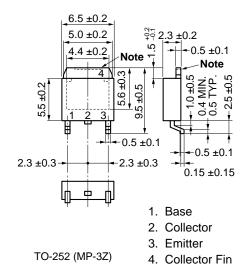
ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Collector to Base voltage	Vсво	-60	V
Collector to Emitter voltage	Vceo	-60	V
Emitter to Base voltage	VEBO	-8	V
Collector Current (DC)	Ic(DC)	∓1.0	Α
Collector Current (pulse) Note 1	C(pulse)	∓2.0	Α
Total Power Dissipation Note 2	P _T (T _A = 25°C)	2.0	W
Junction Temperature	T_{j}	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C

Notes 1. PW \leq 10 ms, Duty Cycle \leq 50%

2. When mounted on ceramic substrate of 7.5 cm $^2 \times 0.7$ mm

<R> PACKAGE DRAWING (Unit: mm)



Note The depth of notch at the top of the fin is from 0 to 0.2 mm.

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Document No. D18261EJ4V0DS00 (4th edition) (Previous No. TC-1627A)



ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

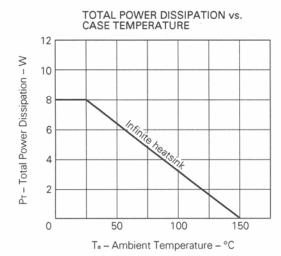
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	Ісво			-10	μΑ	VcB = -60 V, IE = 0
Emitter Cutoff Current	Ієво	314 A. 1	8217.5	-1.0	μΑ	VEB = -5.0 V, Ic = 0
DC Current Gain	h _{FE1} ***	1 000				Vce = -2.0 V, lc = -0.2 A
DC Current Gain	h _{FE2} ***	2 000		30 000	,	Vce = -2.0 V, lc = -0.5 A
Collector Saturation Voltage	VCE(sat)***			-1.5	V	Ic = -0.5 A, I _B = -50 mA
Base Saturation Voltage	VBE(sat)***			-2.0	V	Ic = -0.5 A, I _B = -50 mA
Turn On Time	ton		0.5		μs	Ic = -0.5 A, RL = 100 Ω
Storage Time	tstg		1.0		μs	l _{B1} = −l _{B2} = −0.1 mA
Fall Time	tr		1.0		μs	Vcc = -50 V

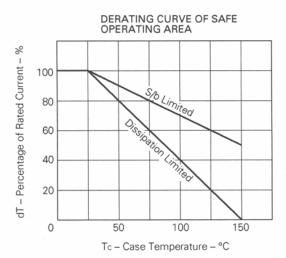
^{***} Pulsed: PW \leq 350 μ s, Duty Cycle \leq 2 %

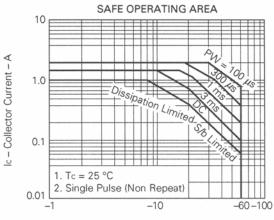
hre Classification

MARKING	M	L	К	
hFE2	2 000 to 5 000	4 000 to 10 000	8 000 to 30 000	

TYPICAL CHARACTERISTICS (Ta = 25 °C)

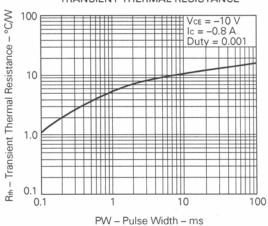


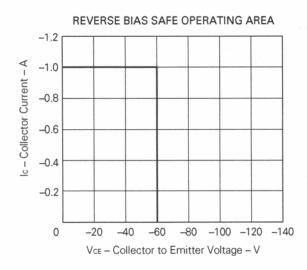


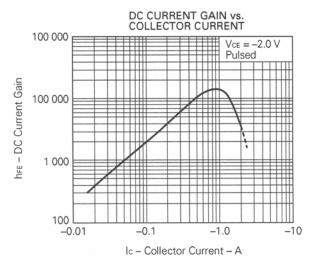


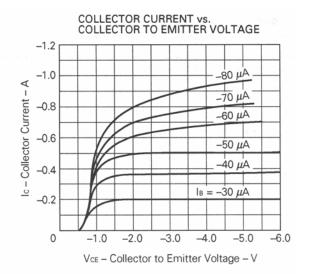
Vce - Collector to Emitter Voltage - V

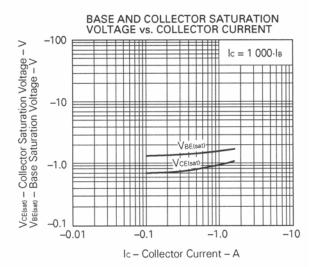
TRANSIENT THERMAL RESISTANCE



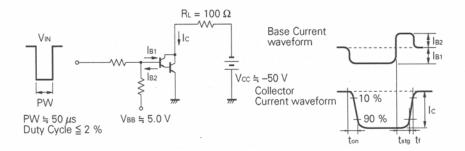








SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT



3

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