2.5V CMOS 1-TO-10 CLOCK DRIVER

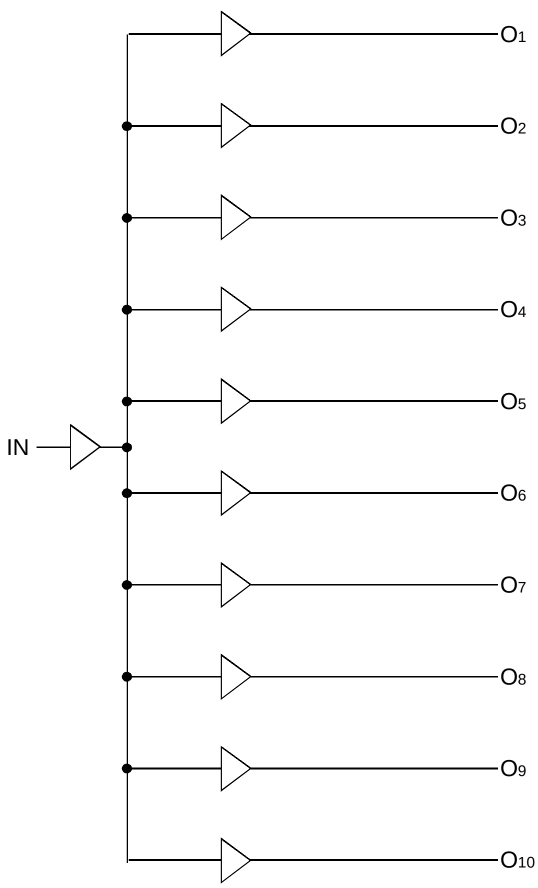
FEATURES:

- High frequency > 150MHz
- Guaranteed low skew < 150ps (max.) between any two outputs
- Very low duty cycle distortion < 300ps
- High speed: propagation delay < 3ns
- Very low CMOS power levels
- TTL compatible inputs and outputs
- 1:10 fanout
- Maximum output rise and fall time < 1.25ns (max.)
- Low input capacitance: 3pF typical
- 2.5V supply voltage
- Available in SSOP and QSOP packages

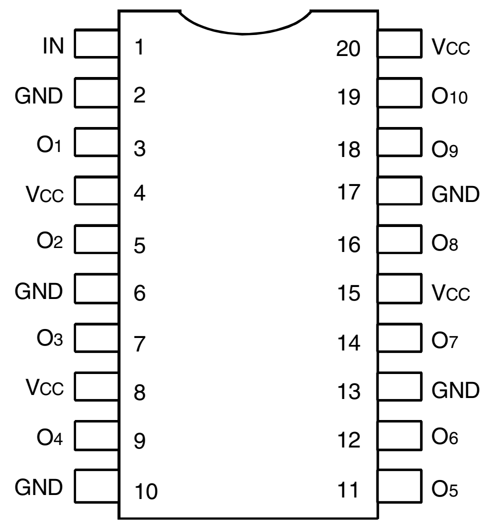
DESCRIPTION:

The FCT20807 is a 2.5V compatible, high speed, low noise, 1:10 fanout, non-inverting clock buffer. The large fanout from a single input reduces loading on the preceding driver and provides an efficient clock distribution network. Providing output to output skew as low as 150ps, the FCT20807 is an ideal clock distribution device for synchronous systems. Multiple power and grounds reduce noise. Typical applications are clock and signal distribution.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



SSOP/ QSOP
TOP VIEW

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max	Unit
V_{TERM}(2)	Terminal Voltage with Respect to GND	–0.5 to +4.6	V
V_{TERM}(3)	Terminal Voltage with Respect to GND	–0.5 to +5.5	V
V_{TERM}(4)	Terminal Voltage with Respect to GND	–0.5 to V_{CC}+0.5	V
T_{STG}	Storage Temperature	–65 to +150	°C
I_{OUT}	DC Output Current	–60 to +60	mA

NOTES:
1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. V_{CC} terminals.
3. Input terminals.
4. Outputs and I/O terminals.

CAPACITANCE (T_A = +25°C, f = 1.0MHz)

Symbol	Parameter(1)	Conditions	Typ.	Max.	Unit
C_{IN}	Input Capacitance	V_{IN} = 0V	3	4	pF
C_{OUT}	Output Capacitance	V_{OUT} = 0V	—	6	pF

NOTE:
1. This parameter is measured at characterization but not tested.

PIN DESCRIPTION

Pin Names	Description
IN	Clock Inputs
Ox	Clock Outputs

POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions(1)		Min.	Typ.(2)	Max.	Unit
I_{CCL} I_{CCH}	Quiescent Power Supply Current TTL Inputs HIGH	V_{CC} = Max. V_{IN} = GND or V_{CC}		—	0.1	20	µA
ΔI_{CC}	Power Supply Current per Input HIGH	V_{CC} = Max. V_{IN} = V_{CC} –0.6V		—	45	300	µA
I_{CCD}	Dynamic Power Supply Current per Output(3)	V_{CC} = 2.7V 15 pF	150 MHz	—	40	—	µA /MHz
I_C	Total Power Supply Current(4)	V_{CC} = Max. C_L = 12pF	V_{IN} = V_{CC} V_{IN} = GND	—	65	90	mA
		All outputs toggling f_i = 150MHz	V_{IN} = V_{CC} –0.6V V_{IN} = GND	—	75	100	

NOTES:
1. For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 2.5V, +25°C ambient.
3. This parameter is not directly testable, but is derived for use in Total Power Supply calculations.
4. $I_C = I_{QUIESCENT} + I_{INPUTS} + I_{DYNAMIC}$
$I_C = I_{CC} + \Delta I_{CC} D_H N_T + I_{CCD} (f_i)$
I_{CC} = Quiescent Current (I_{CCL}, I_{CCH} and I_{CCZ})
ΔI_{CC} = Power Supply Current for a TTL High Input (V_{IN} = V_{CC} -0.6V)
D_H = Duty Cycle for TTL Inputs High
N_T = Number of TTL Inputs at D_H
I_{CCD} = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)
f_i = Input Frequency

DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified
Industrial: T_A = -40°C to +85°C, V_{CC} = 2.5V ± 0.2V

Symbol	Parameter	Test Conditions[1]		Min.	Typ.[2]	Max.	Unit
V_{IH}	Input HIGH Level			1.7	—	—	V
V_{IL}	Input LOW Level			—	—	0.7	V
I_{IH}	Input HIGH Current (Input pins)	V_{CC} = Max., V_I = V_{CC}		—	—	±1	mA
I_{IL}	Input LOW Current (Input pins)	V_{CC} = Max., V_I = GND		—	—	±1	mA
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = –18mA		—	–0.7	–1	V
I_{ODH}	Output HIGH Current	V_{CC} = 2.5V, V_{IN} = V_{IH} or V_{IL}, V_O = 1.25V[3]		-25	-45	-100	mA
I_{ODL}	Output LOW Current	V_{CC} = 2.5V, V_{IN} = V_{IH} or V_{IL}, V_O = 1.25V[3]		20	55	120	mA
V_{OH}	Output HIGH Voltage	V_{CC} = Min.	I_{OH} = –1mA	V_{CC}–0.2	—	—	V
		V_{IN} = V_{IH} or V_{IL}	I_{OH} = –8mA	1.8[5]	—	—	
V_{OL}	Output LOW Voltage	V_{CC} = Min.	I_{OL} = 1mA	—	—	0.4	V
		V_{IN} = V_{IH} or V_{IL}	I_{OL} = 8mA	—	—	0.6	
I_{OS}	Short Circuit Current[4]	V_{CC} = Max., V_O = GND[3]		-25	-60	-135	mA

NOTES:
1. For conditions shown as Max. or Min., use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at Vcc = 2.5V, +25°C ambient.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. This parameter is guaranteed but not tested.
5. V_{OH} = Vcc - 0.6V at rated current.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE(1,2)

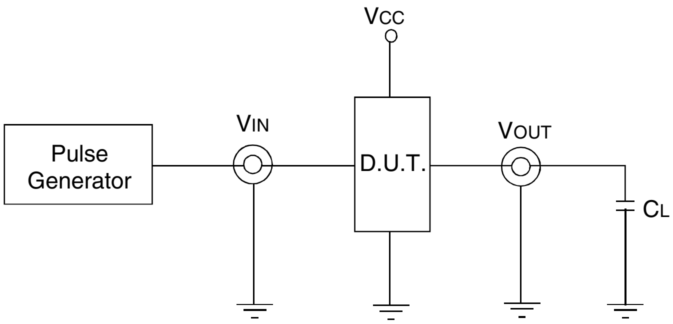
Following Conditions Apply Unless Otherwise Specified
Industrial: T_A = -40°C to +85°C, V_{CC} = 2.5V ± 0.2V

Symbol	Parameter	Conditions(3)	Min.	Typ.	Max.	Unit
t_{PLH} t_{PHL}	Propagation Delay	C_L = 22pF 100 MHz	—	3	3.5	ns
t_R	Output Rise Time		—	1	1.25	ns
t_F	Output Fall Time		—	1	1.25	ns
$t_{SK(O)}$	Same Device Output Pin-to-Pin Skew(4)		—	100	150	ps
$t_{SK(P)}$	Pulse Skew(5)		—	250	300	ps
$t_{SK(PP)}$	Part-to-Part Skew(6)		—	400	600	ps

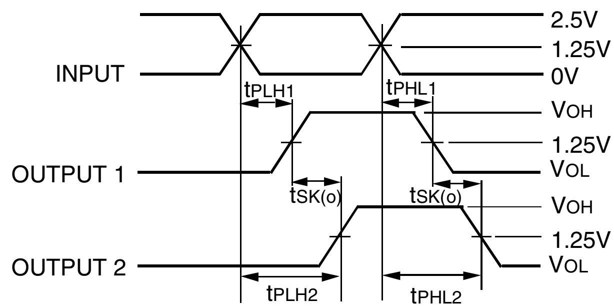
Symbol	Parameter	Conditions(3,7)	Min.	Typ.	Max.	Unit
t_{PLH} t_{PHL}	Propagation Delay	C_L = 12pF 150 MHz	—	2.4	2.7	ns
t_R	Output Rise Time		—	1	1.2	ns
t_F	Output Fall Time		—	1	1.2	ns
$t_{SK(O)}$	Same Device Output Pin-to-Pin Skew(4)		—	100	150	ps
$t_{SK(P)}$	Pulse Skew(5)		—	250	300	ps
$t_{SK(PP)}$	Part-to-Part Skew(6)		—	400	600	ps

NOTES:
1. t_{PLH} and t_{PHL} are production tested. All other parameters guaranteed but not production tested.
2. Propagation delay range indicated by Min. and Max. limit is due to V_{CC}, operating temperature and process parameters. These propagation delay limits do not imply skew.
3. See test circuits and waveforms.
4. Skew measured between all outputs under identical transitions and load conditions.
5. Skew measured is difference between propagation delay times t_{PHL} and t_{PLH} of same output under identical load conditions.
6. Part to part skew for all outputs given identical transitions and load conditions at identical V_{CC} levels and temperature.
7. Airflow of 1m/s is recommended for frequencies above 133MHz.

TEST CIRCUITS AND WAVEFORMS

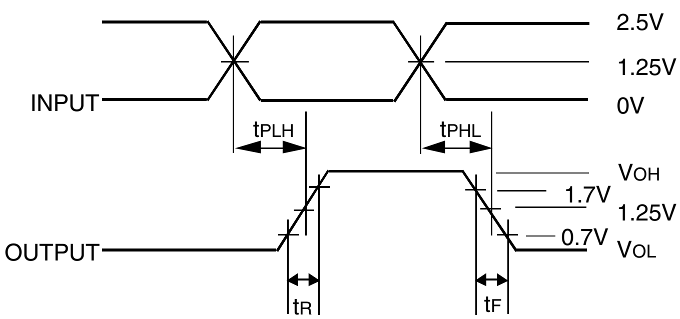


CL = Load Capacitance: Includes Jig and Probe Capacitance

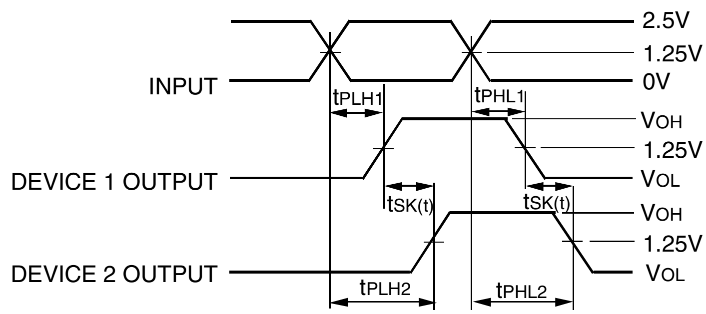


tSK(o) = |tPLH2 - tPLH1| or |tPHL2 - tPHL1|

Output Skew - tSK(O)



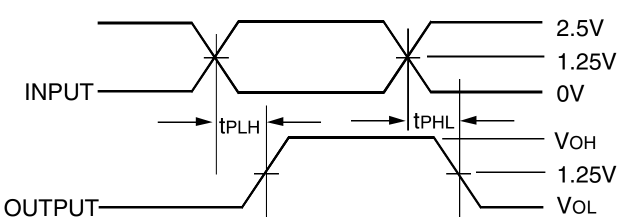
Propagation Delay



tSK(t) = |tPLH2 - tPLH1| or |tPHL2 - tPHL1|

Part-to-Part Skew - tSK(PP)

NOTE: Device 1 and device 2 are same package type and speed grade.



tSK(p) = |tPHL - tPLH|

Pulse Skew - tSK(P)

TEST CONDITIONS

Symbol	VCC = 2.5V ±0.2V	Unit
CL	22(1)	pF
	12(2)	
RT	ZOUT of pulse generator	Ω
tR / tF	1.25(1)	ns
	1.2(2)	

DEFINITIONS:
CL = Load capacitance: includes jig and probe capacitance.
RT = Termination resistance: should be equal to ZOUT of the Pulse Generator.
tR / tF = Rise/Fall time of the input stimulus from the Pulse Generator.

NOTES:
1. Test conditions at 100MHz.
2. Test conditions at 150MHz.

ORDERING INFORMATION

74FCT XXXX X X

Device Type / Package / Temp. Range

I	Industrial (-40°C to +85°C)
PYG	Shrink Small Outline IC
QG	QSOP - Green
20807	1-to-10 2.5V Clock Driver

IMPORTANT NOTICE AND DISCLAIMER

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit www.renesas.com/contact-us/.